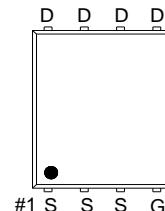
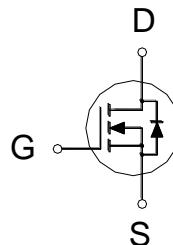


NIKO-SEM
**N-Channel Enhancement Mode
Field Effect Transistor**
**PK664BA
PDFN 5x6P
Halogen-Free & Lead-Free**
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	2.4mΩ	114A

**ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ³	I_D	114	A
$T_C = 100^\circ\text{C}$		72	
Pulsed Drain Current ¹	I_{DM}	250	
Continuous Drain Current (Steady-State)	I_D	23	
$T_A = 70^\circ\text{C}$		18	
Continuous Drain Current ($t \leq 10\text{s}$)	I_D	32	
$T_A = 70^\circ\text{C}$		26	
Avalanche Current	I_{AS}	48	
Avalanche Energy	E_{AS}	115	mJ
Power Dissipation	P_D	59	W
$T_C = 100^\circ\text{C}$		23	
Power Dissipation (Steady-State)	P_D	2.4	W
$T_A = 70^\circ\text{C}$		1.5	
Power Dissipation ($t \leq 10\text{s}$)	P_D	4.8	
$T_A = 70^\circ\text{C}$		3	
Operating Junction & Storage Temperature Range	T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$t \leq 10\text{s}$	$R_{\theta JA}$	$^{\circ}\text{C} / \text{W}$	26	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient ²	Steady-State	$R_{\theta JA}$		51	
Junction-to-Case	Steady-State	$R_{\theta JC}$		2.1	

¹Pulse width limited by maximum junction temperature.²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.³Package limitation current is 51A.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.3	1.75	2.3	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
		$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 20\text{A}$		2.2	3	$\text{m}\Omega$
		$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		1.7	2.4	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 20\text{A}$		62		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1\text{MHz}$		2809		pF
Output Capacitance	C_{oss}			490		
Reverse Transfer Capacitance	C_{rss}			324		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		1		Ω
Total Gate Charge ²	Q_g	$V_{\text{GS}} = 10\text{V}$		57.3		nC
				29.3		
Gate-Source Charge ²	Q_{gs}	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		9		nC
Gate-Drain Charge ²	Q_{gd}			14		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$			26		
Rise Time ²	t_r			15		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$	$V_{\text{DS}} = 15\text{V}, I_D \geq 20\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		56		nS
Fall Time ²	t_f			23		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current ³	I_s				59	A
Forward Voltage ¹	V_{SD}	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$			1	V
Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI_F/dt = 100\text{A} / \mu\text{s}$		40		nS
Reverse Recovery Charge	Q_{rr}			40		nC

¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

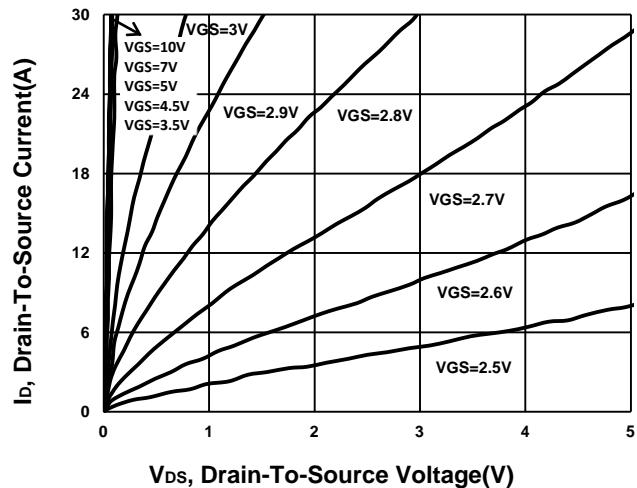
³Package limitation current is 51A.

NIKO-SEM

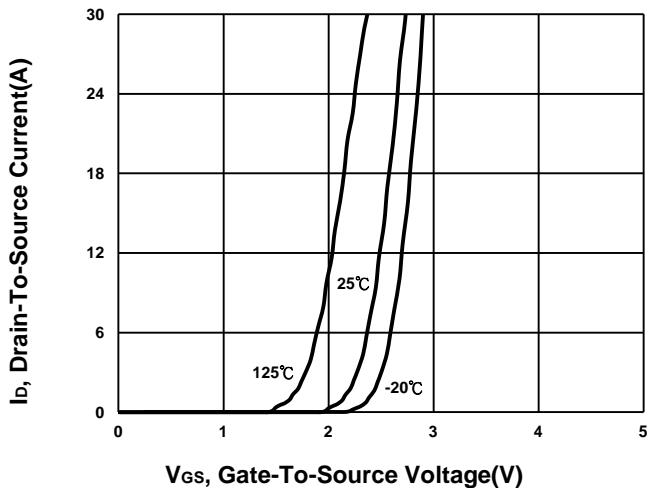
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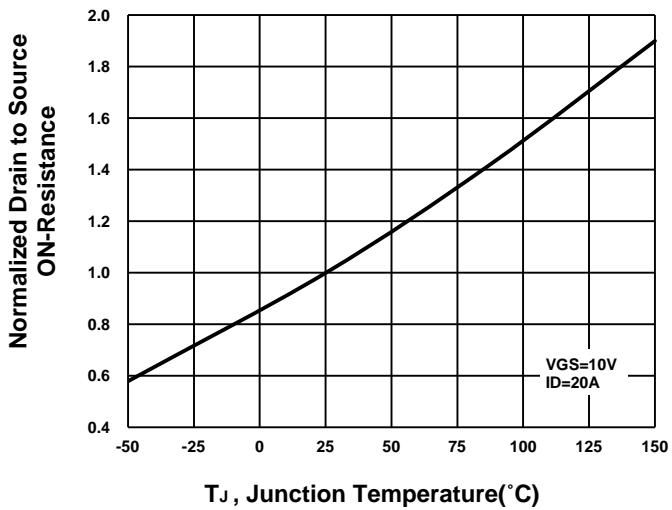
Output Characteristics



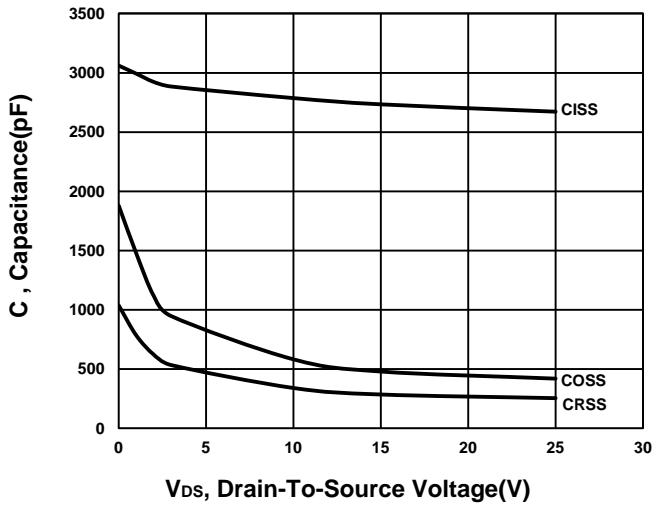
Transfer Characteristics



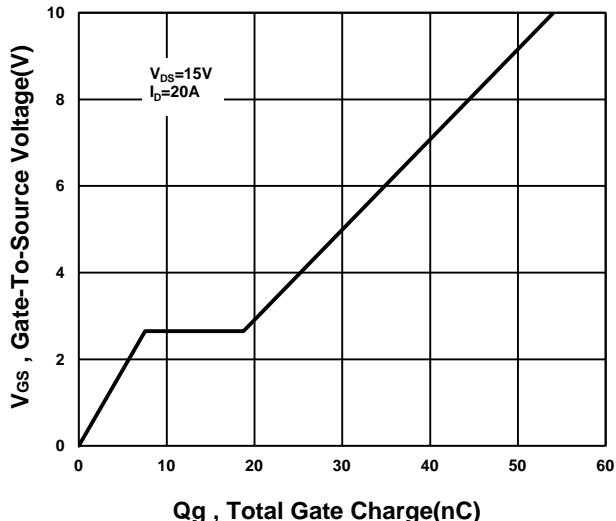
On-Resistance VS Temperature



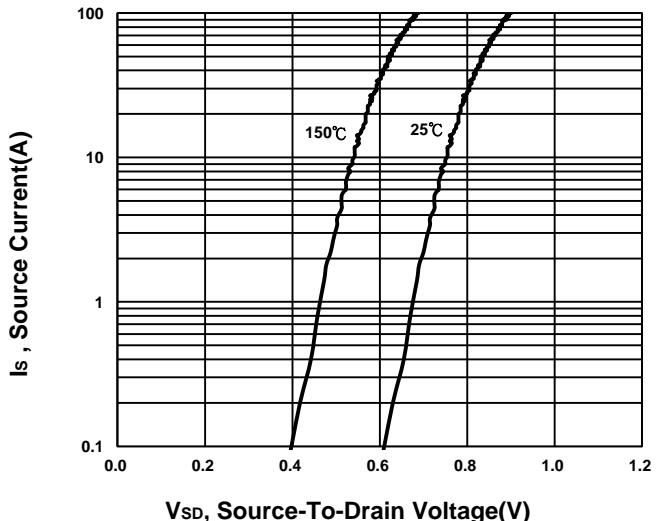
Capacitance Characteristic



Gate charge Characteristics



Source-Drain Diode Forward Voltage

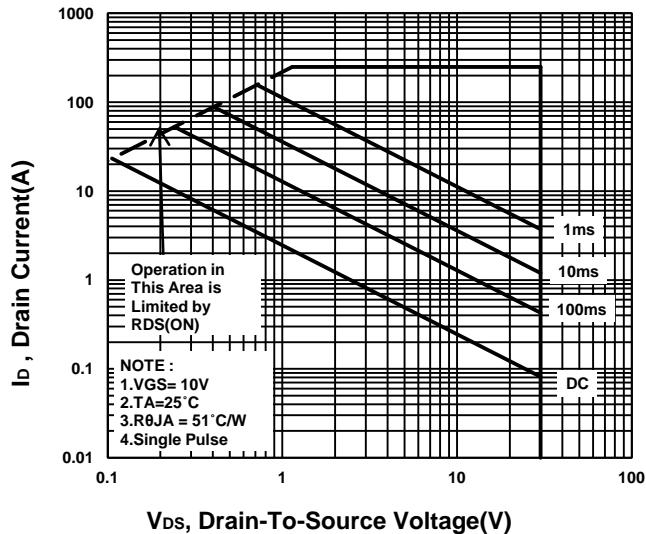


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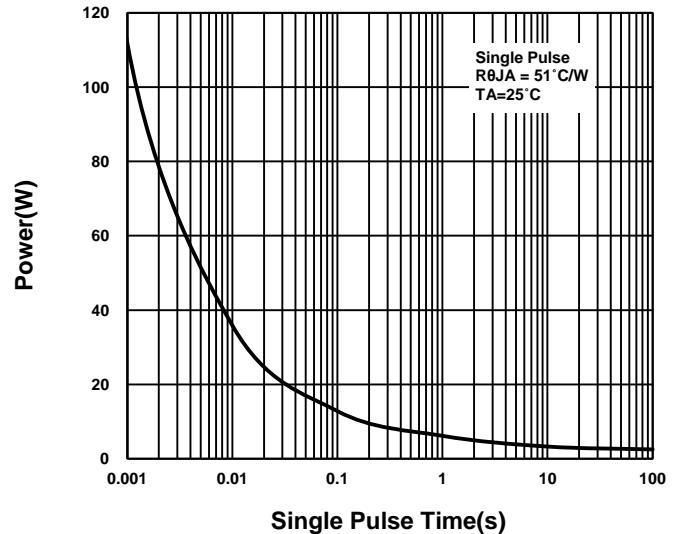
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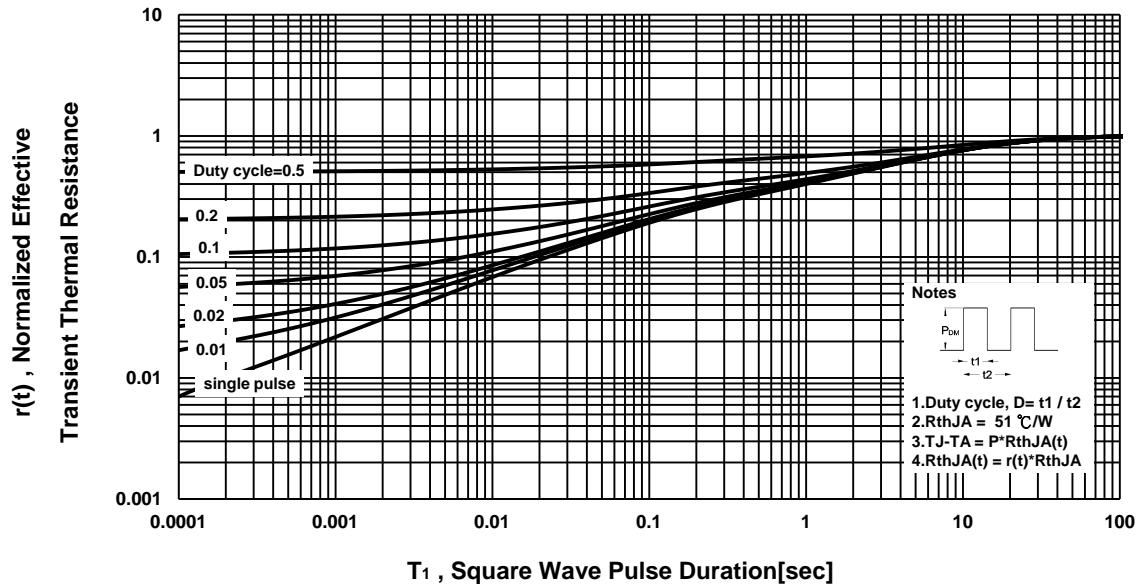
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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