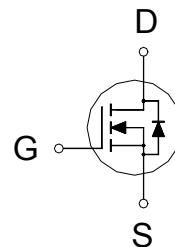


**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****PKC26BB  
PDFN 5x6P  
Halogen-Free & Lead-Free****PRODUCT SUMMARY**

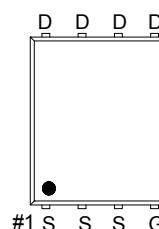
$V_{(BR)DSS}$	$R_{DS(on)}$	$I_D^4$
30V	1.6mΩ	151A

**Features**

- Pb-Free, Halogen Free and RoHS compliant.
- Low  $R_{DS(on)}$  to Minimize Conduction Losses.
- Ohmic Region Good  $R_{DS(on)}$  Ratio.
- Optimized Gate Charge to Minimize Switching Losses.

**Applications**

- Protection Circuits Applications.
- Computer for DC to DC Converters Applications.



G. GATE  
D. DRAIN  
S. SOURCE  
100% UIS Tested  
100% Rg Tested

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>4</sup>	$T_C = 25^\circ\text{C}$	$I_D$	151	A
	$T_C = 100^\circ\text{C}$		95	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	200	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	36	
	$T_A = 70^\circ\text{C}$		28	
Avalanche Current		$I_{AS}$	57	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	162	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	73	W
	$T_C = 100^\circ\text{C}$		29	
Power Dissipation <sup>3</sup>	$T_A = 25^\circ\text{C}$	$P_D$	4.1	
	$T_A = 70^\circ\text{C}$		2.6	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150	°C

**NIKO-SEM****N-Channel Enhancement Mode  
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THERMAL RESISTANCE		SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	$t \leq 10s$	$R_{\theta JA}$		30	°C / W
Junction-to-Ambient <sup>2</sup>	Steady-State	$R_{\theta JA}$		50	
Junction-to-Case	Steady-State	$R_{\theta JC}$		1.7	

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ C$ .<sup>3</sup>The Power dissipation is based on  $R_{\theta JA} t \leq 10s$  value.<sup>4</sup>The maximum current rating is package limited.**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ C$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	1.6	2.35	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24V, V_{GS} = 0V$			1	$\mu A$
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^\circ C$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 20A$		1.6	2.3	$m\Omega$
		$V_{GS} = 10V, I_D = 20A$		1.1	1.6	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 5V, I_D = 20A$		123		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		3499		pF
Output Capacitance	$C_{oss}$			625		
Reverse Transfer Capacitance	$C_{rss}$			408		
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		1.1		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 15V, V_{GS} = 10V, I_D = 20A$		70		nC
				36		
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			9.2		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			17		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 15V, I_D \geq 20A, V_{GS} = 10V, R_{GEN} = 6\Omega$		20		nS
Rise Time <sup>2</sup>	$t_r$			120		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			97		
Fall Time <sup>2</sup>	$t_f$			150		

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PDFN 5x6P

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**SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)**

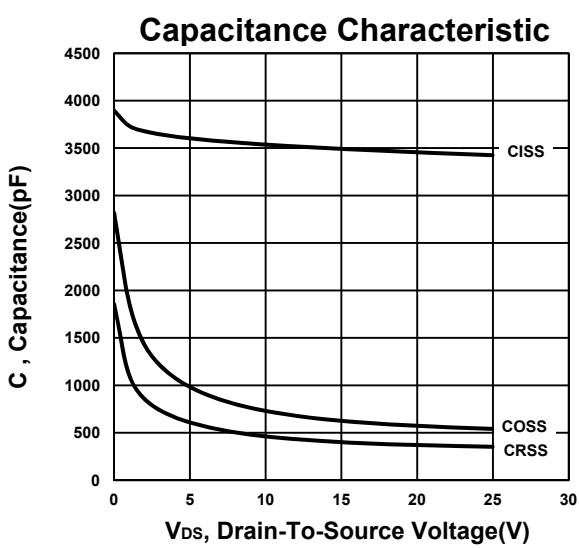
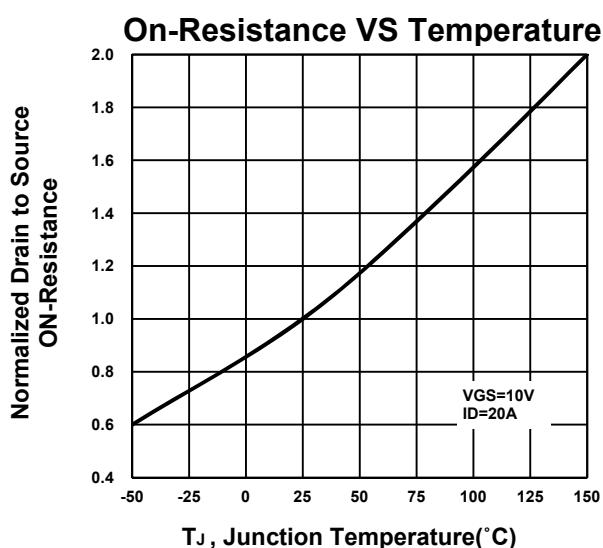
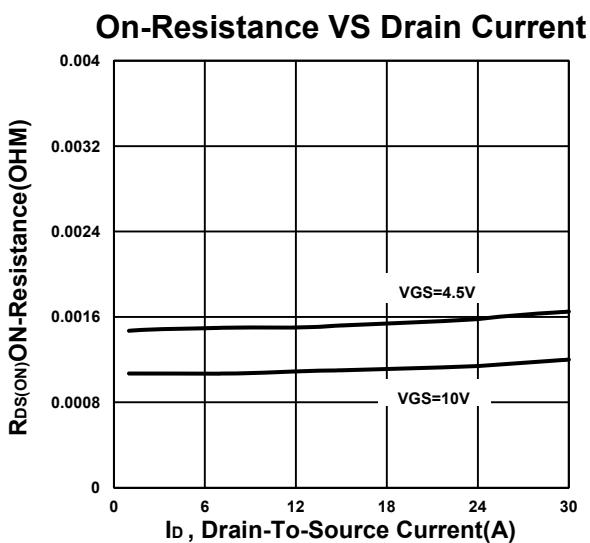
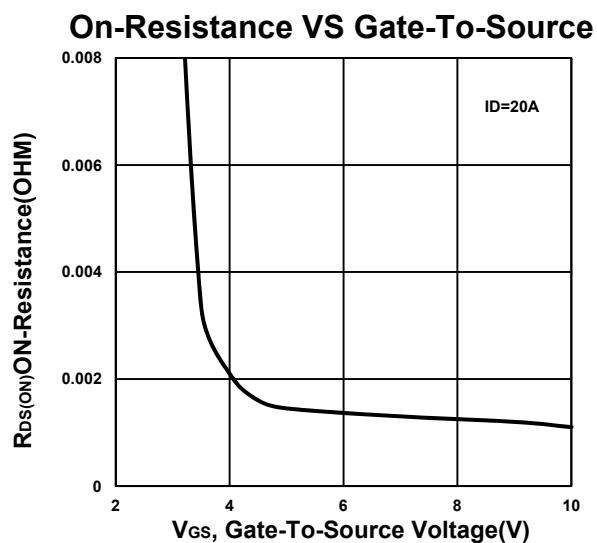
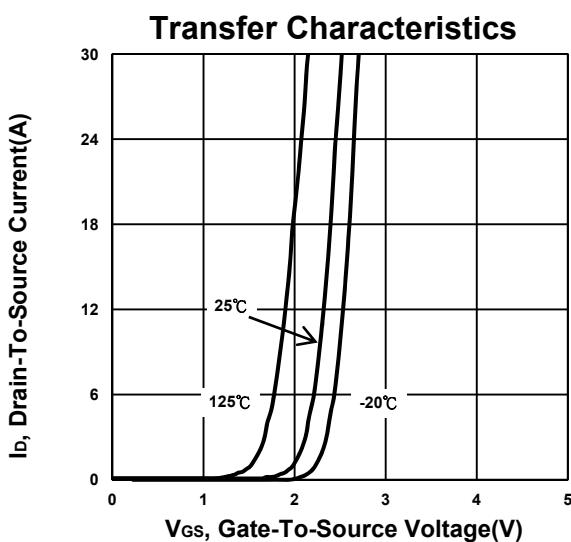
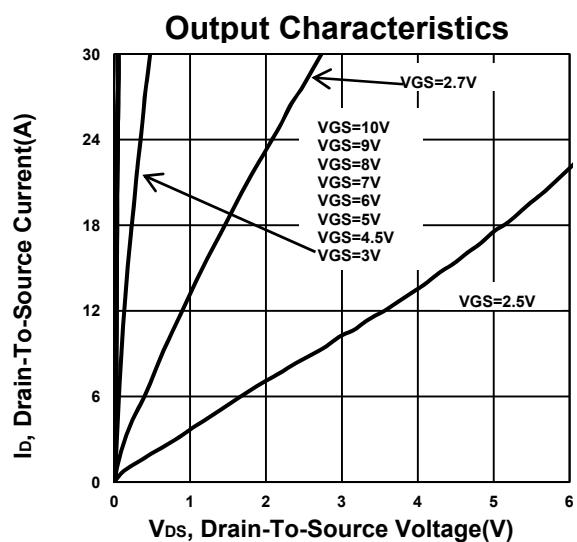
Continuous Current <sup>3</sup>	I <sub>S</sub>				73	A
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = 20A, V <sub>GS</sub> = 0V			1	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A, dI <sub>F</sub> /dt = 100A / μS	28			nS
Reverse Recovery Charge	Q <sub>rr</sub>		13			nC

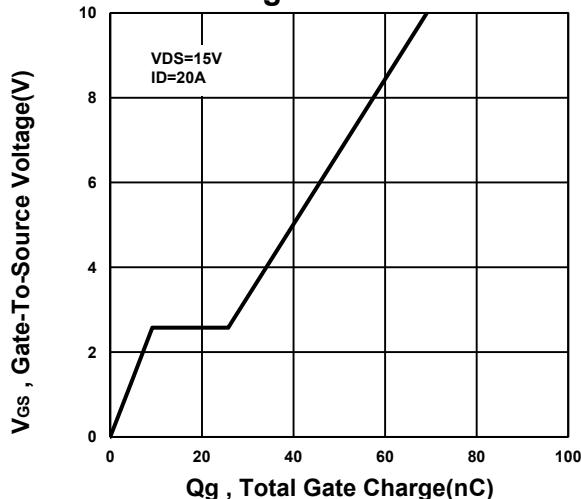
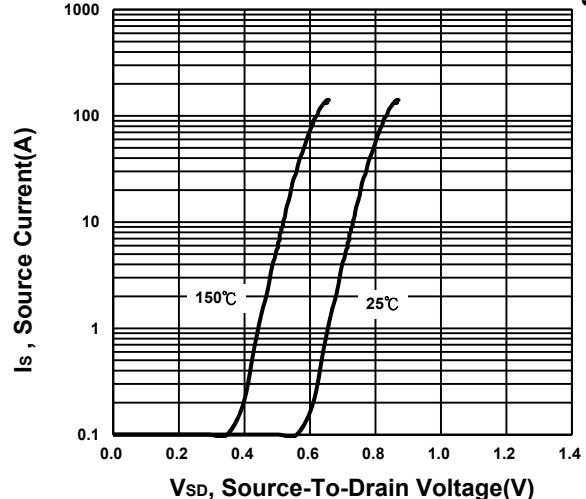
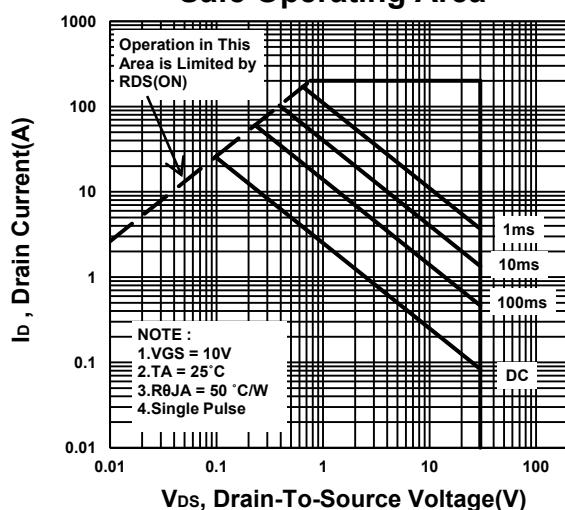
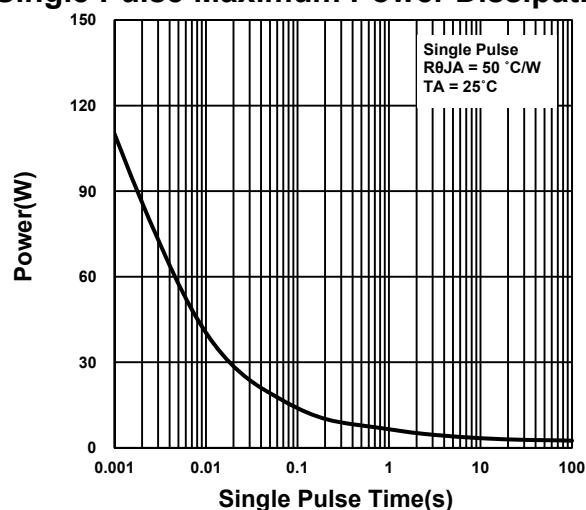
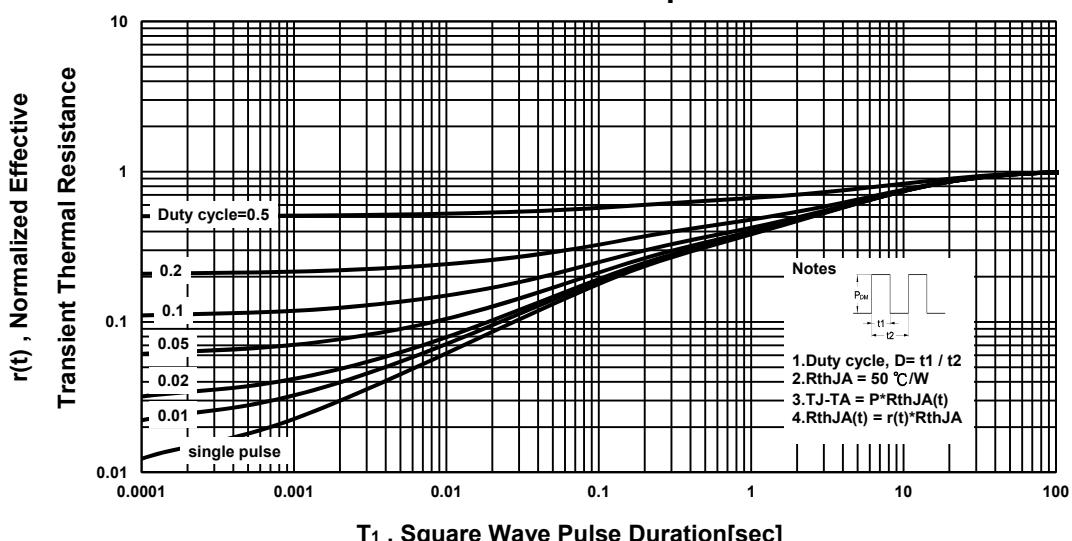
<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.<sup>2</sup>Independent of operating temperature.<sup>3</sup>The maximum current rating is package limited.

**NIKO-SEM**

**N-Channel Enhancement Mode  
Field Effect Transistor**

**PKC26BB**  
**PDFN 5x6P**  
**Halogen-Free & Lead-Free**



**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****PKC26BB  
PDFN 5x6P  
Halogen-Free & Lead-Free****Gate charge Characteristics****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**

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