Designated client product

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New Japan Radio Co.,Ltd.

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■ GENERAL DESCRIPTION

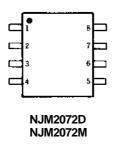
The NJM2072 is a monolithic integrated circuit designed for signal level sensor system. The NJM2072 features low power, low voltage operation, and high input sensitivity and is suited for the signal level sensor system for micro cassette, vox for telecommunications.

■ FEATURES

JRC

- Operating Voltage
- Low Operating Current 0.55mA typ. -36dBV typ.
- High Input Sensitivity
- Package Outline
- Bipolar Technology

■ PIN CONFIGURATION



+0.9 to +7V

DIP8, DMP8

■ PACKAGE OUTLINE





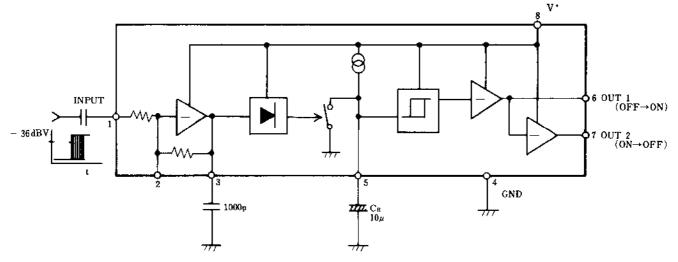
NJM2072D

NJM2072M

PIN FUNCTION

- INPUT 1. **Gain Control** 2.
- 3. Amp.Output
- 4. GND
- 5. Capacitor for Recovery time
- 6. OUT1
- 7. OUT2
- 8. V⁺

BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

			(Ta=25°C)
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	8	V
Power Dissipation	PD	(DIP8) 500 (DMP8) 300	mW
Operating Temperature Range	T _{opr}	-40~+85	°C
Storage Temperature Range	T _{stg}	-40~+125	С
Maximum Input Voltage	V _{imax}	V ⁺ -1	V

■ ELECTRICAL CHARACTERISTICS

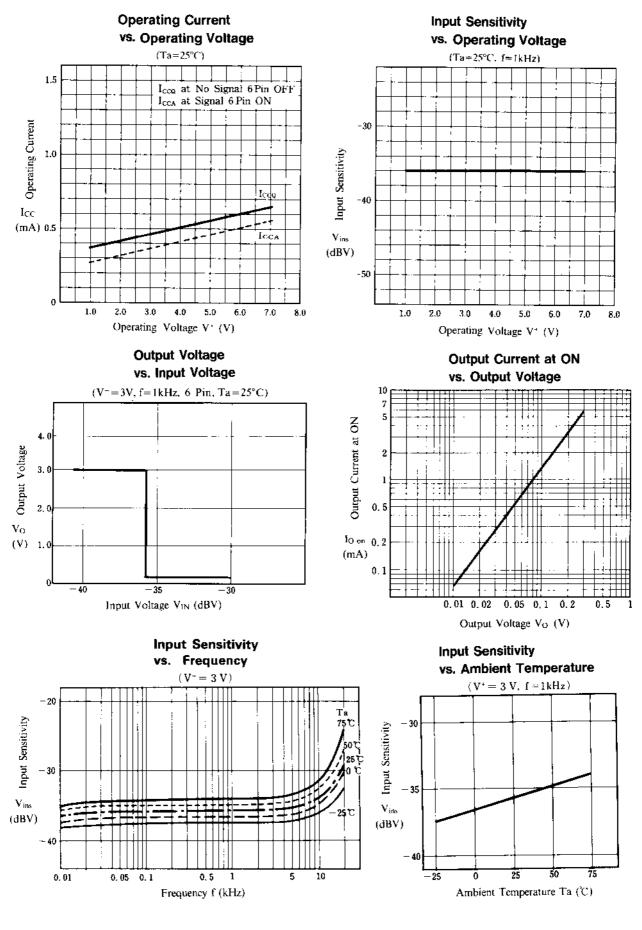
					(14-25 (J, V — J V J
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V+		0.9	-	7	V
Operating Current	Icc	V _{in} =0mVrms,R _L =∞	0.2	0.55	1.5	mA
Input Sensitivity	Vins	f=1kHz	-39	-36	-33	dBV
Attack Time (note1)	T _{atc}	f=1kHz,C _R =10µF	-	1	25	mSec
Recovery Time (note2)	T _{rec}	f=1kHz,C _R =10µF	-	2	-	Sec
Output Current at ON (OUT1)	I _{01 on}	V _{in} =30mVrms,V _O =0.3V	1	3	-	mA
Output Current at ON (OUT2)	1 _{02 on}	V _{in} =0mVrms,V _O =0.3V	1	3	-	mA
Output Current at OFF (OUT1)	I _{01 off}	V _{in} =0mVrms,V _O =8V	-	-	1	μA
Output Current at OFF (OUT2)	I _{02 off}	V _{in} =30mVrms,V _O =8V	-	-	1	μA
Input Resistance	Rin		16	20	24	kΩ
Charge Current	I _{chg}		1.0	2.0	3.0	μA

(note1) Attack Time: Period from putting input signal of more than minimum input sensitive signal to output level change.

(note2) Recovery Time: Period from input signal becoming lower than minimum input sensitive signal to output level change.

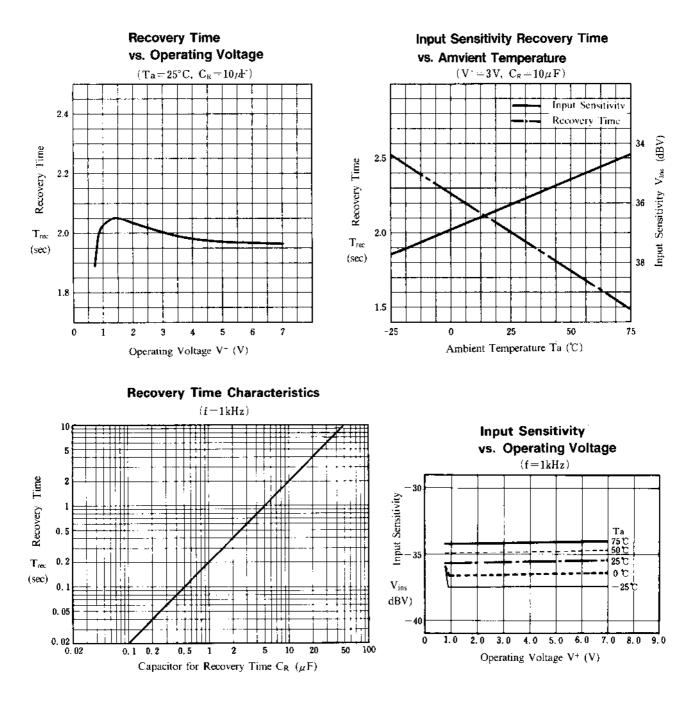
(Ta=25°C.V⁺=3V)

■ TYPICAL CHARACTERISTICS

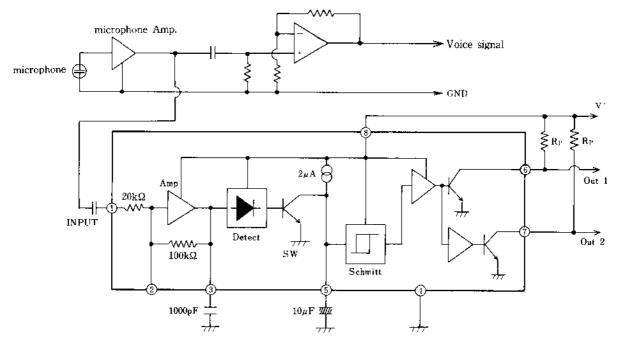


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■ TYPICAL CHARACTERISTICS



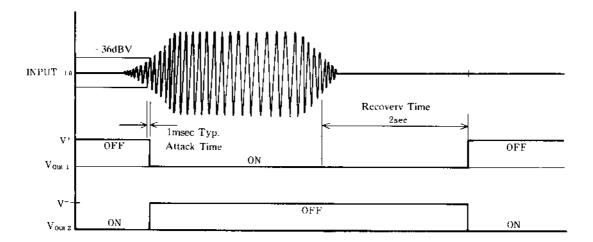
■ TYPICAL APPLICATIONS



Pins 6 and 7 show an open collector. Mount resistor $R_{\rm P}$ shown by the following equation.

R_P=(V⁺_{MIN}-0.2)/0.3 (kΩ)

Resistor R_P to pin 7 is omissible, if pin 6 only is used. But resister R_P to pin 6 should be put when Out2 only is used. V_{MIN}^+ is minimum supply voltage.



[CAU	TION]
T1	

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