■ GENERAL DESCRIPTION

The **NJM2930** 3-terminal positive voltage regulator features an ability to source 150mA of output current (100mA: L-Type) with an input-output differential of 0.6V or less. Efficient use of low input voltages obtained, for example, from an automotive battery during cold crank conditions, allows 5V circuitry to be properly powered with supply voltages as low as 5.6V.

Familiar regulator features such as current limit and thermal overload protection are also provided.

TO-220F

■ FEATURES

JRC

- Operating Voltage
- Input-Output differential less 0.6V
- Output Current in Excess of 150mA
- 40V Load Dump Protection
- Internal Short Circuit Current Limit
- Internal Thermal Overload Protection
- Package Outline
- Bipolar Technology

■ EQUIVALENT CIRCUIT







ABSOLUTE MAXIMUM RATINGS $(T_a = 25^{\circ}C_{abs})$				
PARAMETER	SYMBOL	RATINGS	UNIT	
Operating Input Voltage Range	V _{IN}	26	V	
Input Overvoltage Protection	V _{PR}	40	V	
Input Reverse Voltage	V _{INR1} (100ms)	-12	V	
Input Reverse Voltage	V _{INR2} (DC)	-6	V	
Maximum Output Current	I _{OM}	(TO-220F) 150	mA	
Power Dissipation	PD	(TO-220F) 7.5 (Note)	W	
Operating Temperature Range	T _{opr}	-30 to 75	°C	
Storage Temperature Range	T _{stg}	-40 to 125	°C	

(note) Case Temperature : $T_{case} \le 75^{\circ}$ C, Thermal Resistance : $\theta_{ic} = 5^{\circ}$ C/W TYP.

■ ELECTRICAL CHARACTERISTICS

(All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques.) **NJM2930F05** (V_{IN} = 14V, C_2 = 10µF, T_i = 25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	Vo	6V ≤ V _{IN} ≤ 26V, 5mA ≤ I _O ≤ 100mA	4.5	5	5.5	V
Line Regulation	ΔV_{O} - V_{I}	9V ≤ V _{IN} ≤ 16V, I _O =5mA	-	7	25	mV
	$\Delta V_{O} - V_{I}$	6V ≤ V _{IN} ≤ 26V, I _O =5mA	-	30	80	mV
Load Regulation	$\Delta V_{\rm O}$ - $I_{\rm O}$	5mA≤I ₀ ≤150mA	-	14	50	mV
Quiescent Current	I _{Q1}	I _o =10mA	-	4	7	mA
	I _{Q2}	I ₀ =150mA	-	30	40	mA
Dropout Voltage	ΔV _I - _O	I ₀ =150mA	-	0.3	0.6	V
Output Noise Voltage	V _{NO}	10Hz to 100kHz, I ₀ = 150mA	-	100	-	μV
Ripple Rejection	RR	f=120Hz, I ₀ = 150mA	-	60	-	dB

NJM2930F08 (V_{IN} = 14V, C₂ = 10µF, T_i = 25°C)

	:)	· ·				
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	Vo	9.4V ≤ V _{IN} ≤ 26V, 5mA ≤ I _O ≤ 150mA	7.2	8	8.8	V
Line Regulation	ΔV_{O} - V_{I}	9.4V ≤ V _{IN} ≤ 16V, I _O =5mA	-	12	50	mV
	ΔV_{O} - V_{I}	9.4V ≤ V _{IN} ≤ 26V, I _O =5mA	-	50	100	mV
Load Regulation	$\Delta V_{\rm O}$ - $I_{\rm O}$	5mA≤I ₀ ≤150mA	-	25	50	mV
Quiescent Current	I _{Q1}	I _O =10mA	-	4	7	mA
	I _{Q2}	I ₀ =150mA	-	30	40	mA
Dropout Voltage	ΔV _I - _O	I ₀ =150mA	-	0.3	0.6	V
Output Noise Voltage	V _{NO}	10Hz to 100kHz, I ₀ = 150mA	-	140	-	μV
Ripple Rejection	RR	f=120Hz, I ₀ = 150mA	-	57	-	dB

NJM2930F85 (V_{IN} = 14V, C₂ = 10µF, T_j = 25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	Vo	9.95V ≤ V _{IN} ≤ 26V, 5mA ≤ I _O ≤ 150mA	7.65	8.5	9.35	V
Line Regulation	$\Delta V_{O} - V_{I}$	9.95V ≤ V _{IN} ≤ 16V, I _O =5mA	-	12	50	mV
	ΔV_{O} - V_{I}	9.95V ≤ V _{IN} ≤ 26V, I _O =5mA	-	50	100	mV
Load Regulation	ΔV _o - I _o	5mA≤I₀≤150mA	-	25	50	mV
Quiescent Current	I _{Q1}	I _O =10mA	-	4	7	mA
	I _{Q2}	I ₀ =150mA	-	30	40	mA
Dropout Voltage	ΔV _I - _O	I ₀ =150mA	-	0.3	0.6	V
Output Noise Voltage	V _{NO}	10Hz to 100kHz, I ₀ = 150mA	-	150	-	μV
Ripple Rejection	RR	f=120Hz, I ₀ = 150mA	-	56	-	dB

STANDARD APPLICATION EXAMPLES

- * This NJM2930 is required when the mounting position is separated from the power filter.
- ** Use an aluminum electrolytic capacitor or a tantalum capacitor as C₂. The temperature guarantee range of capacitors should be down to -30°C. A capacity value of 10µF is a minimum requirement for improving the stability and transient response. Mount it at a position as close to the leads as possible.
- *** When application on automobile car operation, the minus pulse might be input on IC. In this case, however, the pulse might trigger to latch up. If it were that, this kind of latching up might be continued, the IC would burn up into defective in many cases. It is advisable to apply D1, D2 as described in the drawing, in order to prevent from making any troubles. It is important to make devices D1, D2 against V_{IN} to be able to stand for brake down voltage, current volume, and then less volume for Vf.



■ POWER DISSIPATION VS. AMBIENT TEMPERATURE



NJM2930 (TO-220F)

■ TYPICAL CHARACTERISTICS



NJM2939F05 Thermal Shutdown



NJM2930F05 Load Characteristics





NJM2930F05 Output Current vs. Quiescent Current



Output Voltage

Vo (V)

■ TYPICAL CHARACTERISTICS



NJM2930F05 Output Voltage $(V_{1N} = 14V, I_{O} = 1mA)$ 5.20 Output Voltage 5.00 4.80 V_{out} (V) 4.60 4.40 - 30 0 25 50 75 Ambient Temperature Ta (°C)

NJM2930F05 Dropout Voltage



NJM2930F05 Dropout Voltage vs. Output Current



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