# RICOH

# R1283x SERIES

### 2ch DC/DC for CCD & OLED

NO.EA-157-160224

### **OUTLINE**

The R1283x 2ch DC/DC converter is designed for CCD & OLED Display power source. It contains a step up DC/DC converter and an inverting DC/DC converter to generate two required voltages by CCD & OLED Display. Step up DC/DC converter generates boosted output voltage up to 20V. Inverting DC/DC converter generates negative voltage up to V<sub>IN</sub> voltage minus 20V independently. Start up sequence is internally made. Each of the R1283x series consists of an oscillator, a PWM control circuit, a voltage reference, error amplifiers, over current protection circuits, short protection circuits, an under voltage lockout circuit (UVLO), an Nch driver for boost operation, a Pch driver for inverting. A high efficiency boost and inverting DC/DC converter can be composed with external inductors, diodes, capacitors, and resistors.

# **FEATURES**

- Step Up DC/DC (CH1)

Internal Nch MOSFET Driver (RoN=400mΩTyp.) Adjustable VouT Up to 20V with external resistor Internal Soft start function (Typ. 4.5ms) Over Current Protection Maximum Duty Cycle: 91%(Typ.)

• Inverting DC/DC (CH2)

Internal Pch MOSFET Driver ( $R_{ON}$ =400 $m\Omega$  Typ.) Adjustable  $V_{OUT}$  Up to Vdd-20V with external resistor Auto Discharge function for negative output Internal Soft start function (Typ. 4.5ms) Over Current Protection Maximum Duty Cycle: 91%(Typ.)

 Short Protection with timer latch function (Typ. 50ms); Short condition for either or both two outputs makes all output drivers off and latches./ If the maximum duty cycle continues for a certain time, these output drivers will be turned off.

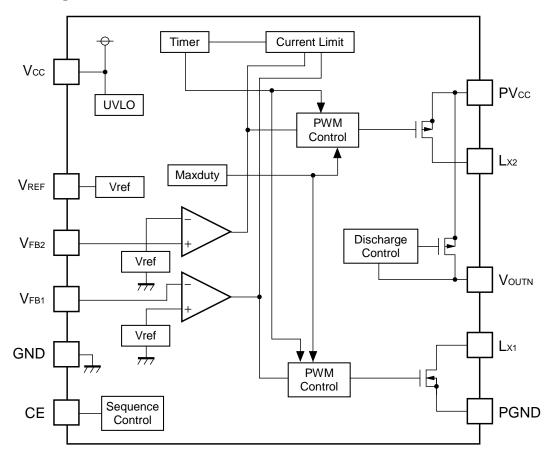
CE with start up sequence function
CH1→CH2 (R1283K001x) / CH2→CH1(R1283K002x) Selectable
UVLO function
Operating Frequency Selection ........300kHz / 700kHz / 1400kHz

Packages ...... DFN(PLP)2730-12, WLCSP-11-P2

### APPLICATION

- Fixed voltage power supply for portable equipment
- Fixed voltage power supply for CCD, OLED, LCD

# **BLOCK DIAGRAM**



# **SELECTION GUIDE**

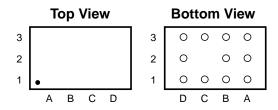
The start-up sequence, oscillator frequency, and the package for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1283Z00x*-E2-F	WLCSP-11-P2	4,000 pcs	Yes	Yes
R1283K00x*-TR	DFN(PLP)2730-12	5,000 pcs	Yes	Yes

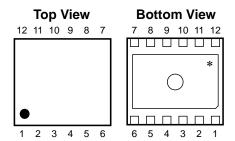
- x : The start-up sequence can be designated.
  - (1) Step-up → Inverting
  - (2) Inverting → Step-up
- \*: The oscillator frequency is the option as follows.
  - (A) 300kHz (A Version for 1283Z packaged in WLCSP-11-P2 is not available)
  - (B) 700kHz
  - (C) 1400kHz

# **PIN CONFIGURATIONS**

### • WLCSP-11-P2



# • DFN(PLP)2730-12



# **PIN DESCRIPTIONS**

### • WLCSP-11-P2

Pin No	Symbol	Pin Description
A1	PGND	Power GND pin
A2	V <sub>FB1</sub>	Feedback pin for Step up DC/DC
A3	L <sub>X1</sub>	Switching pin for Step up DC/DC
B1	PVcc	Power Input pin
B2	CE	Chip Enable pin for the R1283
В3	L <sub>X2</sub>	Switching pin for Inverting DC/DC
C1	GND	Analog GND pin
C3	Voutn	Discharge pin for Negative output
D1	Vcc	Analog power source Input pin
D2	V <sub>REF</sub>	Reference Voltage Output pin
D3	V <sub>FB2</sub>	Feedback pin for Inverting DC/DC

### • DFN(PLP)2730-12

Pin No	Symbol	Pin Description
1	NC	No Connect
2	L <sub>X1</sub>	Switching pin for Step up DC/DC
3	Lx2	Switching pin for Inverting DC/DC
4	Voutn	Discharge pin for Negative Output
5	CE	Chip Enable pin for the R1283
6	V <sub>FB2</sub>	Feedback pin for Inverting DC/DC
7	VREF	Reference Voltage Output pin
8	Vcc	Analog power source Input pin
9	V <sub>FB1</sub>	Feedback pin for Step up DC/DC
10	GND	Analog GND pin
11	PVcc	Power Input pin
12	PGND	Power GND pin

<sup>\*)</sup> Tab is GND level. (They are connected to the reverse side of this IC.)

The tab is better to be connected to the GND, but leaving it open is also acceptable.

### **ABSOLUTE MAXIMUM RATINGS**

(GND/PGND=0V)

Symbol	Item	Rating	Unit
Vcc	Vcc / PVcc pin Voltage	6.5	V
VDTC	V <sub>FB1</sub> pin Voltage	-0.3 to Vcc+0.3	V
V <sub>FB</sub>	V <sub>FB2</sub> pin Voltage	-0.7(*1) to Vcc+0.3	V
Vce	CE pin Voltage	-0.3 to Vcc+0.3	V
VREF	V <sub>REF</sub> pin Voltage	-0.7(*1) to Vcc+0.3	V
V <sub>LX1</sub>	Lx1 pin Voltage	-0.3 to 24	V
I <sub>LX1</sub>	Lx1 pin Current	Internally Limited	Α
V <sub>LX2</sub>	Lx2 pin Voltage	Vcc-24 to Vcc+0.3	V
I <sub>LX2</sub>	Lx2 pin Current	Internally Limited	Α
V <sub>NFB</sub>	Voutn pin Voltage	Vcc-24 to Vcc+0.3	V
PD	Power Dissipation (WLCSP-11-P2) (*2)	1000	m\//
PD	Power Dissipation (DFN(PLP)2730-12) (*2)	1000	mW
Topt	Operating Temperature Range	-40 to 85	°C
Tstg	Storage Temperature Range	-55 to 125	°C

<sup>\*1)</sup> In case the voltage range is from -0.7V to -0.3V, permissible current is 10mA or less.

### **ABSOLUTE MAXIMUM RATINGS**

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.

The functional operation at or over these absolute maximum ratings is not assured.

### RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.



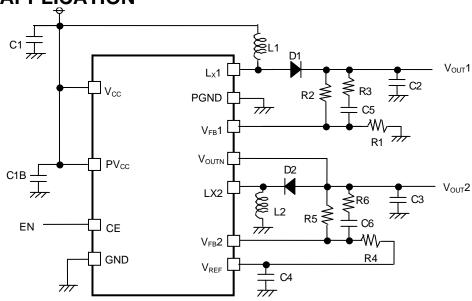
<sup>\*2)</sup> For Power Dissipation, please refer to PACKAGE INFORMATION.

# **ELECTRICAL CHARACTERISTICS**

● **R1283x** Topt=25°C

Symbol	Item	Conditions	Min.	Тур.	Max.	Unit.
Vcc	Operating Input Voltage		2.5	,,,	5.5	V
		Vcc=5.5V, Freq=300kHz	_	2.0		mA
Icc1	Vcc Consumption Current	Vcc=5.5V, Freq=700kHz		4.0		mA
	(Switching)	Vcc=5.5V, FREQ=1400kHz		8.0		mA
		Vcc=5.5V, FREQ=300kHz		250		μА
Icc2	Vcc Consumption Current	Vcc=5.5V, Freq=700kHz		300		μA
	(At no switching)	Vcc=5.5V, FREQ=1400kHz		350		μA
Istandby	Standby Current	Vcc=5.5V		0.1	3	μA
V <sub>UVLO1</sub>	UVLO Detect Voltage	Falling	2.05	2.15	2.25	V
V <sub>UVLO2</sub>	UVLO Released Voltage	Rising		Vuvlo1 +0.16	2.48	V
VREF	VREF Voltage Tolerance	Vcc=3.3V	1.172 +V <sub>FB2</sub>	1.2 +V <sub>FB2</sub>	1.228 +V <sub>FB2</sub>	V
$\Delta V$ REF/ $\Delta T$ opt	V <sub>REF</sub> Voltage Temperature Coefficient	Vcc=3.3V, -40°C≤Topt≤85°C		±150		ppm/ºC
$\Delta V_{REF}/\Delta V_{CC}$	V <sub>REF</sub> Line Regulation	2.5V≤Vcc≤5.5V		5		mV
$\Delta V_{REF} / \Delta I_{OUT}$	VREF Load Regulation	Vcc=3.3V, 0.1mA≤louт≤2mA		5		mV
LIMREF	VREF Short Current Limit	Vcc=3.3V, VREF=0V		15		mA
$V_{FB1}$	V <sub>FB1</sub> Voltage Tolerance	Vcc=3.3V	0.985	1.0	1.015	V
$\Delta V$ FB1 $/\Delta T$ opt	V <sub>FB1</sub> Voltage Temperature Coefficient	Vcc=3.3V, -40°C≤Topt≤85°C		±150		ppm/ºC
<b>I</b> FB1	V <sub>FB1</sub> Input Current	Vcc=5.5V, V <sub>FB1</sub> =0V or 5.5V	-0.1		0.1	μΑ
$V_{FB2}$	V <sub>FB2</sub> Voltage Tolerance	Vcc=3.3V	-25	0	25	mV
I <sub>FB2</sub>	V <sub>FB2</sub> Input Current	Vcc=5.5V, V <sub>FB2</sub> =0V or 5.5V	-0.1		0.1	μΑ
		Vcc=3.3V	240	300	360	kHz
fosc	Oscillator Frequency	Vcc=3.3V	600	700	800	kHz
		Vcc=3.3V	1200	1400	1600	kHz
Maxduty1	CH1 Max. Duty Cycle	Vcc=3.3V	86	91		%
Maxduty2	CH2 Max. Duty Cycle	Vcc=3.3V	86	91		%
<b>t</b> ss1	CH1 Soft-start Time	Vcc=3.3V, V <sub>FB1</sub> =0.9V		4.5		ms
tss2	CH2 Soft-start Time	Vcc=3.3V, V <sub>FB2</sub> =0.12V		4.5		ms
<b>t</b> DLY	Delay Time for Protection	Vcc=3.3V	20	50		ms
R <sub>LX1</sub>	Lx1 ON Resistance	Vcc=3.3V		400		mΩ
lofflx1	Lx1 Leakage Current	Vcc=5.5V, VLX1=20V			5	μΑ
ILIMLX1	Lx1 Current limit	Vcc=3.3V	1.0	1.5		Α
R <sub>LX2</sub>	Lx2 ON Resistance	Vcc=3.3V		400		mΩ
lofflx2	Lx2 Leakage Current	Vcc=5.5V, V <sub>L</sub> x=-14.5V			5	μΑ
ILIMLX2	Lx2 Current limit	Vcc=3.3V	1.0	1.5		А
RVOUTN	Vouth Discharge Resistance	Vcc=3.3V, Voutn=-0.3V		10	25	Ω
Vcel	CE "L" Input Voltage	Vcc=2.5V			0.3	V
Vceh	CE "H" Input Voltage	Vcc=5.5V	1.5			V
ICEL	CE "L" Input Current	Vcc=5.5V	-1.0		1.0	μА
Ісен	CE "H" Input Current	Vcc=5.5V	-1.0		1.0	μА

# TYPICAL APPLICATION



### Pin Connection

Externally short Vcc pin to PVcc pin. Externally short GND pin to PGND pin.

### Step-up DC/DC converter output voltage setting

The output voltage  $V_{\text{OUT1}}$  of the step-up DC/DC converter is controlled with maintaining the  $V_{\text{FB1}}$  as 1.0V.  $V_{\text{OUT1}}$  can be set with adjusting the values of R1 and R2 as in the next formula.  $V_{\text{OUT1}}$  can be set equal or less than 20V.

 $V_{OUT1} = V_{FB1} \times (R1+R2) / R1$ 

### Inverting DC/DC converter output voltage setting

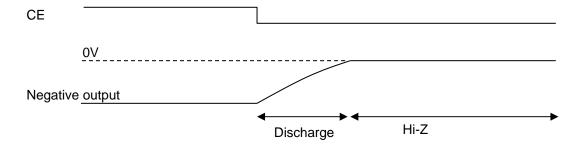
The output voltage  $V_{\text{OUT2}}$  of the inverting DC/DC converter is controlled with maintaining the  $V_{\text{FB2}}$  as 0V.  $V_{\text{OUT2}}$  can be set with adjusting the values of R4 and R5 as in the next formula.

 $V_{\text{OUT2}} = V_{\text{FB2}} - \left(V_{\text{REF}} - V_{\text{FB2}}\right) \times R5 \ / \ R4$ 

### Auto Discharge Function

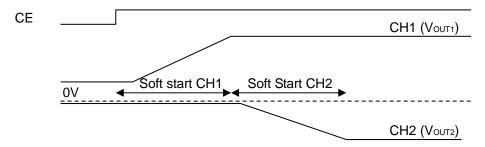
When CE level turns from "H" to "L" level, the R1283x goes into standby mode and switching of the outputs of L<sub>X1</sub> and L<sub>X2</sub> will stop. Then dischage Tr. between V<sub>OUT2</sub> and V<sub>CC</sub> turns on and discharges the negative output voltage. When the negative output voltage is discharged to 0V, the Tr. turns off and the negative output will be Hi-Z.

When the Auto discharge function is unnecessary, Voutn connect to Vcc or make be Hi-Z.



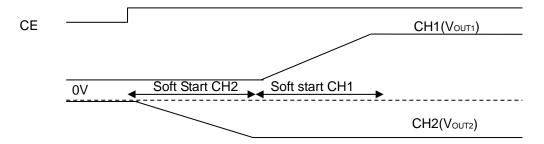
### Start up Sequence (R1283x001x)

When CE level turns from "L" to "H" level, the softstart of CH1 starts the operation. After detecting output voltage of CH1(Vout1) as the nominal level, the soft start of CH2 starts the operation.



### • Start up Sequence (R1283x002x)

When CE level turns from "L" to "H" level, the softstart of CH2 starts the operation. After detecting output voltage of CH2(Vout2) as the nominal level, the soft start of CH1 starts the operation.



### Short protection circuit timer

In case that the voltage of V<sub>FB1</sub> drops, the error amplifier of CH1 outputs "H". In case that the voltage of V<sub>FB2</sub> rises, the error amplifier of CH2 outputs "L". The built-in short protection circuit makes the ineternal timer operate with detecting the output of the error amplifier of CH1 as "H", or the output of the error amplifier of CH2 as "L". After the setting time will pass, the switching of LX1 and LX2 will stop.

To release the latch operatoion, make the Vcc set equal or less than UVLO level and restart or set the CE pin as "L" and make it "H" again.

During the softstart operation of CH1 and CH2, the timer operates independently from the outputs of the error amplifiers. Therefore, even if the softstart cannot finish correctly because of the short circuit, the protection timer function will be able to work correctly.

### Phase Compensation

DC/DC converter's phase may lose 180 degree by external components of L and C and load current. Because of this, the phase margin of the system will be less and the stability will be worse. Therefore, the phase must be gained.

A pole will be formed by external components, L and C.

Fpole ~ 1 /  $\{2 \times \pi \times \sqrt{(L1 \times C2)}\}$  (CH1)

Fpole ~ 1 /  $\{2 \times \pi \times \sqrt{(L2 \times C3)}\}$  (CH2)

Zero will be formed with R2, C5, R5, and C6.

Fzero ~  $1/(2 \times \pi \times R2 \times C5)$  (CH1)

Fzero ~  $1/(2 \times \pi \times R5 \times C6)$  (CH2)

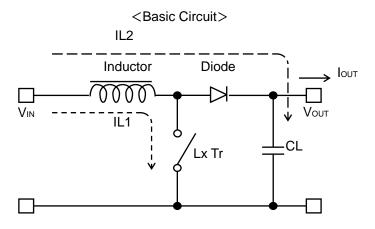
Set the cut-off frequency of the Zero close to the cut off frequency of the pole by L and C.

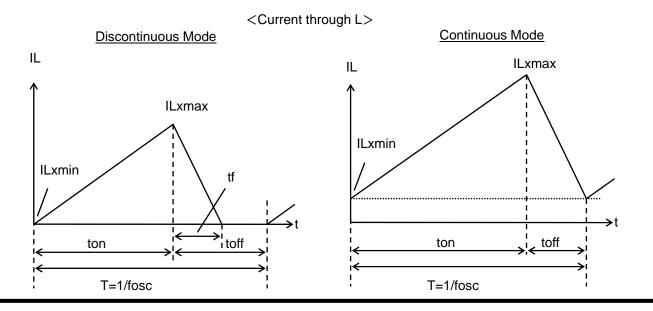
### • To reduce the noise of Feedback voltage

If the noise of the system is large, the output noise affects the feedback and the operation may be unstable. In that case, resistor values, R1, R2, R4, and R5 should be set lower and make the noise into the feedback pin reduce. Another method is set R3 and R6 . The appropriate value range is from  $1k\Omega$  to  $5k\Omega$ .

- Set a ceramic  $1\mu F$  or more capacitor as C1B between Vcc pin and GND. Set another  $4.7\mu F$  or more capacitor between PVcc and GND as C1.
- Set a ceramic 1μF or more capacitor between VouT1 and GND, and between VouT2 and GND for each as C2 and C3. Recommendation value range is from 4.7μF to 22μF.
- Set a ceramic capacitor between VREF and GND as C4. Recommendation value range is from  $0.1\mu F$  to  $2.2\mu F$ .

# Operation of Step-up DC/DC Converter and Output Current





### R1283x

There are two operation modes for the PWM control step-up switching regulator, that is the continuous mode and the discontinuous mode.

When the Lx Tr. is on, the voltage for the inductor L will be V<sub>IN</sub>. The inductor current (IL1) will be;

When the Lx transistor turns off, power will supply continuously. The inductor current at off (IL2) will be;

$$IL2 = (V_{OUT} - V_{IN}) \times tf / L$$
 Formula2

In terms of the PWM control, when the tf=toff, the inductor current will be continuous, the operation of the switching regulator will be continuous mode.

In the continuous mode, the current variation of IL1 and IL2 are same, therefore

$$V_{IN} \times ton / L = (V_{OUT} - V_{IN}) \times toff / L$$
 Formula 3

In the continuous mode, the duty cycle will be

If the input power equals to output power,

$$I_{OUT} = V_{IN}^2 \times ton / (2 \times L \times V_{OUT})$$
 Formula5

When lout becomes more then Formula5, it will be continuous mode.

In this moment, the peak current, ILxmax flowing through the inductor is described as follows:

ILxmax = 
$$I_{OUT} \times V_{OUT} / V_{IN} + V_{IN} \times ton / (2 \times L)$$
......Formula6

$$ILxmax = I_{\text{OUT}} \times V_{\text{OUT}} / V_{\text{IN}} + V_{\text{IN}} \times T \times (V_{\text{OUT}} - V_{\text{IN}}) / (2 \times L \times V_{\text{OUT}}) \dots Formula 7$$

Therefore, peak current is more than IouT. Considering the value of ILxmax, the condition of input and output, and external components should be selected.

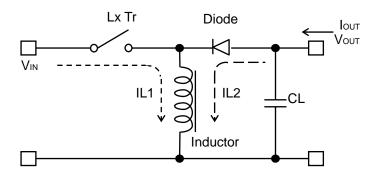
The explanation above is based on the ideal calculation, and the loss caused by Lx switch and external components is not included.

The actual maximum output current is between 50% and 80% of the calculation.

Especially, when the IL is large, or  $V_{IN}$  is low, the loss of  $V_{IN}$  is generated with on resistance of the switch. As for  $V_{OUT}$ ,  $V_F$ (as much as 0.3V) of the diode should be considered.

# **Operation of Inverting DC/DC Converter and Output Current**

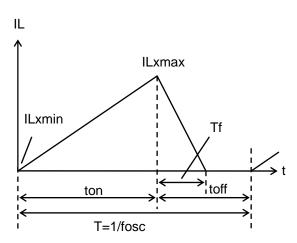
# <Basic Circuit>

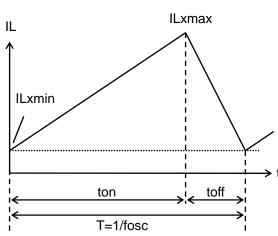


### <Current through L>

### Discontinuous Mode

### Continuous Mode





There are also two operation modes for the PWM control inverting switching regulator, that is the continuous mode and the discontinuous mode.

When the Lx Tr. is on, the voltage for the inductor L will be V<sub>IN</sub>. The inductor current (IL1) will be;

$$IL1 = V_{IN} \times ton / L$$
 Formula8

Inverting circuit saves energy during on time of Lx Tr, and supplies the energy to output during off time, output voltage opposed to input voltage is obtained. The inductor current at off (IL2) will be;

In terms of the PWM control, when the tf=toff, the inductor current will be continuous, the operation of the switching regulator will be continuous mode.

In the continuous mode, the current variation of IL1 and IL2 are same, therefore

### R1283x

 $V_{IN} \times ton / L = |V_{OUT}| \times toff / L$  Formula 10

In the continuous mode, the duty cycle will be:

$$DUTY = ton / (ton + toff) = |Vout| / (|Vout| + V_{IN}) .... Formula 11$$

If the input power equals to output power,

$$I_{OUT} = V_{IN}^2 \times ton / (2 \times L \times |V_{OUT}|)$$
 Formula 12

When lout becomes more then Formula 12, it will be continuous mode.

In this moment, the peak current, ILxmax flowing through the inductor is described as follows:

$$ILxmax = Iout \times |Vout| / Vin + Vin \times ton / (2 \times L) .....Formula 13$$

$$ILxmax = Iout \times |Vout| / Vin + Vin \times |Vout| \times T / \left\{ 2 \times L \times (|Vout| + Vin) \right\} .....Formula 14$$

Therefore, peak current is more than IouT. Considering the value of ILxmax, the condition of input and output, and external components should be selected.

The explanation above is based on the ideal calculation, and the loss caused by Lx switch and external components is not included.

The actual maximum output current is between 50% and 80% of the calculation.

Especially, when the IL is large, or  $V_{IN}$  is low, the loss of  $V_{IN}$  is generated with on resistance of the switch. As for  $V_{OUT}$ ,  $V_F$  (as much as 0.3V) of the diode should be considered.

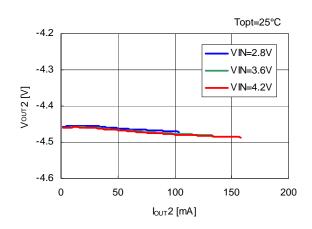
### TYPICAL CHARACTERISTICS

### 1) Output Voltage VS. Output Current

### R1283x001A

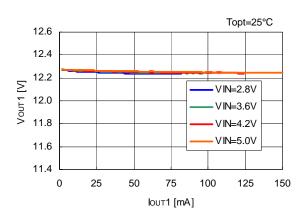
# 4.8 4.7 4.6 4.5 4.4 0 50 100 150 200

### R1283x001A

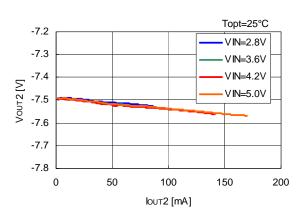


R1283x001A

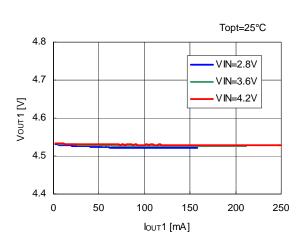
lout1 [mA]



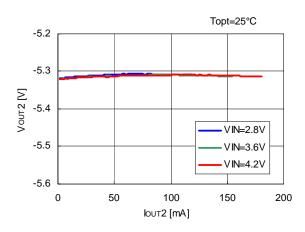
R1283x001A



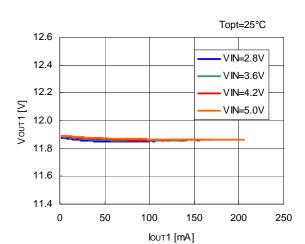
R1283x001B



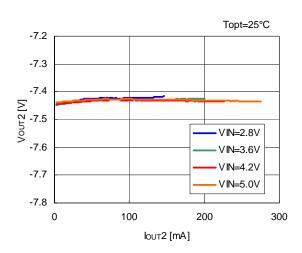
R1283x001B



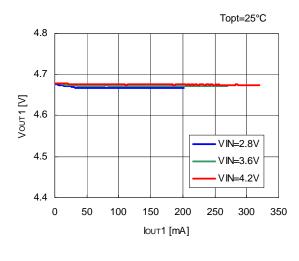
### R1283x001B



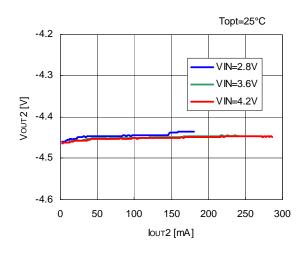
### R1283x001B



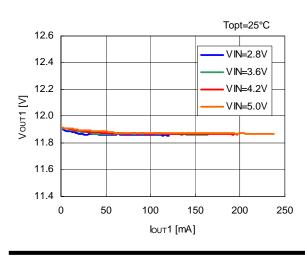
### R1283x001C



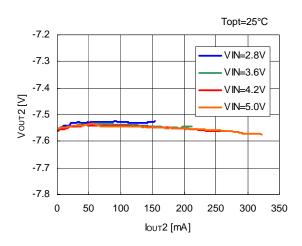
R1283x001C



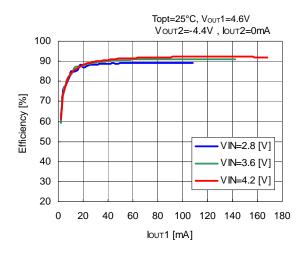
### R1283x001C



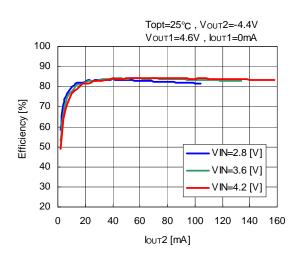
### R1283x001C



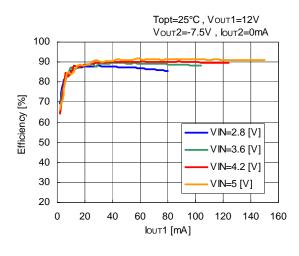
# 2) Efficiency vs. Output Current R1283x001A



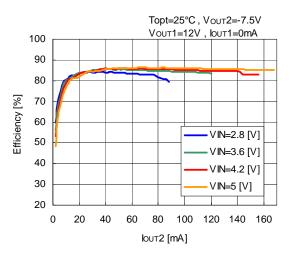
### R1283x001A



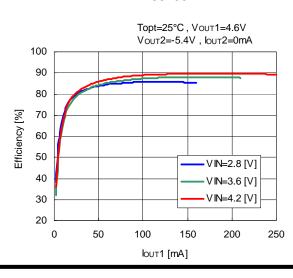
R1283x001A



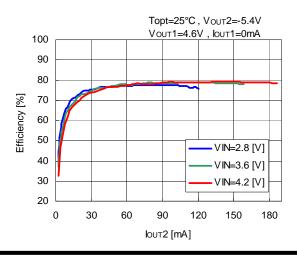
R1283x001A



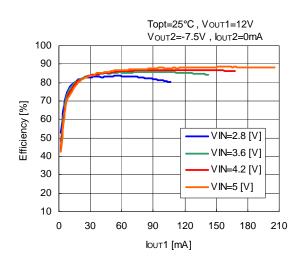
R1283x001B



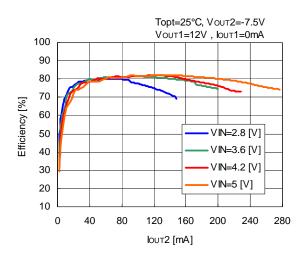
R1283x001B



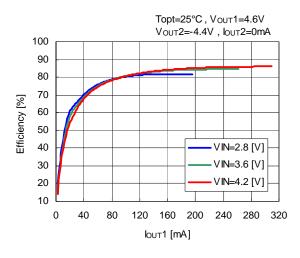
### R1283x001B



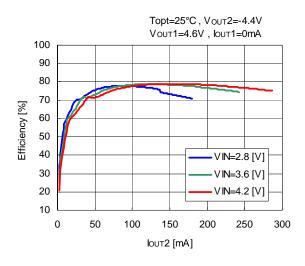
### R1283x001B



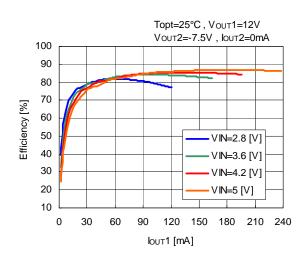
### R1283x001C



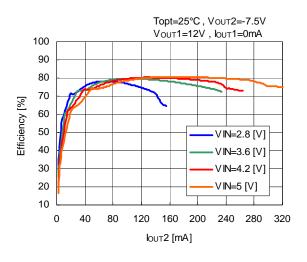
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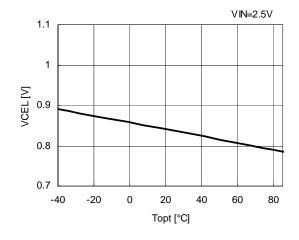
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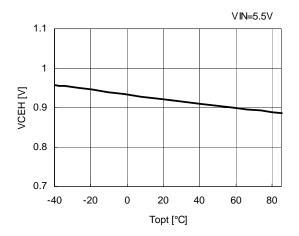
### R1283x001C



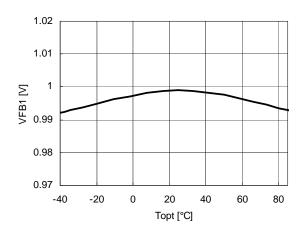
3) CE "L" Input Voltage vs. Temperature R1283x00xx



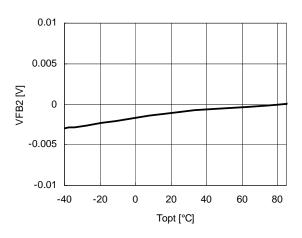
4) CE "H" Input Voltage vs. Temperature R1283x00xx



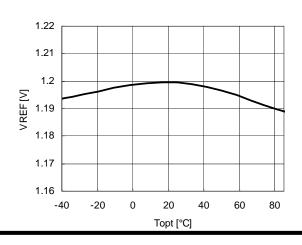
5) VFB1 Voltage vs. Temperature R1283x00xx



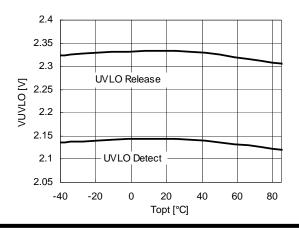
6) VFB2 Voltage vs. Temperature R1283x00xx



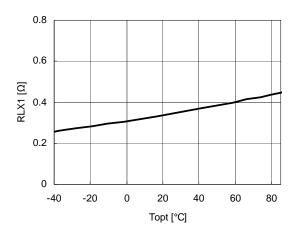
7) VREF Voltage vs. Temperature R1283x00xx



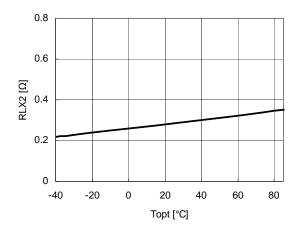
8) UVLO Voltage vs. Temperature R1283x00xx



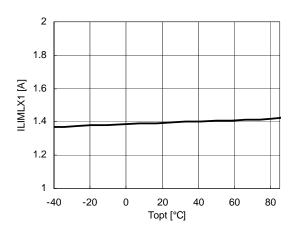
9) LX1 ON Resistance vs. Temperature R1283x00xx



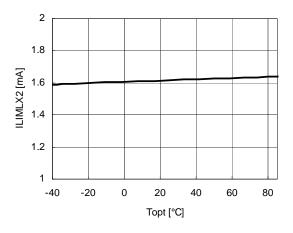
10) LX2 ON Resistance VS. Temperature R1283x00xx



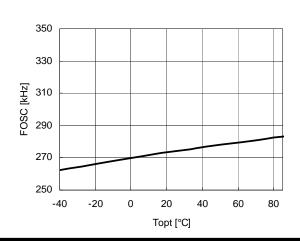
11) LX1 Limit Current VS. Temperature R1283x00xx



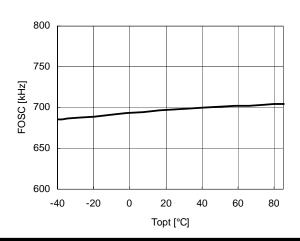
12) LX2 Limit Current vs. Temperature R1283x00xx



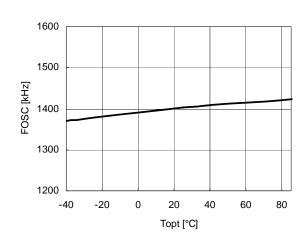
13) Osillator Frequency vs. Temperature R1283x00xA



R1283x00xB

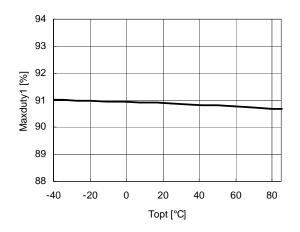


### R1283x00xC

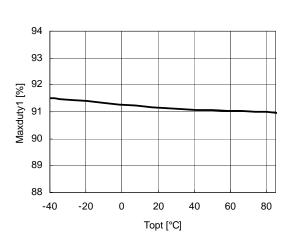


# 14) Maxduty1 vs. Temperature

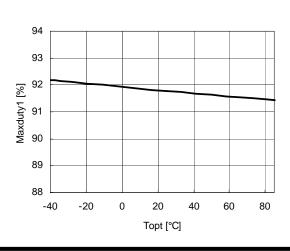
### R1283x00xA



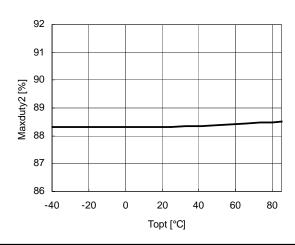
### R1283x00xB



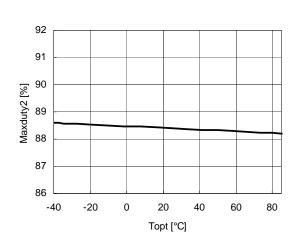
### R1283x00xC



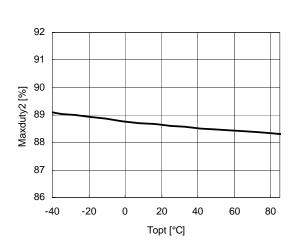
# 15) Maxduty2 vs. Temperature R1283x00xA



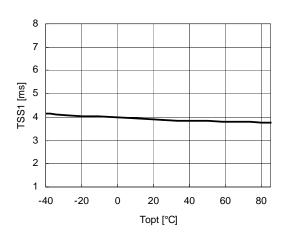
R1283x00xB



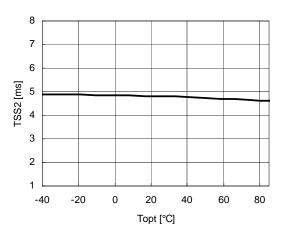
R1283x00xC



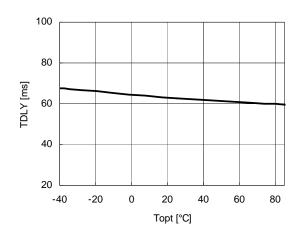
16) CH1 Soft-start Time VS. Temperature R1283x00xx



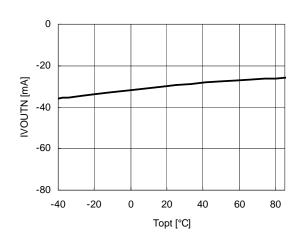
17) CH2 Soft-start Time VS. Temperature R1283x00xx



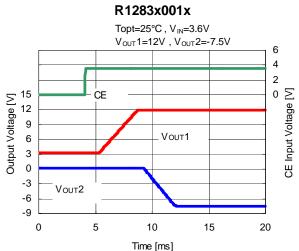
18) Timer Latch Delay Time vs. Temperature R1283x00xx

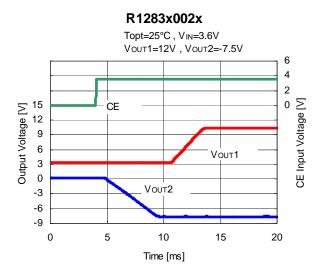


19) VOUTN Discharge Current vs. Temperature R1283x00xx



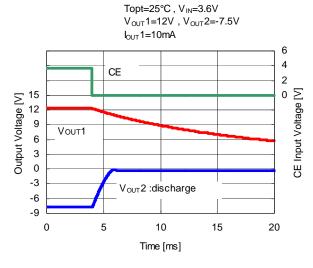
### 20) Startup Response



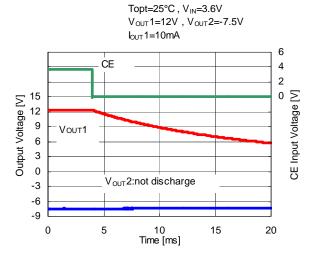


### 21)Shut down Response

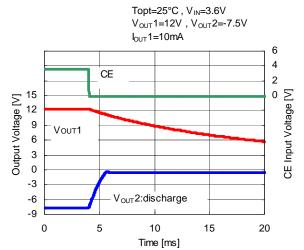
### R1283x001x



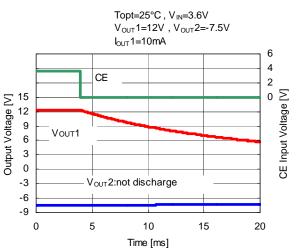
### R1283x001x (VOUTN=Open)

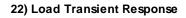


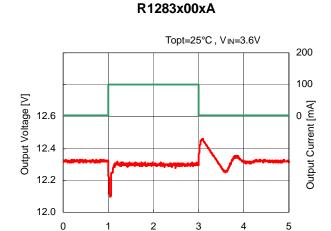
### R1283x002x



### R1283x002x (VOUTN=Open)

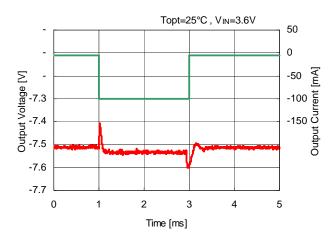


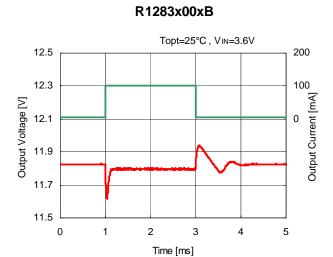




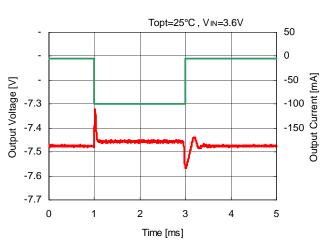
Time [ms]

### R1283x00xA

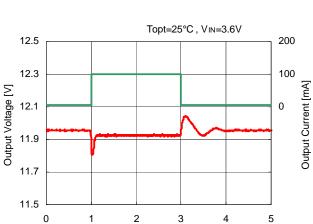




### R1283x00xB



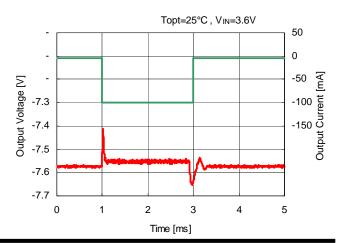




Time [ms]

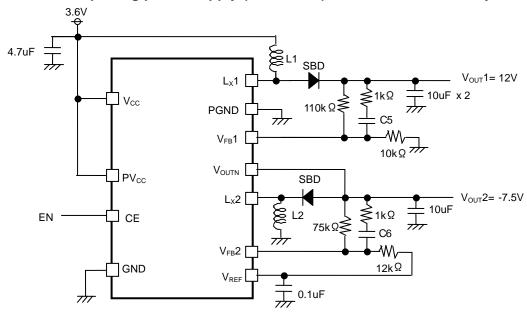
R1283x00xC

### R1283x00xC



# **APPLIED CIRCUIT**

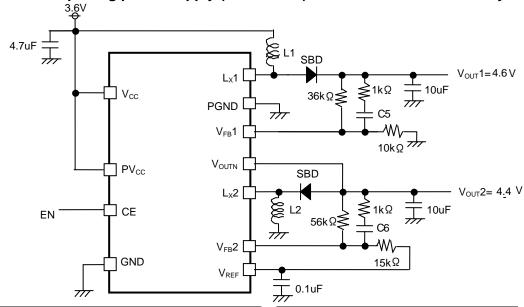
### 1) Application with outputting power supply (+12V/-7.5V) for CCD from Li battery



	L1	L2	C5	C6
R1283x00xA	15μΗ	10μΗ	220pF	220pF
R1283x00xB	6.8µH	6.8μΗ	150pF	150pF
R1283x00xC	4.7μH	4.7μH	120pF	120pF

Inductor	VLF3010 (TDK)
SBD	CRS10I30A (TOSHIBA)

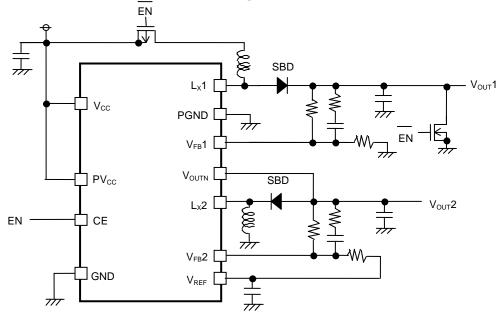
# 2) Application with outputting power supply (+4.6V/-4.4V) for AMOLED from Li battery



	L1	L2	C5	C6
R1283x00xA	15μΗ	10μΗ	100pF	100pF
R1283x00xB	4.7μΗ	4.7μΗ	47pF	33pF
R1283x00xC	4.7μΗ	4.7μΗ	68pF	47pF

Inductor	VLF3010 (TDK)		
SBD	CRS10I30A (TOSHIBA)		

# 3) Application with output disconnect and discharge.





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