

R1526x Series

AEC-Q100 Compliant

300 mA, 42 V Voltage Regulator with High Noise Immunity for Automotive Applications

No. EC-527-211022

OVERVIEW

The R1526x is a voltage regulator featuring 300mA output current and 42 V maximum input voltage. Since this device has excellent noise immunity to external electromagnetic interference, it is suitable for use in environments where electromagnetic waves may cause malfunctions.

KEY BENEFITS

- Excellent noise immunity. Refer to Noise Immunity Test in Typical Characteristics.
- Pin configuration considering functional safety

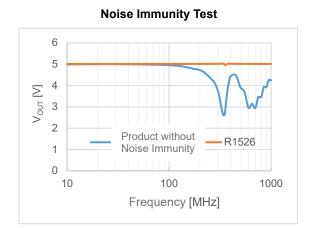
KEY SPECIFICATIONS

- Input Voltage Range (Maximum Rating):
 3.5 V to 42 V (50 V)
- Operating temperature range: -40°C to 125°C
- Standby Current: Typ. 0.1 μA
- Dropout Voltage: Typ. 0.4 V (I_{OUT} = 300 mA, V_{SET} = 5.0 V)
- Output Voltage: 1.8 V to 9.0 V (in 0.1 V step)
- Output Voltage Accuracy: ±0.6 % (Ta = 25°C)

 $\pm 1.6 \% (-40^{\circ}\text{C} \le \text{Ta} \le 125^{\circ}\text{C})$

- Short-circuit Protection: Limit at Typ. 100 mA
- Overcurrent Protection: Limit at Typ. 450 mA
- Thermal Shutdown: Detection Temperature. Typ. 160°C
- Output capacitor: C_{0∪T} ≥ 10 µF
- Ripple Rejection: Typ. 50 dB (f = 100 Hz)

TYPICAL CHARACTERISTICS



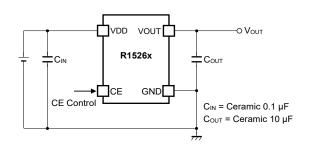
DPI method

PACKAGE

TYPICAL APPLICATION



HSOP-8E 5.2 x 6.2 x 1.45 mm



APPLICATIONS

 In-vehicle electrical equipment such as EPSECU, ADAS/autonomous driving system ECU, meter ECU, telematics ECU.

SELECTION GUIDE

The set output voltage and the quality class are user-selectable.

Selection Guide

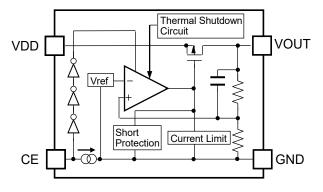
Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1526Sxx1B-E2-#E	HSOP-8E	1,000 pcs	Yes	Yes

xx: Specify the set output voltage (V_{SET})
1.8 V (18) to 9.0 V (90) in 0.1 V step
Refer to *Product-specific Electrical Characteristics* for details.

#: Quality Class

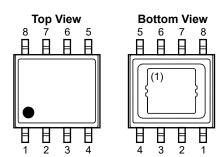
#	Operating Temperature Range	Test Temperature
Α	−40°C to 125°C	25°C, High
K	−40°C to 125°C	Low, 25°C, High

BLOCK DIAGRAM



R1526x Block Diagram

PIN DESCRIPTIONS



HSOP-8E Pin Configuration

HSOP-8E Pin Descriptions

Pin No.	Pin Name	Description			
1	VOUT	Output Pin			
2	NC ⁽²⁾	No Connection			
3	NC ⁽²⁾	No Connection			
4	CE	Chip Enable Pin (Active-high)			
5	GND ⁽³⁾	Ground Pin			
6	GND ⁽³⁾	Ground Pin			
7	NC ⁽²⁾	No Connection			
8	VDD	Input Pin			

Pin Equivalent Circuit Diagrams



VOUT Pin Equivalent Circuit Diagram

CE Pin Equivalent Circuit Diagram

⁽¹⁾ The tab on the bottom of the package is substrate level (GND). The tab must be connected to the ground plane on the board.

⁽²⁾ NC pin should be set to "Open".

⁽³⁾ GND pins should be connected together when mounted on a board.

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
V _{IN}	VIN Pin Input Voltage	-0.3 to 50	V
VIN	VIN Pin Peak Voltage ⁽¹⁾	60	V
Vce	CE Pin Input Voltage	-0.3 to 50	V
V _{CE}	CE Pin Peak Voltage (1)	60	V
Vout	VOUT Pin Voltage	-0.3 to $V_{IN} + 0.3 \le 50$	V
l _{оит}	Output Current	500	mA
P _D	Power Dissipation	Refer to Appendix "Power Dissipation"	
Tj	Junction Temperature Range	-40 to 150	°C
Tstg	Storage Temperature Range	−55 to 150	°C

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause permanent damage and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS

Recommended Operating Conditions

Symbol	Parameter	Rating	Unit
V _{IN}	Operating Input Voltage	3.5 to 42	V
Та	Operating Temperature Range	-40 to 125	°C

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

⁽¹⁾ Duration time: within 200 ms

ELECTRICAL CHARACTERISTICS

 V_{IN} = 14 V, V_{CE} = V_{IN} , unless otherwise specified. The specifications surrounded by are guaranteed by design engineering at -40° C \leq Ta \leq 125 $^{\circ}$ C.

R1526x-AE Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Symbol	Parameter	Condi	tions	Min.	Тур.	Max.	Unit
Iss	Supply Current	$V_{IN} = 14 \text{ V}, I_{OUT} = 0 \text{ m/s}$	4		32	55	μA
Istandby	Standby Current	V _{IN} = 42 V, V _{CE} = 0 V			0.1	2.0	μA
V Outpu	Outout Valtage	$8 \text{ V}^{(1)} \le \text{V}_{\text{IN}} \le 16 \text{ V},$	Ta = 25°C	×0.994		×1.006	V
Vout	Output Voltage	I _{OUT} = 1 mA	-40°C ≤ Ta ≤ 125°C	×0.984		×1.016	V
			1.8 V ≤ V _{SET} ≤ 2.8 V	-5		45	
		$V_{IN} = V_{SET} + 2.0 V$, $1 \text{ mA} \le I_{OUT} \le 100 \text{ mA}$	2.8 V < V _{SET} ≤ 5.4 V	-5		40	
ΔV оит	Load Degulation (2)		5.4 V < V _{SET} ≤ 9.0 V	-5		72	m)/
ΔI_{OUT}	Load Regulation (2)		1.8 V ≤ V _{SET} ≤ 2.8 V	-5		68	mV
		$V_{IN} = V_{SET} + 2.0 V$, $1 \text{ mA} \le I_{OUT} \le 300 \text{ mA}$	2.8 V < V _{SET} ≤ 5.4 V	-5		60	
			5.4 V < V _{SET} ≤ 9.0 V	-5		108	
ΔV_OUT	Line Degulation (3)	V _{SET} +1V ⁽⁴⁾ ≤ V _{IN} ≤42V,	1.8 V ≤ V _{SET} ≤ 2.8 V	-30		30	mV
ΔV_{IN}	Line Regulation (3)	I _{OUT} = 1 mA	2.8 V < V _{SET} ≤ 9.0 V	-0.02		0.02	%/V
			1.8 V ≤ V _{SET} ≤ 2.4 V		1.73	1.76	
			2.4 V < V _{SET} ≤ 2.8 V		0.75	1.35	
V_{DIF}	Dropout Voltage (5)	I _{ОUТ} = 300 mA	2.8 V < V _{SET} < 5.0 V		0.71	1.23	V
			5.0 V ≤ V _{SET} < 8.0 V		0.40	0.74	
			8.0 V ≤ V _{SET} ≤ 9.0 V		0.35	0.65	
ILIM	Output Current Limit	V _{IN} = V _{SET} + 3.0 V	300	450		mA	
Isc	Short-circuit Current	$V_{IN} = 3.5 \text{ V}, V_{OUT} = 0 \text{ V}$		100		mA	
V _{CEH}	CE Pin Input Voltage, High		2.0		42	V	
V _{CEL}	CE Pin Input Voltage, Low	V _{IN} = 42 V			1.0	V	
I _{PD}	CE Pull-down Current	V _{IN} = 42 V, V _{CE} = 2 V			0.2	0.6	μA

All parameters are tested under the pulse load condition (Tj ≈ Ta = 25°C).

 $^{^{(1)}}$ When $V_{SET} > 7 \text{ V}$, $V_{IN} = V_{SET} + 1 \text{ V}$

 $^{^{(2)}}$ Output voltage change amount when 1 mA \leq $I_{OUT} \leq$ 100 mA and 1 mA \leq $I_{OUT} \leq$ 300 mA,

 $[\]Delta V_{OUT}/\Delta I_{OUT} = V_{OUT}$ (@ $I_{OUT} = 100$ mA) - V_{OUT} (@ $I_{OUT} = 1$ mA) or

 $[\]Delta V_{OUT} / \Delta I_{OUT} = V_{OUT}$ (@ $I_{OUT} = 300 \text{ mA}$) - V_{OUT} (@ $I_{OUT} = 1 \text{ mA}$)

 $^{^{(3)}}$ Output voltage change amount when V_{SET} +1V \leq V_{IN} \leq 42 V,

in case $V_{SET} \le 2.8 \text{ V}$, $\triangle V_{OUT}/\triangle V_{IN} = V_{OUT}$ (@ $V_{IN} = 42 \text{ V}$) - V_{OUT} (@ $V_{IN} = V_{SET} + 1 \text{ V}$) or

in case $V_{SET} > 2.8 \text{ V}$, $\Delta V_{OUT}/\Delta V_{IN} = (V_{OUT} (@V_{IN} = 42 \text{ V}) - V_{OUT} (@V_{IN} = V_{SET} + 1 \text{ V})) / (42 - (V_{SET} + 1)) / V_{SET} x 100 + (2.8 \text{ V}) + (2.8$

 $^{^{(4)}}$ When $V_{SET} \le 2.5 \text{ V}$, $V_{IN} = 3.5 \text{ V}$.

⁽⁵⁾ Dropout voltage is defined as the minimum value of the difference between the input and output voltages in order to supply a regulated output voltage with the specified load current.

 V_{IN} = 14 V, V_{CE} = V_{IN} , unless otherwise specified.

R1526x-KE Electrical Characteristics

(-40°C ≤ Ta ≤ 125°C)

1110207-		(+0 0 = 1a = 1		120 0			
Symbol	Parameter	Condi	itions	Min.	Тур.	Max.	Unit
Iss	Supply Current	V _{IN} = 14 V, I _{OUT} = 0 m	A		32	55	μA
Istandby	Standby Current	V _{IN} = 42 V, V _{CE} = 0 V			0.1	2.0	μA
	Output Voltage	$8 \text{ V}^{(1)} \le \text{V}_{\text{IN}} \le 16 \text{ V}$	Ta = 25°C	×0.994		×1.006	V
V _{OUT}	Output voltage	I _{OUT} = 1 mA	-40°C ≤ Ta ≤ 125°C	×0.984		×1.016	V
			1.8 V ≤ V _{SET} ≤ 2.8 V	-5		45	
		$V_{IN} = V_{SET} + 2.0 V$, $1 \text{ mA} \le I_{OUT} \le 100 \text{ mA}$	2.8 V < V _{SET} ≤ 5.4 V	-5		40	
ΔV_OUT	Load Degulation (2)	11111/ = 1001 = 100 1111/	5.4 V < V _{SET} ≤ 9.0 V	-5		72	m)/
ΔI_{OUT}	Load Regulation (2)		1.8 V ≤ V _{SET} ≤ 2.8 V	-5		68	mV
		$V_{IN} = V_{SET} + 2.0 V$, $1 \text{ mA} \le I_{OUT} \le 300 \text{ mA}$	2.8 V < V _{SET} ≤ 5.4 V	-5		60	
		1111111 = 1001 = 000 111111	5.4 V < V _{SET} ≤ 9.0 V	-5		108	
ΔV_{OUT}	Line Degulation (3)	V _{SET} +1V ⁽⁴⁾ ≤ V _{IN} ≤42V	1.8 V ≤ V _{SET} ≤ 2.8 V	-30		30	mV
ΔV_{IN}	Line Regulation (3)	I _{OUT} = 1 mA	2.8 V < V _{SET} ≤ 9.0 V	-0.02		0.02	%/V
			1.8 V ≤ V _{SET} ≤ 2.4 V		1.73	1.76	
			2.4 V < V _{SET} ≤ 2.8 V		0.75	1.35	
V_{DIF}	Dropout Voltage (5)	I _{OUT} = 300 mA	2.8 V < V _{SET} < 5.0 V		0.71	1.23	V
			5.0 V ≤ V _{SET} < 8.0 V		0.40	0.74	
			8.0 V ≤ V _{SET} ≤ 9.0 V		0.35	0.65	
ILIM	Output Current Limit	V _{IN} = V _{SET} + 3.0 V	300	450		mA	
Isc	Short-circuit Current	$V_{IN} = 3.5 \text{ V}, V_{OUT} = 0 \text{ V}$		100		mA	
V _{CEH}	CE Pin Input Voltage, High		2.0		42	V	
VCEL	CE Pin Input Voltage, Low	V _{IN} = 42 V	0		1.0	V	
I _{PD}	CE Pull-down Current	V _{IN} = 42 V, V _{CE} = 2 V			0.2	0.6	μA

 $^{^{(1)}}$ When $V_{SET} > 7 \text{ V}$, $V_{IN} = V_{SET} + 1 \text{ V}$

 $^{^{(2)}}$ Output voltage change amount when 1 mA \leq $I_{OUT} \leq$ 100 mA and 1 mA \leq $I_{OUT} \leq$ 300 mA,

 $[\]Delta V_{OUT}/\Delta I_{OUT} = V_{OUT}$ (@ $I_{OUT} = 100$ mA) - V_{OUT} (@ $I_{OUT} = 1$ mA) or

 $[\]Delta V_{OUT} / \Delta I_{OUT} = V_{OUT}$ (@ $I_{OUT} = 300 \text{ mA}$) - V_{OUT} (@ $I_{OUT} = 1 \text{ mA}$)

 $^{^{(3)}}$ Output voltage change amount when V_{SET} +1V \leq V_{IN} \leq 42 V,

in case $V_{SET} \le 2.8 \text{ V}$, $\triangle V_{OUT}/\triangle V_{IN} = V_{OUT}$ (@ $V_{IN} = 42 \text{ V}$) - V_{OUT} (@ $V_{IN} = V_{SET} + 1 \text{ V}$) or

in case $V_{SET} > 2.8 \text{ V}$, $\Delta V_{OUT}/\Delta V_{IN} = (V_{OUT} (@V_{IN} = 42 \text{ V}) - V_{OUT} (@V_{IN} = V_{SET} + 1 \text{ V})) / (42 - (V_{SET} + 1)) / V_{SET} x 100 + (2.8 \text{ V}) + (2.8$

 $^{^{(4)}}$ When V_{SET} ≤ 2.5 V, V_{IN} = 3.5 V.

⁽⁵⁾ Dropout voltage is defined as the minimum value of the difference between the input and output voltages in order to supply a regulated output voltage with the specified load current.

The specifications surrounded by \square are guaranteed by design engineering at -40°C \leq Ta \leq 125°C.

R1526x (-AE) Product-specific Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Product name		V _{OUT} (V) (Ta = 25°C)		(-40°0	V _{OUT} (V) C ≤ Ta ≤ 1	125°C)	V _{DIF}	· (V)
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	TYP.	MAX.
R1526S181B	1.7892	1.80	1.8108	1.7712	1.80	1.8288	1.73	1.76
R1526S251B	2.4850	2.50	2.5150	2.4600	2.50	2.5400	0.75	1 25
R1526S281B	2.7832	2.80	2.8168	2.7552	2.80	2.8448	0.75	1.35
R1526S301B	2.9820	3.00	3.0180	2.9520	3.00	3.0480		
R1526S331B	3.2802	3.30	3.3198	3.2472	3.30	3.3528	0.71	1.23
R1526S341B	3.3796	3.40	3.4204	3.3456	3.40	3.4544		
R1526S501B	4.9700	5.00	5.0300	4.9200	5.00	5.0800		
R1526S551B	5.4670	5.50	5.5330	5.4120	5.50	5.5880		
R1526S601B	5.9640	6.00	6.0360	5.9040	6.00	6.0960	0.40	0.74
R1526S641B	6.3616	6.40	6.4384	6.2976	6.40	6.5024		
R1526S751B	7.4550	7.50	7.5450	7.3800	7.50	7.6200		
R1526S801B	7.9520	8.00	8.0480	7.8720	8.00	8.1280		
R1526S851B	8.4490	8.50	8.5510	8.3640	8.50	8.6360	0.35	0.65
R1526S901B	8.9460	9.00	9.0540	8.8560	9.00	9.1440		

Product name		l _{ouT} (mV) _T ≤ 100 mA)		$\Delta V_{OUT}/\Delta I_{OUT}$ (mV) (1 mA $\leq I_{OUT} \leq 300$ mA) $\Delta V_{OUT}/\Delta V_{IN}$		τ/Δ V IN			
	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.			
R1526S181B								[00]	60
R1526S251B	-5	45	-5	68	<u>-30</u> (mV)	30 (mV)			
R1526S281B					(1117)	(1117)			
R1526S301B									
R1526S331B	[<u></u>	40	40	40	40		E0		
R1526S341B	-5		-5 60	60	00				
R1526S501B									
R1526S551B									
R1526S601B				l					-0.02 (%/V)
R1526S641B					(70/ V)	(/o/ v)			
R1526S751B	-5	72	-5	108					
R1526S801B	_	_	_						
R1526S851B									
R1526S901B									

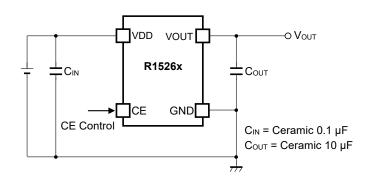
R1526x (-KE) Product-specific Electrical Characteristics

(-40°C ≤ Ta ≤ 125°C)

Product name		V _{оит} (V) (Та = 25°С)			V _{ουτ} (V) (−40°C ≤ Ta ≤ 125°C)			V _{DIF} (V)	
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	TYP.	MAX.	
R1526S181B	1.7892	1.80	1.8108	1.7712	1.80	1.8288	1.73	1.76	
R1526S251B	2.4850	2.50	2.5150	2.4600	2.50	2.5400	0.75	1.35	
R1526S281B	2.7832	2.80	2.8168	2.7552	2.80	2.8448	0.75	1.33	
R1526S301B	2.9820	3.00	3.0180	2.9520	3.00	3.0480			
R1526S331B	3.2802	3.30	3.3198	3.2472	3.30	3.3528	0.71	1.23	
R1526S341B	3.3796	3.40	3.4204	3.3456	3.40	3.4544			
R1526S501B	4.9700	5.00	5.0300	4.9200	5.00	5.0800			
R1526S551B	5.4670	5.50	5.5330	5.4120	5.50	5.5880			
R1526S601B	5.9640	6.00	6.0360	5.9040	6.00	6.0960	0.40	0.74	
R1526S641B	6.3616	6.40	6.4384	6.2976	6.40	6.5024			
R1526S751B	7.4550	7.50	7.5450	7.3800	7.50	7.6200			
R1526S801B	7.9520	8.00	8.0480	7.8720	8.00	8.1280			
R1526S851B	8.4490	8.50	8.5510	8.3640	8.50	8.6360	0.35	0.65	
R1526S901B	8.9460	9.00	9.0540	8.8560	9.00	9.1440			

Product name	ΔV _{OUT} /Δl _{OUT} (mV) (1 mA ≤ l _{OUT} ≤ 100 mA)			$\Delta V_{OUT}/\Delta I_{OUT}$ (mV) (1 mA \leq I _{OUT} \leq 300 mA)		_Γ /ΔV _{IN}		
	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.		
R1526S181B							0.0	
R1526S251B	-5	45	-5	68	-30 (mV)	30 (mV)		
R1526S281B					(1117)	(1117)		
R1526S301B					-0.02 (%/V)			
R1526S331B	E	40	40 -5	60				
R1526S341B	-5	40						
R1526S501B								
R1526S551B								
R1526S601B						0.02 (%/V)		
R1526S641B					(70/ V)	(70/ V)		
R1526S751B	-5	72	-5	108				
R1526S801B								
R1526S851B								
R1526S901B								

TYPICAL APPLICATION CIRCUIT



R1526x Typical Application Circuit

Component examples

Symbol	Capacitance	Tolerance	Voltage resistance	Temperature characteristics
C _{IN}	0.1 μF	±10%	50 V	X7R
Соит	10 μF	±10%	50 V	X7S

THEORY OF OPERATION

Thermal Shutdown Function

When the junction temperature exceeds the thermal shutdown detection temperature (Typ.160°C), R1526x goes into standby state and suppresses its self-heating. When the junction temperature falls below the thermal shutdown release temperature (Typ.135°C), this device becomes active.

Chip Enable Function

By inputting "High" and "Low" to the CE pin, R1526x can be set to active or standby state. The CE pin is pulled down with a constant current of Typ. $0.2~\mu$ A inside the IC. If the chip enable function is not needed, connect the CE pin directly to the VDD pin. R1526x can apply a voltage to the CE pin even when no voltage is applied to VDD pin.

TECHNICAL NOTES

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed a rated voltage, a rated current or a rated power. When designing a peripheral circuit, please be fully aware of the following points.

Phase Compensation

R1526x uses the output capacitor capacitance and equivalent series resistance (ESR) for phase compensation, to secure stable operation even when the load current is varied. For this purpose, make sure to use an output capacitor (C_{OUT}) of 10 μ F or more as close as possible to the VOUT pin. Since the output may oscillate depending on the ESR, select a low ESR capacitor with reference to *the series equivalent resistance vs. output current* characteristics in the datasheet. In addition, Make the power supply and GND lines sufficient. Connect a capacitor (C_{IN}) of 0.1 μ F or more between the VDD pin and GND, and keep the wiring as short as possible.

Behavior below the minimum operating voltage

When $V_{SET} \le 2.8$ V and the power supply voltage is below the recommended operating voltage, the output voltage may become unstable and exceed the set output voltage of LDO. To avoid this behavior at power-on, turn on the voltage of both VDD and CE pins at a slew rate of 35 V/ms or more when both pins are turned on at the same time. When turning on the VDD pin at a slew rate of 35 V/ms or less, change the CE pin from "Low" to "High" after the power supply voltage exceeds 3.5 V.

To avoid this behavior at power-off, turn off the voltage of both VDD and CE pins at a steeper slew rate than -35 V/ms when both pins are turned off at the same time.

When turning off the VDD pin at a slower slew rate than -35 V/ms, change the CE pin from "High" to "Low" before the power supply voltage falls below 3.5 V.

Thermal Shutdown Function

The thermal shutdown function prevents the IC from fuming and ignition but does not ensure the IC's reliability or keep the IC below the absolute maximum ratings. The thermal shutdown function does not operate on the heat generated by other than the normal IC operation such as latch-up and overvoltage application.

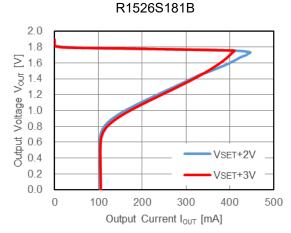
The thermal shutdown function operates in a state over the absolute maximum ratings, therefore the thermal shutdown function should not be used for a system design.

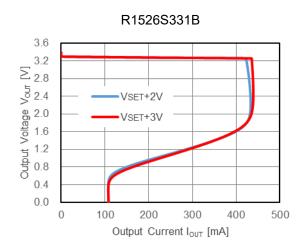
TYPICAL CHARACTERISTICS

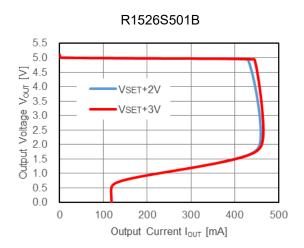
Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.

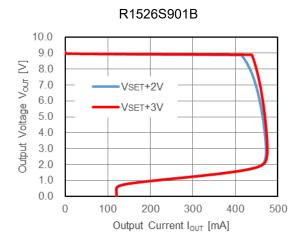
1) Output Voltage vs. Output Current

 C_{IN} = none, C_{OUT} = 10 μ F, Ta = 25 $^{\circ}$ C



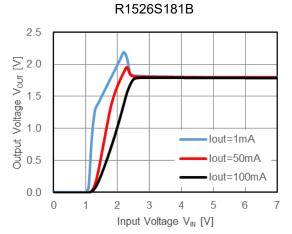


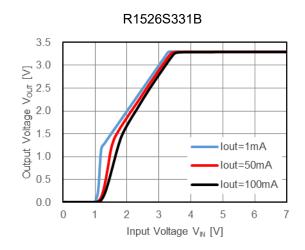




2) Output Voltage vs. Input Voltage

 C_{IN} = none, C_{OUT} = 10 μ F, Ta = 25 $^{\circ}$ C





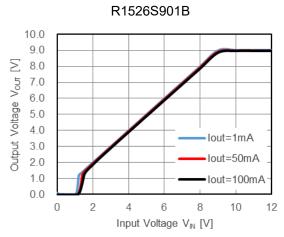
3

Input Voltage V_{IN} [V]

5

6

4

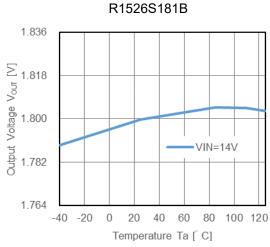


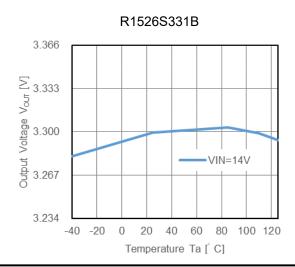
3) Output Voltage vs. Temperature

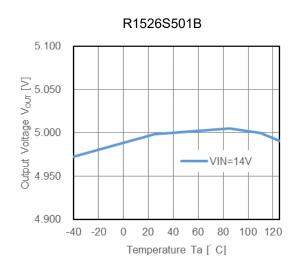
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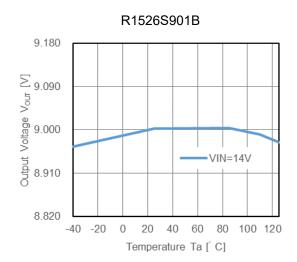
1

 $I_{OUT} = 1 \text{mA}, C_{IN} = \text{none}, C_{OUT} = 10 \mu \text{F}$



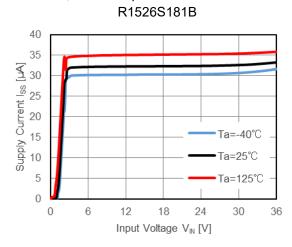


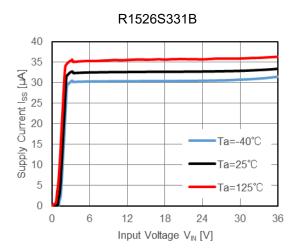




4) Supply Current vs. Input Voltage

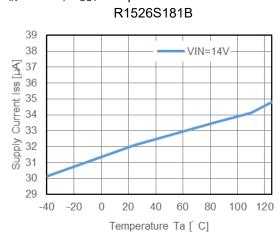
 C_{IN} = none, C_{OUT} = $10\mu F$

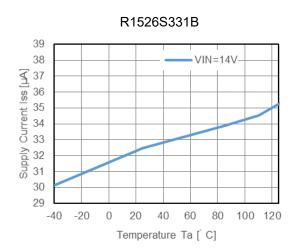


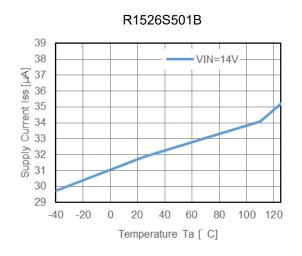


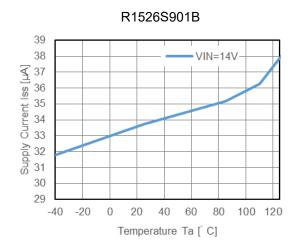
5) Supply Current vs. Temperature

 C_{IN} = none, C_{OUT} = $10\mu F$



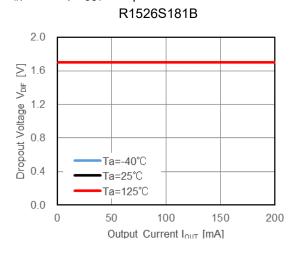


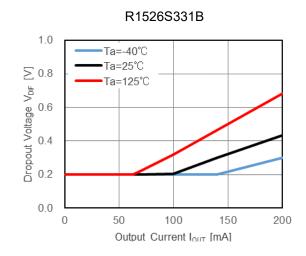


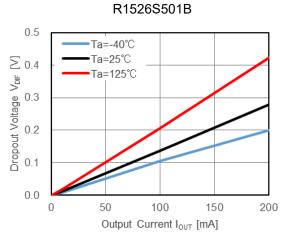


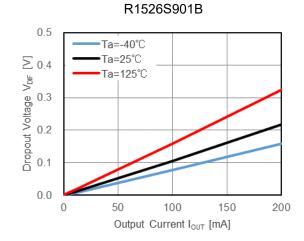
6) Dropout Voltage vs. Output Current

 C_{IN} = none, C_{OUT} = $10\mu F$



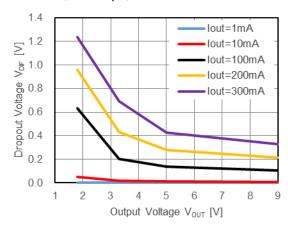






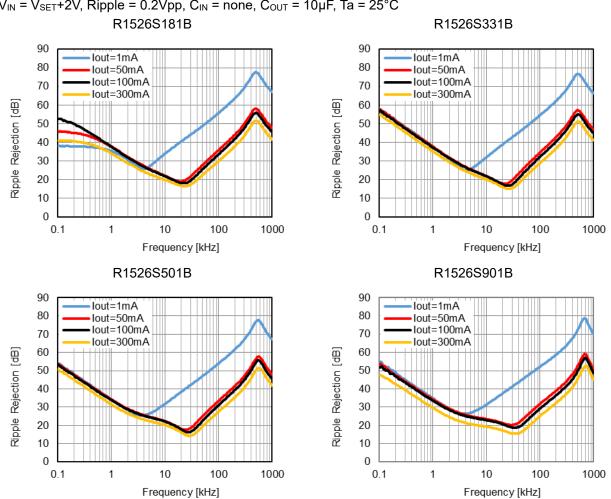
7) Dropout Voltage vs. Output Voltage

 C_{IN} = none, $C = 10\mu F$, $Ta = 25^{\circ}C$



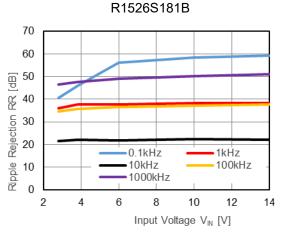
8) Ripple Rejection vs. Frequency

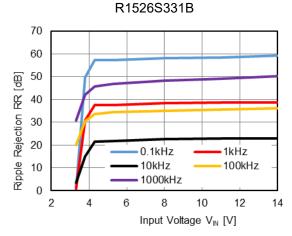
 $V_{IN} = V_{SET} + 2V$, Ripple = 0.2Vpp, $C_{IN} = \text{none}$, $C_{OUT} = 10 \mu F$, Ta = 25°C

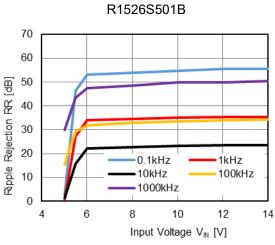


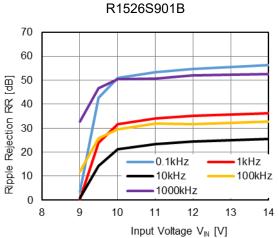
9) Ripple Rejection vs. Input Voltage

Ripple = 0.2Vpp, $I_{OUT} = 50$ mA, $C_{IN} =$ none, $C_{OUT} = 10\mu$ F, Ta = 25°C



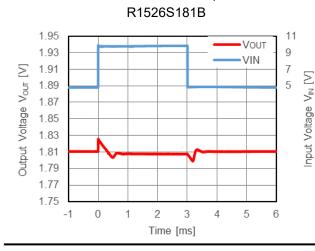


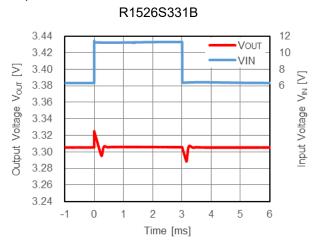


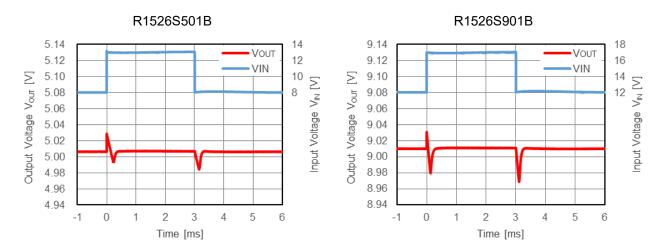


10) Input Transient Response

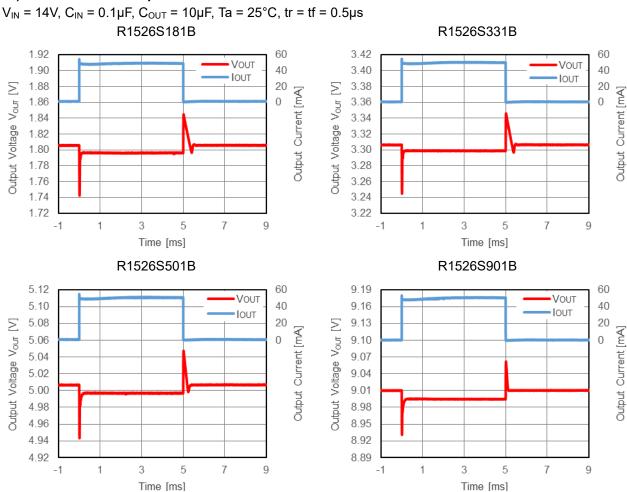
 $I_{OUT} = 1mA$, $C_{IN} = none$, $C_{OUT} = 10\mu F$, Ta = 25°C, $tr = tf = 1\mu s$





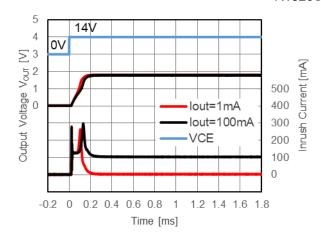


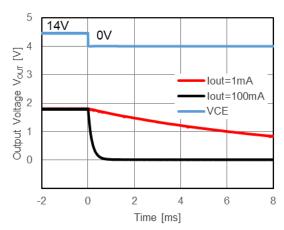
11) Load Transient Response



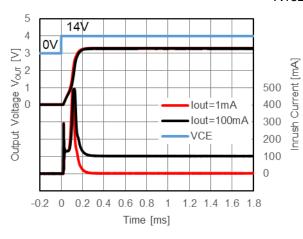
12) CE Transient Response

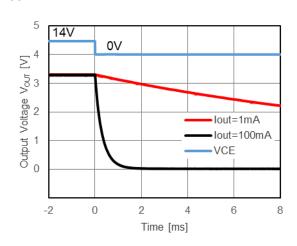
 V_{IN} = 14V, V_{CE} = 0V \rightarrow 14V, C_{IN} = 0.1 μ F, C_{OUT} = 10 μ F, Ta = 25°C, tr = tf = 1 μ s R1526S181B



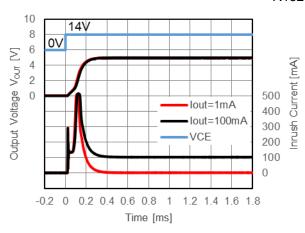


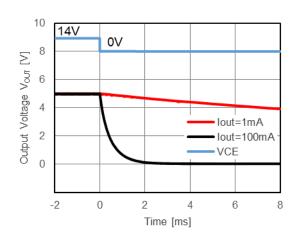
R1526S331B



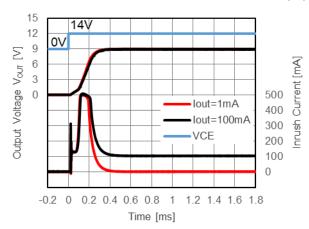


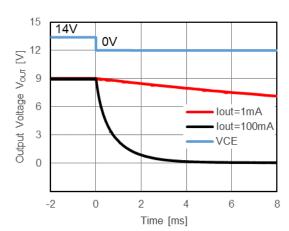
R1526S501B





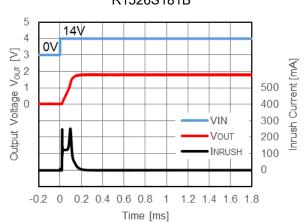
R1526S901B



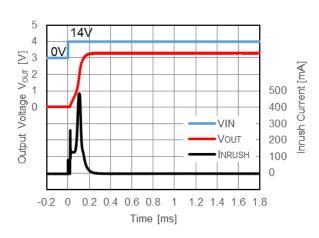


13) Power-on Transient Response

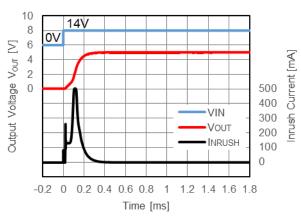
 I_{OUT} = 1mA, C_{IN} = none, C_{OUT} = 10 μ F, Ta = 25°C, tr = 1 μ s R1526S181B



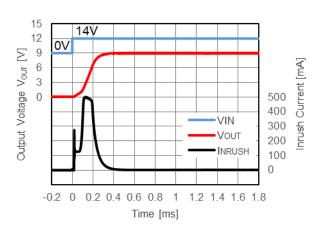
R1526S331B



R1526S501B

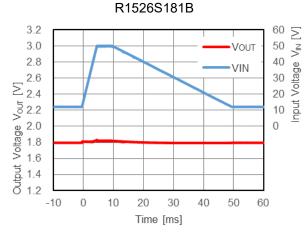


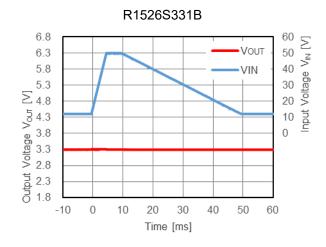
R1526S901B



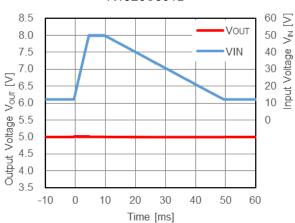
14) Load Dump

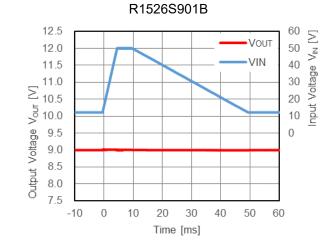
 I_{OUT} = 50mA, C_{IN} = 0.1 μ F, C_{OUT} = 10 μ F, Ta = 25 $^{\circ}$ C





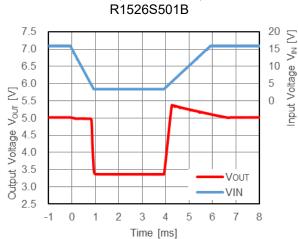
R1526S501B

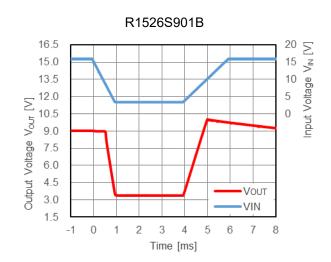




15) Cold Crank

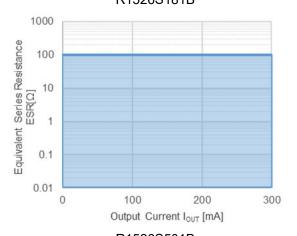
 $I_{OUT} = 1 \text{mA}, C_{IN} = 0.1 \mu\text{F}, C_{OUT} = 10 \mu\text{F}, Ta = 25 ^{\circ}\text{C}$

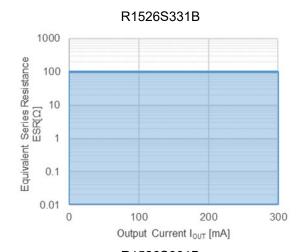




16) ESR (Equivalent Series Resistance)

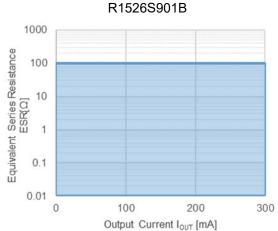
 C_{IN} = 0.1 μ F, C_{OUT} = 10 μ F, Ta = -40°C, 25°C, 125°C R1526S181B





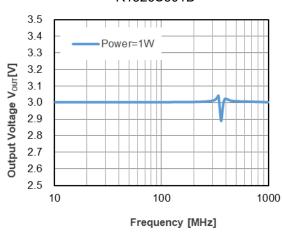
R1526S501B 1000 100 100 100 100 100 100 200 300

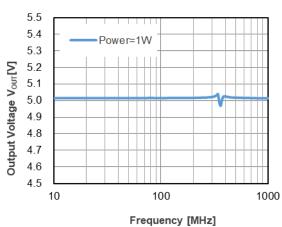
Output Current I_{OUT} [mA]



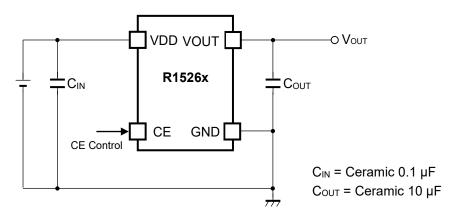
17) Noise Immunity

DPI method, V_{IN} = 14V, V_{CE} = 3V, V_{OUT} = 1W, C_{IN} = C_{CE} = 0.1 μ F, C_{OUT} = 10 μ F, Ta = 25°C R1526S301B R1526S501B





Test Circuit



Test Circuit for Typical Characteristics

Measurement Components

Symbol	Specification	Measurement Item	Manufacturer	Parts Number
C _{IN}	0.1µF	11,12,14,15,16,17	TDK	CGA4J2X7R1H104K
Соит	10µF	All Items	TDK	CGA4J1X7S1C106K

Measurement Components of Typical Characteristics

PD-HSOP-8E-(125150)-JE-B

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-7.

Measurement Conditions

Item	Measurement Conditions		
Environment	Mounting on Board (Wind Velocity = 0 m/s)		
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)		
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm		
Copper Ratio	Outer Layer (First Layer): Less than 95% of 50 mm Square Inner Layers (Second and Third Layers): Approx. 100% of 50 mm Square Outer Layer (Fourth Layer): Approx. 100% of 50 mm Square		
Through-holes	φ 0.3 mm × 21 pcs		

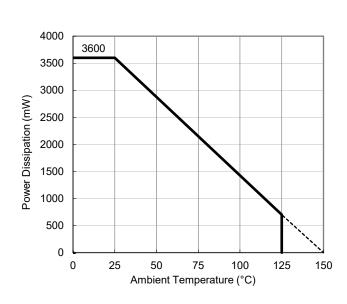
Measurement Result

(Ta = 25°C, Tjmax = 150°C)

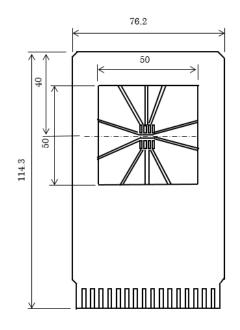
Item	Measurement Result
Power Dissipation	3600 mW
Thermal Resistance (θja)	θja = 34.5°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 10°C/W

θja: Junction-to-Ambient Thermal Resistance

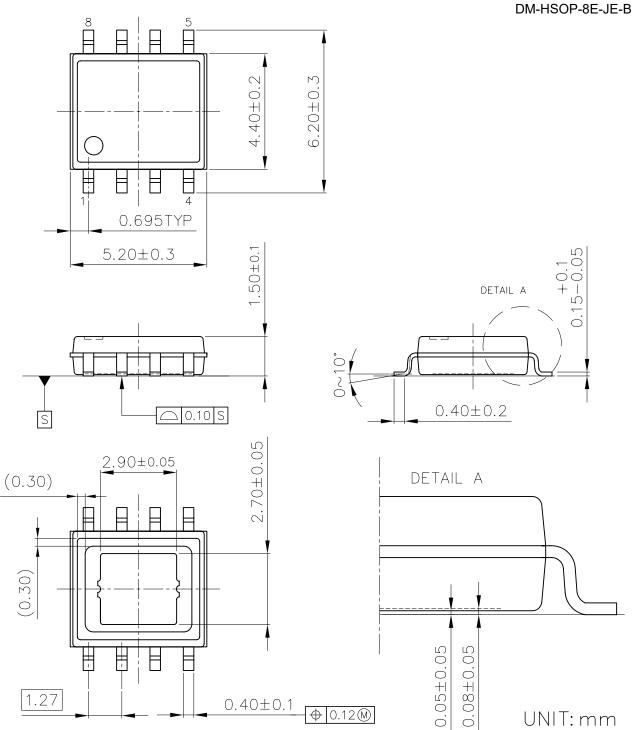
ψjt: Junction-to-Top Thermal Characterization Parameter



Power Dissipation vs. Ambient Temperature



Measurement Board Pattern



HSOP-8E Package Dimensions



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