Low ON Resistance Nch Load Switch IC

NO.EA-319-190108

OUTLINE

The R5541K is a CMOS-based dual supply voltage load switch IC. The R5541K is an ideal switch for supplying the power from the secondary power source such as the output of a step-down DC/DC converter to the load circuit. A built-in Nch. transistor with typically 18 mΩ ON resistance allows the R5541K to provide a low dropout voltage and prevents the reverse current during shutdown mode. Internally, a single IC consists of an internal voltage step-up circuit, a soft-start circuit, a thermal shutdown circuit, a chip enable circuit and a UVLO circuit.

The gate voltage of Nch. driver transistor is supplied by a soft-start circuit. The soft-start circuit is supplied by the external power source (V_{BIAS}). Soft-start time is adjustable by connecting an external capacitor.

The R5541K is offered in an ultra-small 6-pin DFN(PLP)1216-6G package which achieve the smallest possible footprint solution on boards where area is limited.

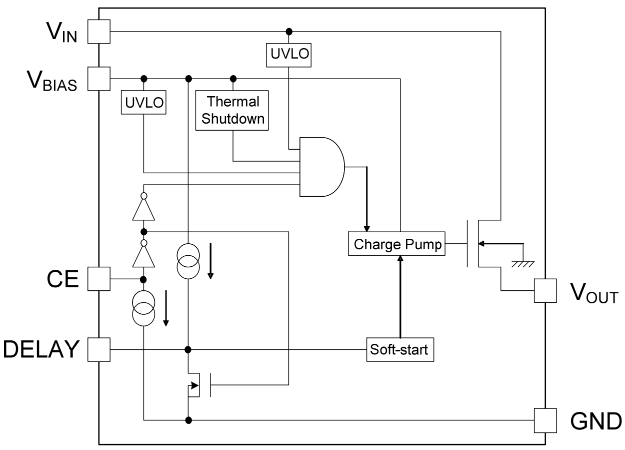
FEATURES

- Supply Current ·· Typ. 25 μA (I_{OUT} = 0 mA)
- Standby Current·· Typ. 0.01 μA
- V_{IN} Input Voltage Range······························ 0.6 V to 4.8 V
- V_{BIAS} Input Voltage Range ···························· 2.5 V to 5.5 V
- Switch ON Resistance································ Typ. 18 mΩ (V_{IN} = 1.0 V, V_{BIAS} = 5.0 V)
- Output Current·· Max. 3 A
- A single Nch MOSFET Circuit
- Soft-start Function
- Thermal Shutdown Circuit
- Auto-discharge Function (R5541K001D)
- Package·· DFN(PLP)1216-6G

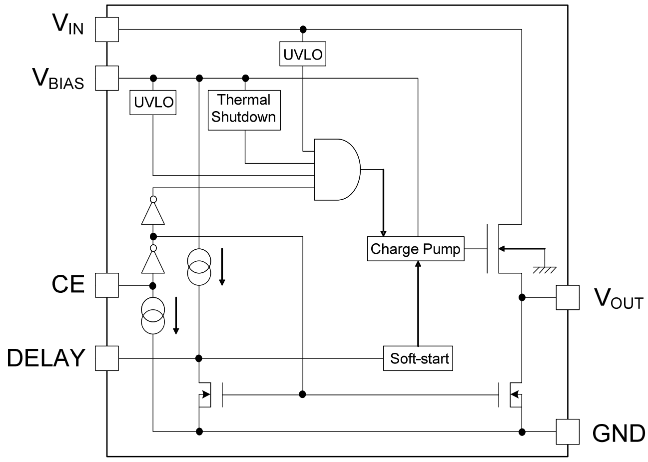
APPLICATIONS

- Secondary Power Source for hand-held communication equipments and laptop PCs

BLOCK DIAGRAMS



R5541K001B Block Diagram



R5541K001D Block Diagram

SELECTION GUIDE

The auto-discharge function[*1] is a user-selectable option.

Selection Guide

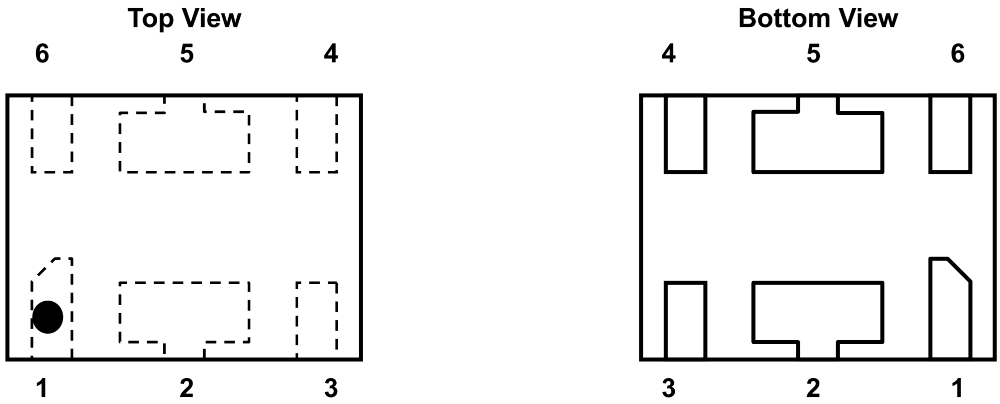
Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R5541K001∗-E2	DFN(PLP)1216-6G	5,000 pcs	Yes	Yes

∗: Specify the CE Pin Polarity and auto-discharge option.

B: Active-High, no auto-discharge function
D: Active-High, auto-discharge function

[*1] Auto-discharge function quickly lowers the output voltage to 0 V, when the chip enable signal is switched from the active mode to the standby mode, by releasing the electrical charge accumulated in the external capacitor.

PIN DESCRIPTION



DFN(PLP)1216-6G Pin Configurations

DFN(PLP)1216-6G Pin Description

Pin No.	Symbol	Description
1	CE	Chip Enable Pin (Active-High)
2	V_{IN}	Input Pin 2[*1]
3	V_{BIAS}	Input Pin 1[*1]
4	GND	Ground Pin
5	V_{OUT}	Output Pin
6	DELAY	DELAY Pin for Soft-start Setting

[*1] V_{IN} should be used as $V_{IN} \leq V_{BIAS}$.

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

Symbol	Item	Rating	Unit
V_{BIAS}	V_{BIAS} Pin Input Voltage	−0.3 to 6.0	V
V_{IN}	V_{IN} Pin Input Voltage	−0.3 to 5.5	V
V_{CE}	CE Pin Input Voltage	−0.3 to 6.0	V
V_{OUT}	V_{OUT} Pin Voltage	−0.3 to V_{IN}	V
I_{OUT}	Output Current	3.0	A
P_D	Power Dissipation (JEDEC STD.51-7 Test Land Pattern)*1	714	mW
Tj	Junction Temperature	−40 to 125	°C
Tstg	Storage Temperature Range	−55 to 125	°C

*1 Refer to *PACKAGE INFORMATION* for detailed information.

ABSOLUTE MAXIMUM RATINGS
Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)
All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

ELECTRICAL CHARACTERISTICS

V_{BIAS} = 5.0 V, V_{IN} = 1.0 V, C_{BIAS} = 1 μF, C_{IN} = none, C_{OUT} = 0.1 μF, unless otherwise noted.

The specifications surrounded by ☐ are guaranteed by Design Engineering at −40°C ≤ Ta ≤ 85°C.

R5541K Electrical Characteristics (Ta = 25°C)

Symbol	Item	Conditions		Min.	Typ.	Max.	Unit
V_{BIAS}	V_{BIAS} Pin Input Voltage			2.5		5.5	V
V_{IN}	V_{IN} Pin Input Voltage			0.6		4.8	V
R_{ON}	Switch ON Resistance	I_{OUT} = 500 mA			18	28	mΩ
I_{SS}	Supply Current	I_{OUT} = 0 mA, V_{BIAS} Pin			25	47	μA
Istandby	Standby Current	V_{CE} = 0 V, V_{IN} = 4.8 V, V_{BIAS} = 5.5 V	V_{BIAS} Pin		0.01	0.15	μA
			V_{IN} Pin		0.01	1	μA
UVLO	Undervoltage Lockout Voltage	V_{BIAS} Pin*1		2.0		2.49	V
		V_{IN} Pin*2		0.3		0.59	V
T_{TSD}	Thermal Shutdown Temperature	Junction Temperature			145		°C
T_{TSR}	Thermal Shutdown Release Temperature	Junction Temperature			125		°C
I_{CEPD}	CE Pull-down Current				0.4	0.8	μA
V_{CEH}	CE Input Voltage "H"			1.0			V
V_{CEL}	CE Input Voltage "L"					0.4	V
I_{DELAY}	DELAY Pin Current	*3		1.25	1.5	1.8	μA
R_{LOW}	Low Output Nch Tr. ON Resistance (R5541K001D)	V_{CE} = 0 V			80		Ω

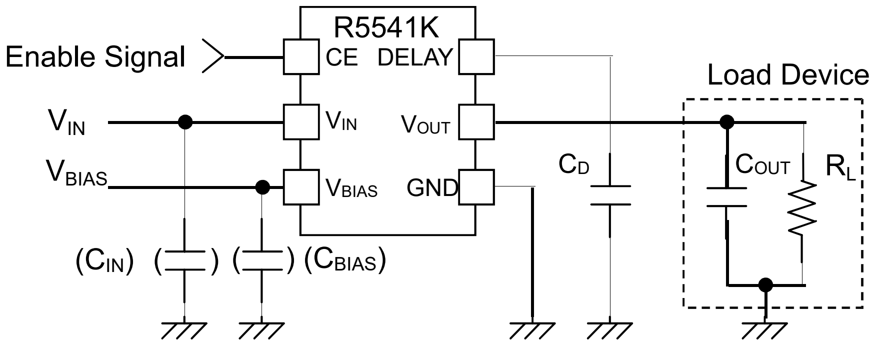
All test items listed under *ELECTRICAL CHARACTERISTICS* are done under the pulse load condition (Tj ≈ Ta = 25°C).

*1 The UVLO detector threshold and the UVLO release voltage are between the min and the max of UVLO with Typ. 90 mV hysteresis.

*2 The UVLO detector threshold and the UVLO release voltage are between the min and the max of UVLO with Typ. 70 mV hysteresis.

*3 Soft-start time can be adjusted by using I_{DELAY} and a capacitor (C_D). Refer to *Soft-start Function* in *TECHNICAL NOTES* for detailed Information.

TYPICAL APPLICATION



R5541K Typical Application

TECHNICAL NOTES

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed a rated voltage, a rated current or a rated power. When designing a peripheral circuit, please be fully aware of the following points.

- An input capacitor (C_{IN}) and a bypass capacitor (C_{BIAS}) are NOT necessarily required between the V_{IN} pin and GND. If there is a possibility that the parasitic element (inductance) of V_{IN} may generate spike noise, connect an appropriate capacitor (about 0.1 μF) between the V_{IN} pin and GND.
- V_{IN} and V_{BIAS} should always be used as $V_{IN} \leq V_{BIAS}$.
- Connect the DELAY pin to a capacitor (C_D) or leave the DELAY pin floating.

SOFT-START FUNCTION

Soft-start function maintains the smooth control of the output voltage to prevent an inrush current during start-up by adjusting the soft-start time (tstart) (V_{OUT} = 10% to 90%). tstart can be adjusted by connecting a capacitor (C_D) between the DELAY pin and GND. The calculation of C_D is as follows.

$$C_D \text{ [nF]} = 7.5 \times \text{tstart [ms]} \times I_{DELAY} \text{ [μA]} / V_{IN} \text{ [V]}$$

If C_D is not connected to the DELAY pin, leave the DELAY pin floating. If the DELAY pin is left floating, the calculation of the start-up time (tr) (V_{OUT} = 10% to 90%) is as follows.

$$\text{tr [ms]} = 0.04 \times V_{IN} \text{ [V] (Typ.)}$$

V_{BIAS}, V_{IN} and CE can be sequenced in any order; the device can start up with soft-start function.

PACKAGE INFORMATION

POWER DISSIPATION (DFN(PLP)1216-6G)

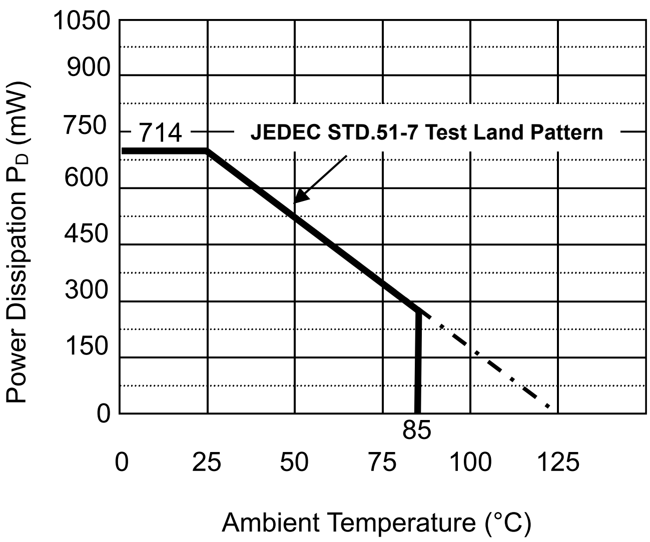
Power Dissipation (P_D) of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

Measurement Conditions

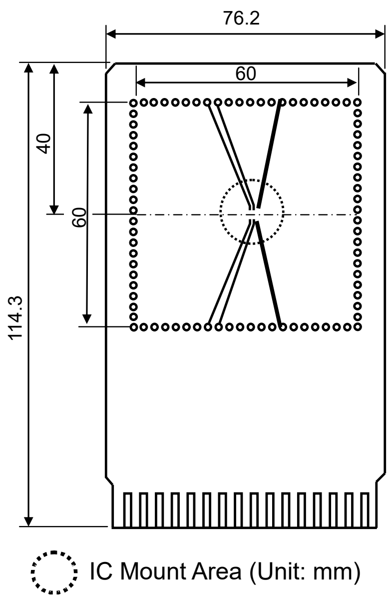
	JEDEC STD.51-7 Test Land Pattern
Environment	Mounting on Board (Wind Velocity = 0m/s)
Board Material	Glass Cloth Epoxy Plastic (4 Layer)
Board Dimensions	76.2 mm × 114.3 mm × 1.6 mm
Copper Ratio	Top side, Back side: 60 mm x 60mm, Approx.10% 2nd, 3rd layers: 74.2 mm x 74.2 mm, Approx. 100%
Through-holes	ϕ 0.85 mm x 44 pcs

Measurement Result (Ta = 25°C, Tjmax = 125°C)

	JEDEC STD.51-7 Test Land Pattern
Power Dissipation	714 mW
Thermal Resistance	θja = (125 − 25°C) / 0.714 W = 140°C/W
	θjc = 21°C/W



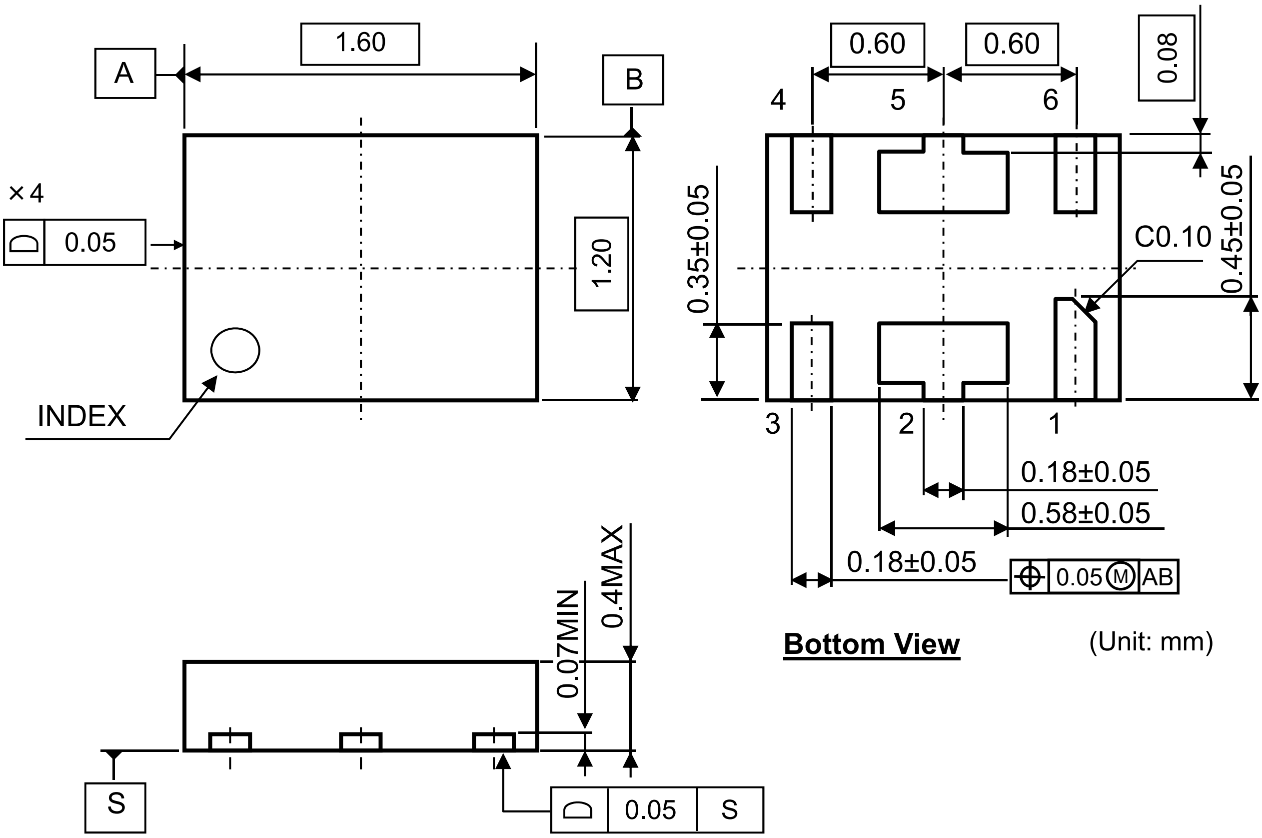
Ambient Temperature vs. Power Dissipation



Measurement Board Pattern

RICOH

PACKAGE DIMENSIONS (DFN(PLP)1216-6G)

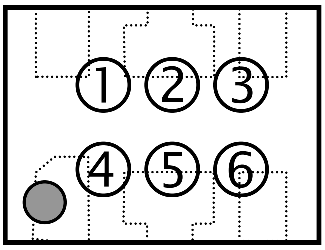


DFN(PLP)1216-6G Package Dimensions

MARK SPECIFICATION (DFN(PLP)1216-6G)

①②③④: Product Code …**Refer to *MARK SPECIFICATION TABLE DFN(PLP)1216-6G*.**

⑤⑥: Lot Number …Alphanumeric Serial Number



DFN(PLP)1216-6G Mark Specification

MARK SPECIFICATION TABLE (DFN(PLP)1216-6G)

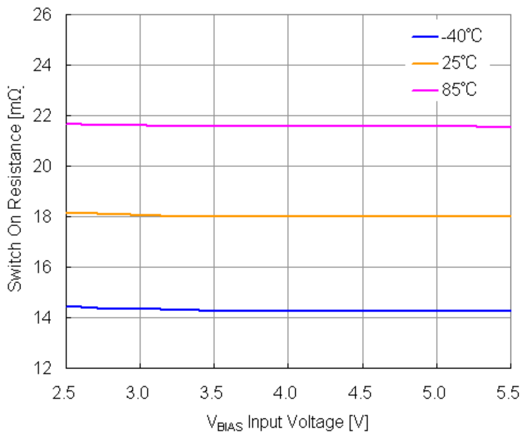
Mark Specification Table

Product Name	①②③④
R5541K001B	D Z 0 1
R5541K001D	D Z 0 3

TYPICAL CHARACTERISTICS

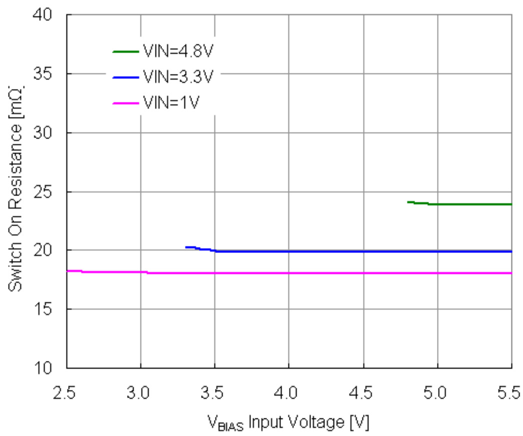
Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.

R5541K001x
V_{IN} = 1.0 V, I_{OUT} = 500 mA



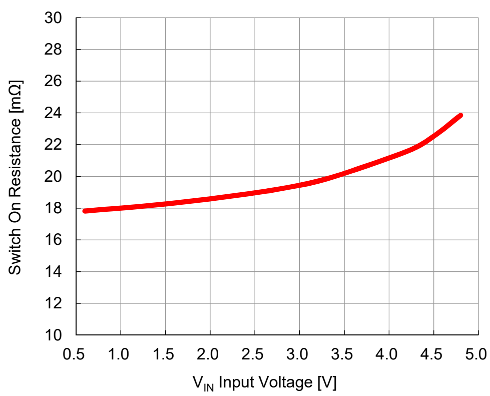
Switch On Resistance vs. V_{BIAS} Input Voltage

R5541K001x
I_{OUT} = 500 mA, Ta = 25°C



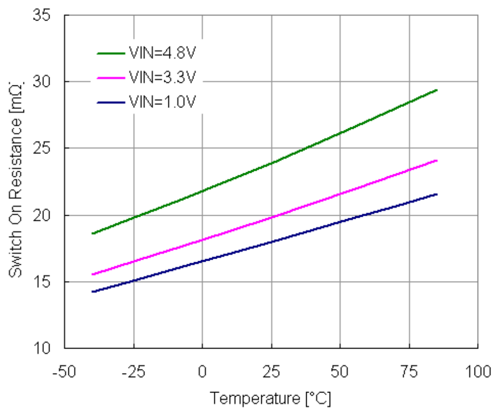
Switch On Resistance vs. V_{BIAS} Input Voltage

R5541K001x
V_{BIAS} = 5.0 V, I_{OUT} = 500 mA, Ta = 25°C



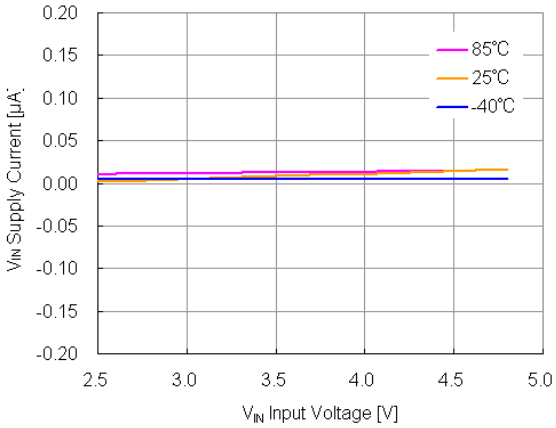
Switch On Resistance vs. V_{IN} Input Voltage

R5541K001x
V_{IN} = 1.0 V, V_{BIAS} = 5.0 V, I_{OUT} = 500 mA



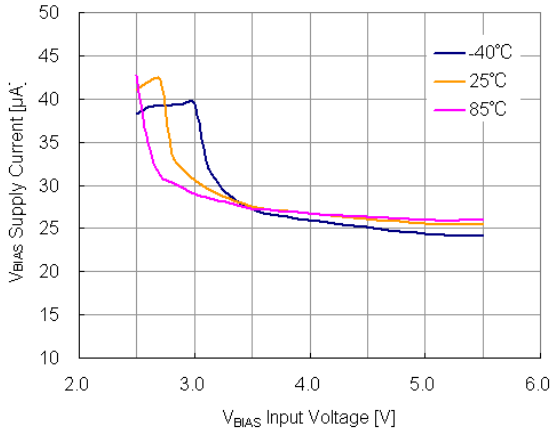
Switch On Resistance vs. Temperature

R5541K001x
V_{BIAS} = 5.5 V



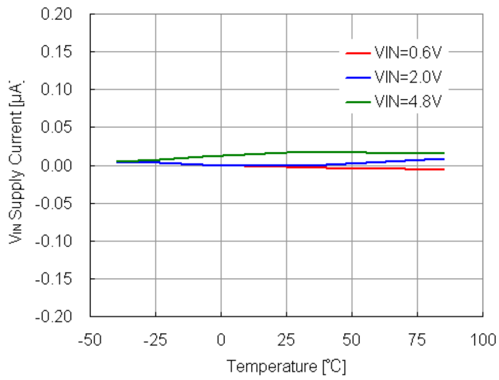
V_{IN} Supply Current vs. V_{IN} Input Voltage

R5541K001x
V_{IN} = 1.0 V



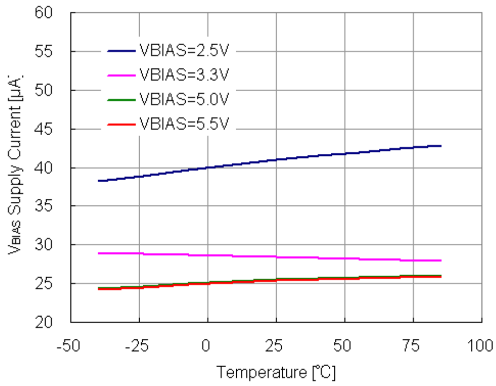
V_{BIAS} Supply Current vs. V_{BIAS} Input Voltage

R5541K001x
V_{BIAS} = 5.5 V



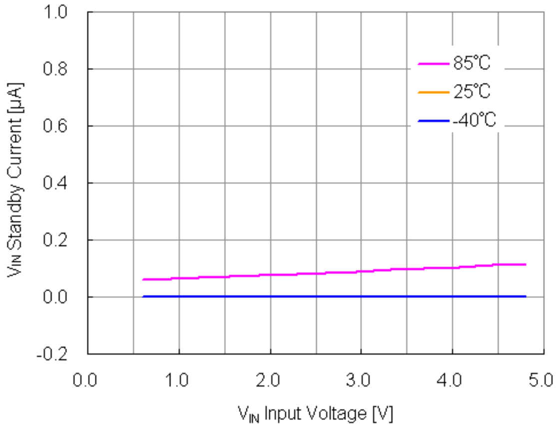
V_{IN} Supply Current vs. Temperature

R5541K001x
V_{IN} = 0.6 V



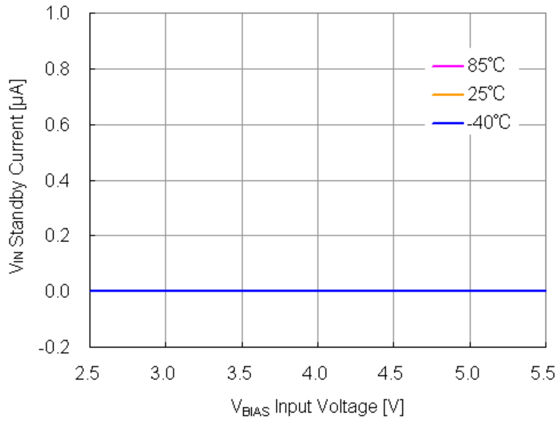
V_{BIAS} Supply Current vs. Temperature

R5541K001x
V_{BIAS} = 5.5 V



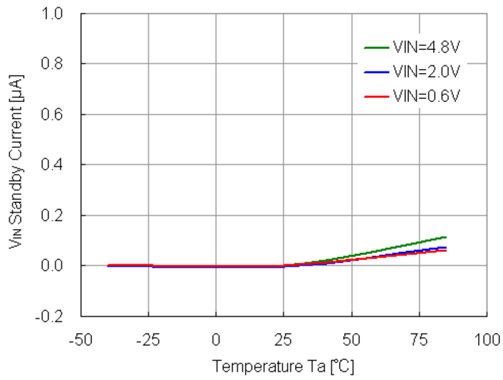
V_{IN} Standby Current vs. V_{IN} Input Voltage

R5541K001x
V_{IN} = 0.6 V



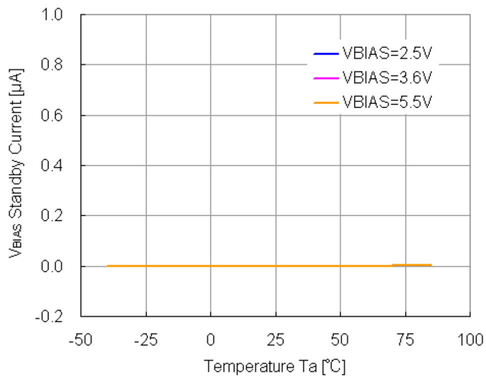
V_{IN} Standby Current vs. V_{BIAS} Input Voltage

R5541K001x
V_{BIAS} = 5.5 V



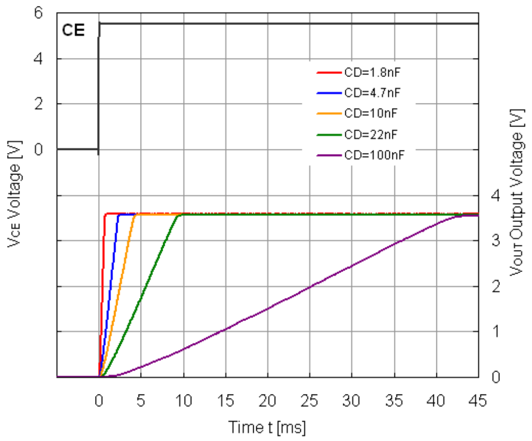
V_{IN} Standby Current vs. Temperature

R5541K001x
V_{IN} = 0.6 V



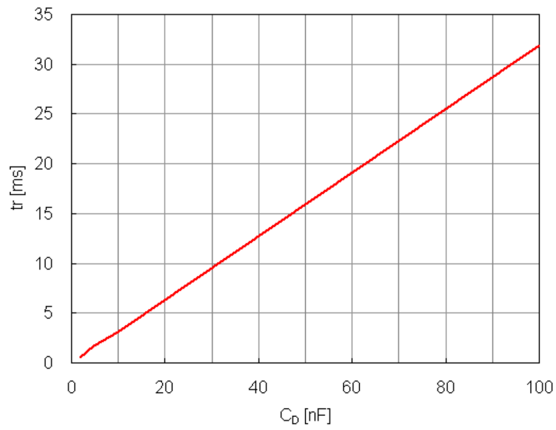
V_{BIAS} Standby Current vs. Temperature

R5541K001x
V_{IN} = 3.6 V, V_{BIAS} = 5.5 V, R_{LOAD} = 10 Ω, C_{OUT} = 0.1 μF



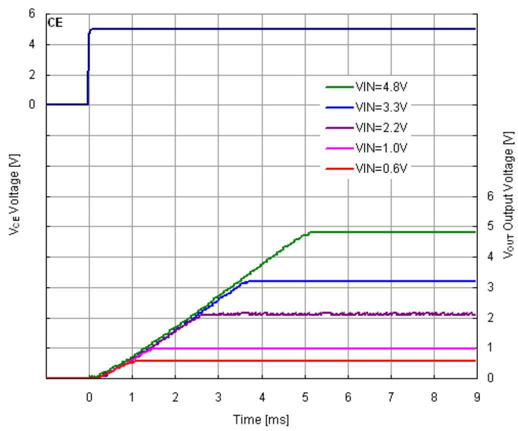
V_{OUT} Output Voltage On Time vs. DELAY Capacitance

R5541K001x
V_{IN} = 3.6 V, V_{BIAS} = 5.5 V, R_{LOAD} = 10 Ω, C_{OUT} = 0.1μF



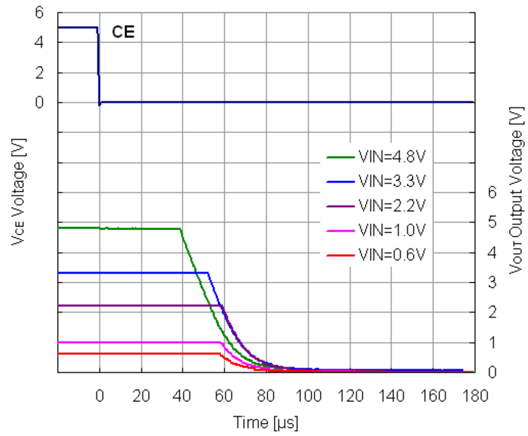
tr vs. DELAY Capacitance

R5541K001x
V_{BIAS} = 5.0 V, C_D = 10 nF, R_{LOAD} = 10 Ω, C_{OUT} = 0.1 μF



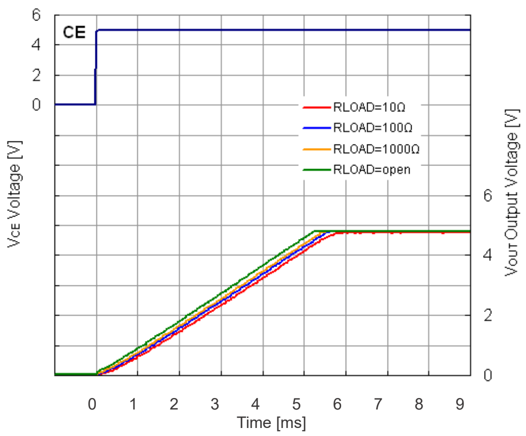
V_{OUT} Output Voltage On Time vs. V_{IN} Input Voltage

R5541K001D
V_{BIAS} = 5.0 V, C_D = 10 nF, C_{OUT} = 0.1 μF



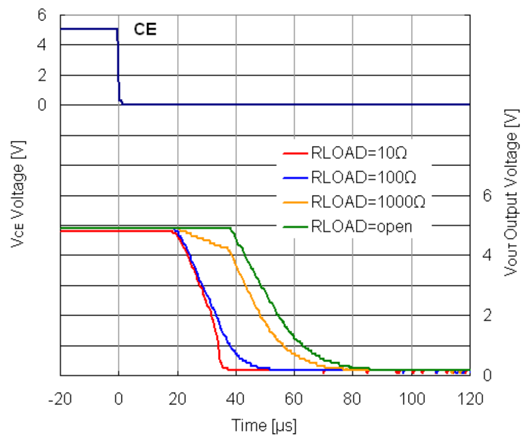
V_{OUT} Output Voltage Off Time vs. V_{IN} Input Voltage

R5541K001x
V_{IN} = 4.8 V, V_{BIAS} = 5.0 V, C_D = 10 nF, C_{OUT} = 0.1 μF



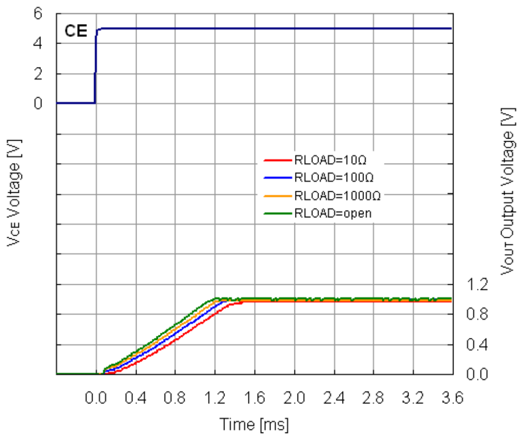
V_{OUT} Output Voltage On Time vs. Load Resistance

R5541K001D
V_{IN} = 4.8 V, V_{BIAS} = 5.0 V, C_D = 10 nF, C_{OUT} = 0.1 μF



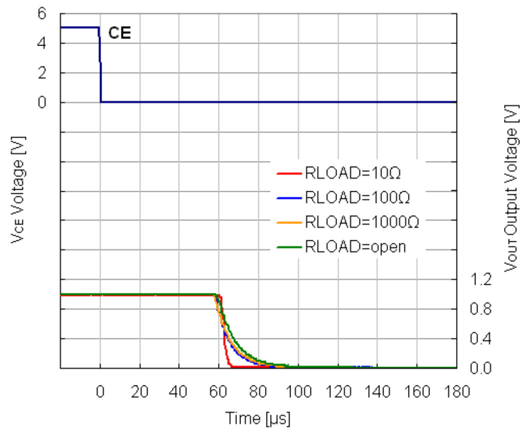
V_{OUT} Output Voltage Off Time vs. Load Resistance

R5541K001x
V_{IN} = 1.0 V, V_{BIAS} = 5.5 V, C_D = 10 nF, C_{OUT} = 0.1 µF



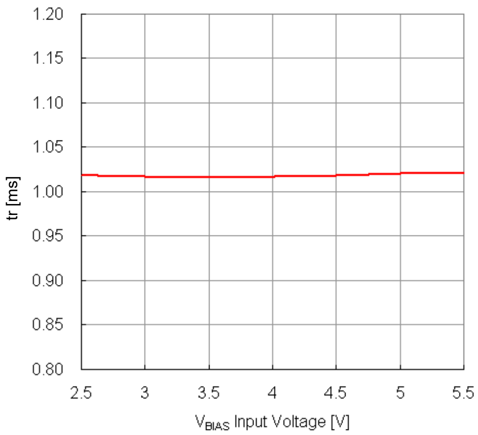
V_{OUT} Output Voltage On Time vs. Load Resistance

R5541K001D
V_{IN} = 1.0 V, V_{BIAS} = 5.5 V, C_D = 10 nF, C_{OUT} = 0.1 µF



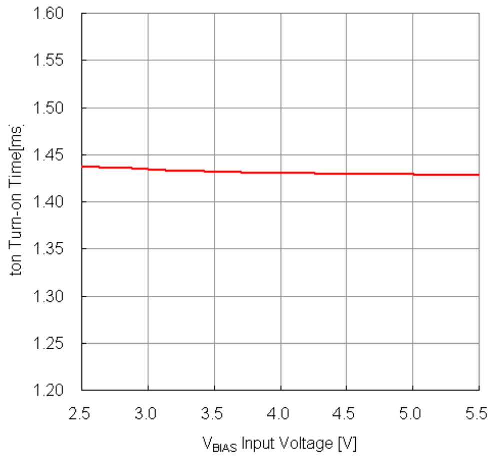
V_{OUT} Output Voltage Off Time vs. Load Resistance

R5541K001x
V_{IN} = 1.0 V, C_D = 10 nF, R_{LOAD} = 10 Ω, C_{OUT} = 0.1 µF



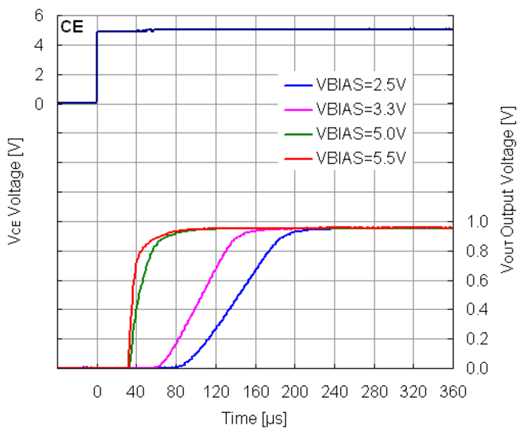
tr vs. V_{BIAS} Input Voltage

R5541K001x
V_{IN} = 1.0 V, C_D = 10 nF, R_{LOAD} = 10 Ω, C_{OUT} = 0.1 µF



ton Turn-on Time vs. V_{BIAS} Input Voltage

R5541K001x
V_{IN} = 1.0 V, R_{LOAD} = 10 Ω, C_{OUT} = 0.1 µF



V_{OUT} Output Voltage On Time vs. V_{BIAS} Input Voltage

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Halogen Free

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