RICOH |

R5560Z Series

High-Current/Overvoltage Protection Switch IC with Voltage Suppressor

No. EA-328-170517

OUTLINE

The R5560Z is a CMOS-based high-current and overvoltage protection switch IC with voltage suppressor that uses an NMOS pass transistor to achieve ultra-low on resistance (Typ. 38 m Ω). The R5560Z consists of a soft-start circuit, a startup debounce circuit, an overvoltage lockout (OVLO) circuit, and a thermal shutdown circuit.

The OVLO threshold is adjustable with optional external resistors to any voltage between 4 V and 20 V. The internal OVLO threshold (preset to 6.8 V $\pm 3\%$) is available when connecting the OVLO pin to GND. An internal clamp can protect low-voltage systems from surges up to ± 80 V (The surge waveform is compliant with IEC 61000-4-5 Combination Wave.) without using a transient-voltage-suppression (TVS) diode.

The R5560Z is offered in a small and thin WLCSP-12-P2 (1.288 mm x 1.828 mm) package which achieves the smallest possible footprint solution on boards where area is limited.

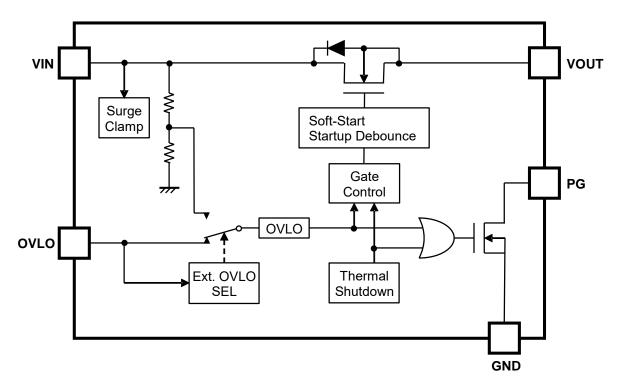
FEATURES

Input Voltage Range (Maximum Rating) · · · · · · 2.5 V to 28 V (29 V)
Surge Immunity ····· 80 V
Switch On Resistance · · · · Typ.38 m Ω
Input Supply Current ······Typ. 19 μA
Internal Fixed Preset OVLO Threshold ···················6.8 V±3%
Adjustable OVLO Threshold with OVLO Pin
Adjustable OVLO Threshold Range······· 4 V to 20 V
Power Good (PG) Function
Soft-start Function
Internal Startup Debounce · · · · Typ.15 ms
Thermal Shutdown Protection · · · · Typ.150°C
Package·····WLCSP-12-P2

APPLICATIONS

- Smartphones
- Tablet PCs
- Mobile Internet Devices

BLOCK DIAGRAM



R5560Z Block Diagram

SELECTION GUIDE

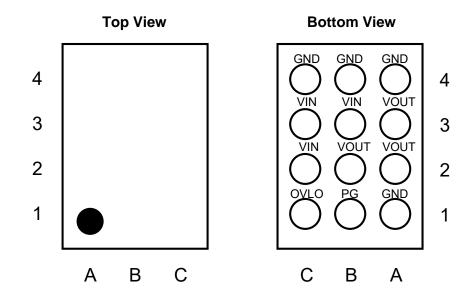
Selection Guide

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R5560Zxx1A-TL-F	WLCSP-12-P2	5,000 pcs	Yes	Yes

xx: Designate the internal OVLO threshold.

01: 6.8 V

PIN DESCRIPTION



WLCSP-12-P2 Pin Configuration

WLCSP-12-P2 Pin Description

Pin No	Symbol	Pin Description	
A1, A4, B4, C4	GND (1)	Ground pins.	
A2, A3, B2	VOUT (2)	Output pins.	
B1	PG	Power Good output pin. (Nch open drain) PG is driven low after input voltage is stable between minimum V_{IN} and $V_{\text{IN}_\text{OVLO}}$ after startup debounce except during thermal shutdown operation.	
B3, C2, C3	VIN (3)	Input pins.	
C1	OVLO	External OVLO adjustment pin. Connect OVLO to GND when using the internal threshold. Connect a resistor-divider to OVLO to set a different OVLO threshold.	

 $^{^{(1)}\}mbox{Connect}$ the pins that have the same symbols together: A1, A4, B4 and C4

⁽²⁾ Connect the pins that have the same symbols together: A2, A3 and B2 (3) Connect the pins that have the same symbols together: B3, C2 and C3

R5560Z

No. EA-328-170517

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

Symbol	Item	Rating	Unit
Vin	Input Voltage	-0.3 to 29	V
V _{OUT}	Output Voltage	-0.3 to V _{IN} + 0.3	V
Vovlo	OVLO Pin Input Voltage	-0.3 to 24	V
V_{PG}	PG Pin Voltage	-0.3 to 6.5	V
l _{PG}	PG Pin Current	14	mA
Іоит	Continuous Output Current	4.5	Α
P _D	Power Dissipation (1)	1000	mW
Та	Operating Temperature Range	-40 to 85	°C
Tstg	Storage Temerature	-55 to 125	°C

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

⁽¹⁾ Refer to PACKAGE INFORMATION for detailed information.

ELECTRICAL CHARACTERISTICS

 V_{IN} = 2.5 V to 28 V, I_{OUT} = 1 mA, C_{IN} = 0.1 μ F, unless otherwise noted. Typical values are V_{IN} = 5 V, Ta = 25°C.

The specifications surrounded by are guaranteed by design engineering at -40°C to 85°C.

R5560Z Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
VIN	Input Voltage			2.5		28	V
VIN_CLAMP	Input Clamp Voltage	I _{IN} = 10 mA			33		٧
I _{IN}	Input Supply Current	V _{IN} = 5 V, I _{OUT} = 0	mA		19	50	μA
I _{IN_OVLO}	OVLO Supply Current	V _{IN} = 5 V, V _{OUT} = 0) V, V _{OVLO} = 3 V		16	50	μA
Ron	On Resistance	V _{IN} = 5 V, I _{OUT} = 1	A, Ta = 25°C		38	53	mΩ
V	Internal Fixed Preset OVLO Threshold	.,	V _{IN} rising	6.6	6.8	7.0	V
VIN_OVLO		V _{OVLO} = 0 V	V _{IN} falling	6.5			V
Vovlo_sel	External OVLO Select Threshold			0.2	0.25	0.3	V
V _{OVLO_TH}	OVLO set Threshold			1.18	1.22	1.26	V
VIN_OVLO	Adjustable OVLO Threshold Range (1)			4		20	V
lovLo	OVLO Input Leakage			-100		100	nA
Vol	PG Output Low Voltage	Isink = 1 mA				0.4	V
V _{PG_LEAK}	PG Leakage Current	V _{IO} = 3.3 V		-1		1	μΑ
t _{DEB}	IN Debounce Time	2.5 V < V _{IN} < V _{IN_OVLO} to V _{OUT} = 10% of V _{IN}			15		ms
ton	Turn-On Time during Soft-Start	$V_{IN} = 5 \text{ V},$ $R_{LOAD} = 100 \Omega, C_{OUT} = 100 \text{ uF},$ $V_{OUT} = 10\% \text{ of } V_{IN} \text{ to } 90\% \text{ of } V_{IN}$			2		ms
t _{OFF}	Turn-Off Time	V_{IN} > V_{OVLO} , 2 V/μs to V_{OUT} = 80% of V_{IN} , R_{LOAD} = 100 Ω			2		μs
Соит	OUT Load Capacitance					1000	μF
T _{SHDN}	Thermal Shutdown				150		°C
T _{HYST}	Thermal Shutdown Hysteresis				20		°C

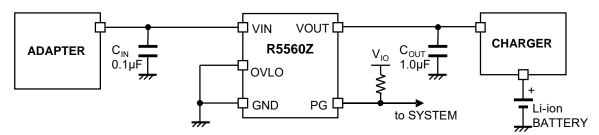
All test items listed under ELECTRICAL CHARACTERISTICS are done under the pulse load condition (Tj \approx Ta = 25°C) except Adjustable OVLO Threshold Range, Turn-On Time during Soft-Start, Turn-Off Time and OUT Load Capacitance.

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

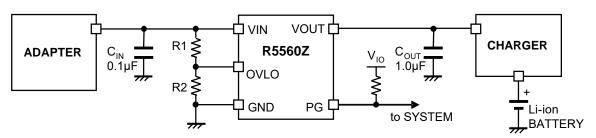
All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

⁽¹⁾ Refer to TECHNICAL NOTES for details.

APPLICATION INFORMATION



Internal Fixed Preset OVLO Typical Application



External Adjustable OVLO Typical Application

TECHNICAL NOTES

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed a rated voltage, a rated current or a rated power. When designing a peripheral circuit, please be fully aware of the following points.

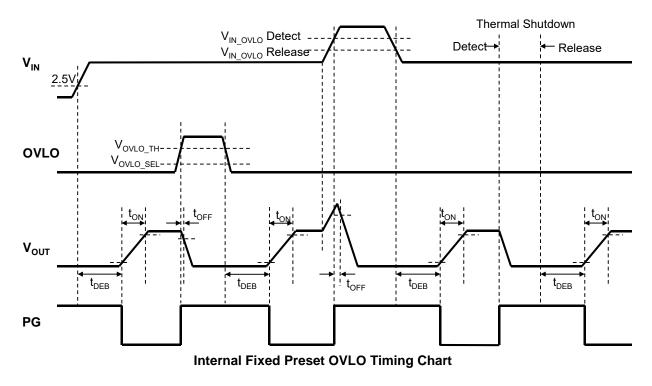
- Set the OVLO pin input voltage to or below the external OVLO select threshold (Typ. 0.25 V) when using the internal fixed preset OVLO threshold (preset to 6.8 V ±3%). Connecting the OVLO pin to the GND pin without using R1 and R2 is recommended. Don't leave the OVLO pin the floating state.
- External resistors R1 and R2 are required in order to adjust the OVLO threshold. The formula to calculate the OVLO threshold is as follow. Adjustable OVLO threshold range is between 4 V and 20 V.

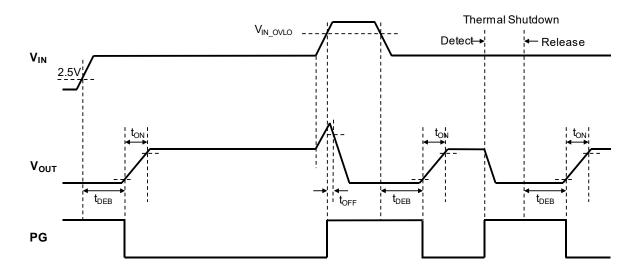
$$V_{IN_OVLO} = V_{OVLO_TH} \times \left(1 + \frac{R1}{R2}\right)$$

The appropriate value for reducing current consumption is R1 = 1 $M\Omega$.

 If the voltage at the V_{OUT} is larger than the V_{IN}, large currents may flow and can cause permanent damage to the device. The R5560Z is designed to control a current flow from V_{IN} to V_{OUT}.

TIMING CHART





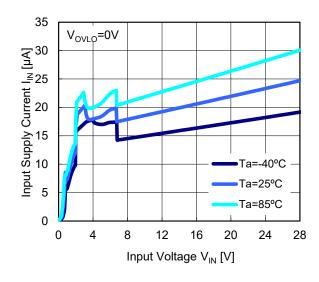
External Adjustable OVLO Timing Chart

TYPICAL CHARACTERISTICS

Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.

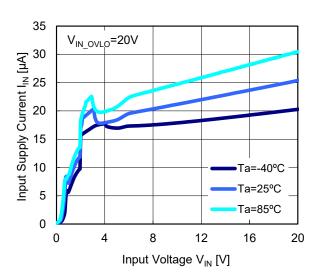
1) Input Supply Current vs. Input Voltage

 $(V_{IN_OVLO} = 6.8 V)$

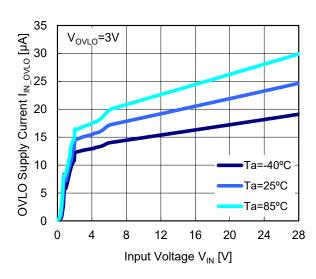


2) Input Supply Current vs. Input Voltage

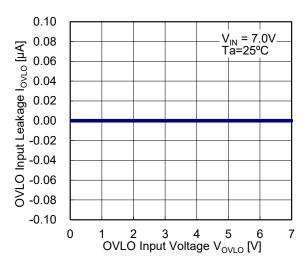
 $(V_{IN_OVLO} = 20 V)$



3) OVLO Supply Current vs. Input Voltage



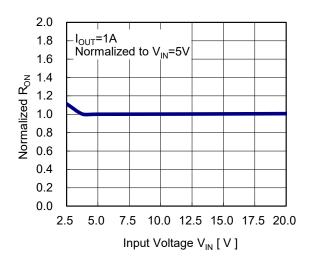
4) OVLO Input leakage vs. OVLO Input Voltage



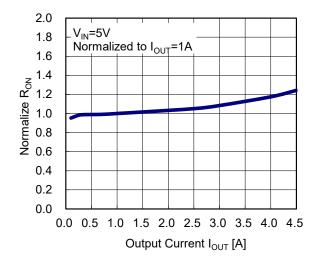
5) Normalized On Resistance vs. Temperature

2.0 V_{IN}=5V 1.8 I_{OUT}=1A 1.6 Normalized to Ta=25°C Normalized RoN 1.4 1.2 1.0 8.0 0.6 0.4 0.2 0.0 -40 -15 10 35 60 85 Temperature Ta [°C]

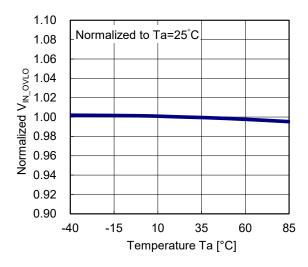
6) Normalized On Resistance vs. Input Voltage



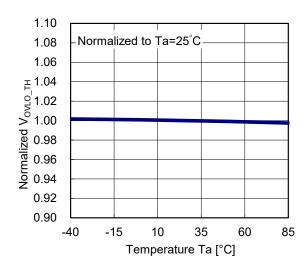
7) Normalized On Resistance vs. Output Current



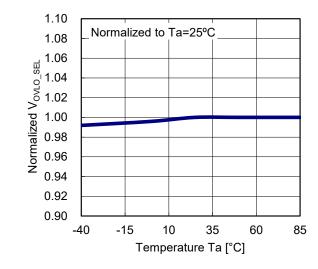
8) Normalized Internal Fixed Preset OVLO Threshold vs. Temperature



9) Normalized OVLO set Threshold vs. Temperature



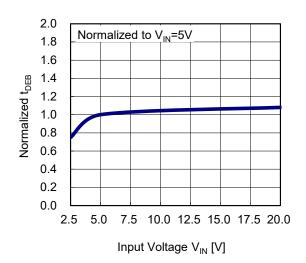
10) Normalized External OVLO Select Threshold vs. Temperature



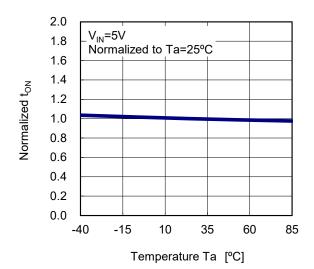
11) Normalized Debounce Time vs. Temperature

2.0 1.8 1.6 1.6 1.4 1.2 1.0 0.8 0.6 0.4 0.2 0.0 -40 -15 10 35 60 85 Temperature Ta [°C]

12) Normalized Debounce Time vs. Input Voltage



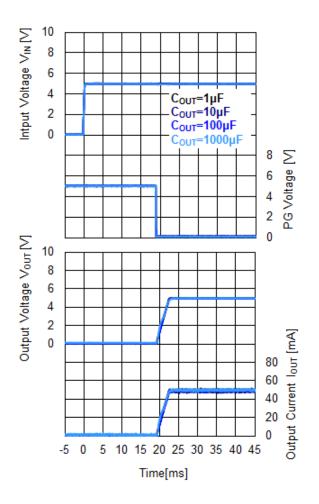
13) Normalized Turn On Time during Soft-Start vs. Temperature

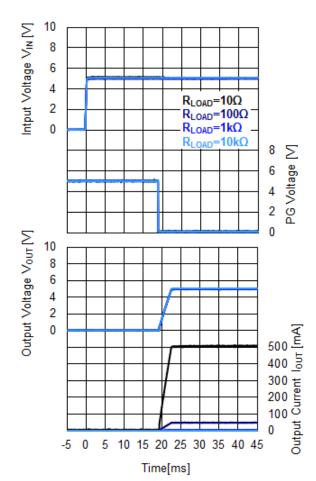


14) Power-Up Response (C_{OUT} dependence)

15) Power-Up Response (R_{LOAD} dependence)

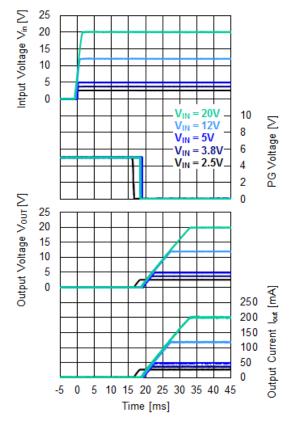
 R_{LOAD} = 100 Ω , V_{IO} = 5.0 V, PG pull up resistance = 100 k Ω C_{OUT} = 1 μ F, V_{IO} = 5.0 V, PG pull up resistance = 100 k Ω





16) Power-Up Response (V_{IN} dependence)

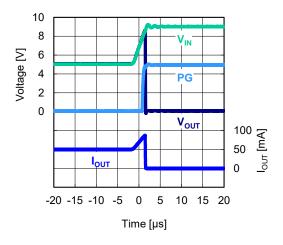
 $C_{OUT} = 1~\mu F,~R_{LOAD} = 100~\Omega,~V_{IO} = 5.0~V,$ PG pull up resistance = 100 k Ω



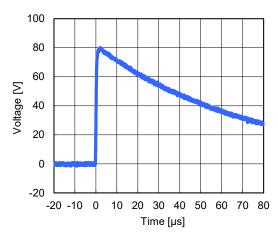
17) OVLO Response

 $V_{OVLO} = 0 V (V_{IN_OVLO} = 6.8 V),$

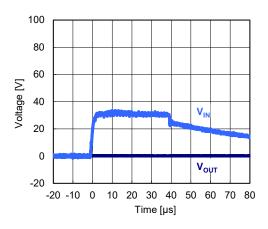
 R_{LOAD} = 100 Ω , V_{IO} = 5.0 V, PG pull up resistance = 100 k Ω



18) Surge Suppression



80 V Surge Test Waveform



With 5560Z

Ver. A

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

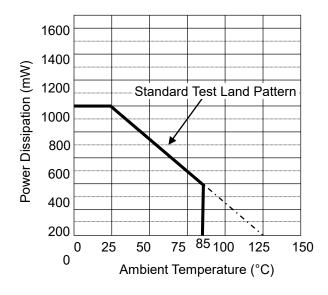
Measurement Conditions

	Standard Test Land Pattern		
Environment	Mounting on Board (Wind Velocity = 0 m/s)		
Board Material	ial Glass Cloth Epoxy Plastic (Double-Sided Board)		
Board Dimensions	ons 40 mm × 40 mm × 1.6 mm		
Copper Ratio	Top Side: Approx. 80%		
	Bottom Side: Approx. 90%		
Through-holes	φ 0.6 mm × 31 pcs		

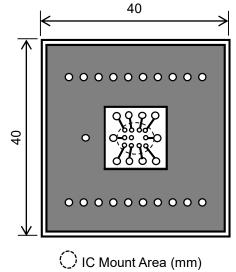
Measurement Result

 $(Ta = 25^{\circ}C, Tjmax = 125^{\circ}C)$

	Standard Test Land Pattern
Power Dissipation	1000 mW
Thermal Resistance	θja = (125 – 25°C) / 1.0 W = 100°C/W



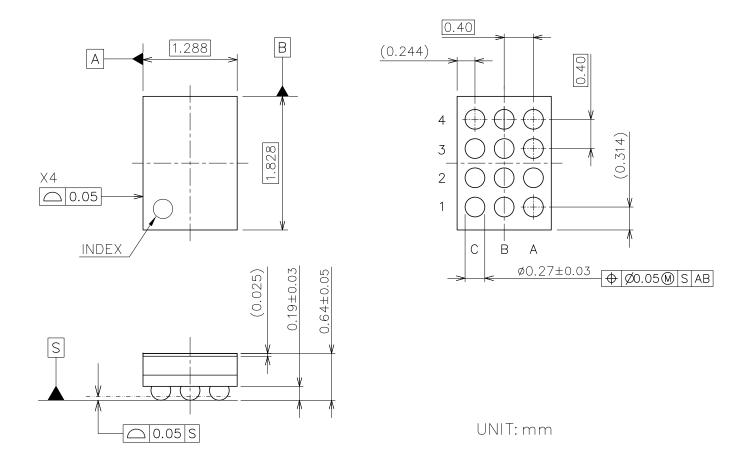
Power Dissipation vs. Ambient Temperature



, ,

Measurement Board Pattern

Ver. A



WLCSP-12-P2 Package Dimensions



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