RICOH

RP106x SERIES

0.8% ACCURACY 0.7V OUTPUT 400mA LDO REGULATOR

NO.EA-180-200325

OUTLINE

The RP106x Series are low voltage 400mA voltage regulator. These ICs had been further improved of low-voltage capability compared with previous low-voltage product.

The input voltage is as low as Min. 1.0V and the output voltage can be set from 0.7V. The output voltage accuracy has been improved to $\pm 0.8\%$ and due to a built-in transistor with low on-resistance of 0.55 Ω (at Vout=1.5V).

Each of these ICs consists of a voltage reference unit, an error amplifier, a resistor-net for voltage setting, and a current limit circuits for over-current for the destruction prevention by the over-current.

The CE pin can switch the regulator to standby mode. In addition to SOT-23-5 and SC-88A packages, a 0.69mm square WLCSP-4-P5 package and a 1.2mm square DFN(PLP)1212-6 are also available.

FEATURES

- Supply CurrentTyp. 48μA
- Supply Current (Standby)......Typ. 0.1µA
- Ripple RejectionTyp. 60dB (f=10kHz)
- Input Voltage Range1.0V to 3.6V
- Output Voltage Range......0.7V to 1.8V (0.1V steps)
 - (For other voltages, please refer to MARK INFORMATIONS.)
- Output Voltage Accuracy......±0.8% (Vou⊤≥1.0V, Topt=25°C)
- Temperature-Drift Coefficient of Output Voltage ... Typ. ±60ppm/°C
- Dropout VoltageTyp. 0.22V (Vout=1.5V)
- Line RegulationTyp. 0.10%/V
- PackagesWLCSP-4-P5, DFN(PLP)1212-6, SC-88A, SOT-23-5
- Built-in Fold Back Protection Circuit Typ. 110mA (Current at short mode)
- Built-in Constant Slope Circuit
- Ceramic capacitors are recommended to be used with this IC1.0 μ F or more

APPLICATIONS

- Power source for portable communication equipment.
- Power source for electrical appliances such as cameras, VCRs and camcorders.
- Power source for battery-powered equipment.

NO.EA-180-200325

BLOCK DIAGRAMS



SELECTION GUIDE

The output voltage, auto discharge function, package, and the taping type, etc. for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
RP106Zxx1*-TR-F	WLCSP-4-P5	5,000 pcs	Yes	Yes
RP106Kxx1*-TR	DFN(PLP)1212-6	5,000 pcs	Yes	Yes
RP106Qxx2*-TR-FE	SC-88A	3,000 pcs	Yes	Yes
RP106Nxx1*-TR-FE	SOT-23-5	3,000 pcs	Yes	Yes

xx : Setting Output Voltage (Vout) :

Fixed Type: 07 to 18 Stepwise setting with 0.1V increment in the range from 0.7V to 1.8V Exception: 1.25V=RP106x12x*5-xx 1.85V=RP106x18x*5-xx

* : Designation of Active Type:

B:"H" Active, without auto discharge function at off state.

D:"H" Active, with auto discharge function at off state.

NO.EA-180-200325

PIN CONFIGURATIONS



5

1

4

3

(mark side)

2



PIN DESCRIPTIONS

• WLCSP-4-P5

Pin No.	Symbol	Description	
A1	Vdd	Input Pin	
A2	Vout	Output Pin	
B1	CE	Chip Enable Pin ("H" Active)	
B2	GND	Ground Pin	

• SC-88A

Pin No.	Symbol Description	
1	CE	Chip Enable Pin ("H" Active)
2 *	NC	No Connection
3	GND	Ground Pin
4	Vout	Output Pin
5	Vdd	Input Pin

* Pin No. 2 is connected to the bottom of the IC. It is recommended that the pin be connected to the ground plane on the board, or otherwise be left floating so that there is no contact with other potentials.

• DFN(PLP)1212-6

Pin No.	Symbol	Description	
1	NC	No Connection	
2	GND	Ground Pin	
3	CE	Chip Enable Pin ("H" Active)	
4	Vdd	Input Pin	
5	NC	No Connection	
6	Vout	Output Pin	

• SOT-23-5

Pin No.	Symbol	Description
1	Vdd	Input Pin
2	GND	Ground Pin
3	CE	Chip Enable Pin ("H" Active)
4	NC	No Connection
5	Vout	Output Pin

• DFN(PLP)1212-6

NO.EA-180-200325

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
Vin	Input Voltage	4.0	V
Vce	Input Voltage (CE Pin)	-0.3 to 4.0	V
Vout	Output Voltage	-0.3 to VIN+0.3	V
Ιουτ	Output Current	500	mA
	Power Dissipation (WLCSP-4-P5)*	278	
5	Power Dissipation (DFN(PLP)1212-6)*	400	~\\/
PD	Power Dissipation (SC-88A)*	380	TTIVV
	Power Dissipation (SOT-23-5)*	420	
Topt	Operating Temperature Range	-40 to 85	٥C
Tstg	Storage Temperature Range	-55 to 125	°C

*) For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause permanent damage and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

NO.EA-180-200325

ELECTRICAL CHARACTERISTICS

• RP106x

 $V_{\text{IN}} = Set \text{ V}_{\text{OUT}} + 1V \text{, I}_{\text{OUT}} = 1mA, \text{ C}_{\text{IN}} = C_{\text{OUT}} = 1\mu F \text{, unless otherwise noted.}$

The specifications surrounded by \square are guaranteed by Design Engineering at $-40^{\circ}C \le T_{opt} \le 85^{\circ}C$.

						Т	opt=25°C
Symbol	ltem	Conditior	IS	Min.	Тур.	Max.	Unit
		T . 0500	Vou⊤ ≥ 1.0V	×0.992		×1.008	V
Maria			Vout < 1.0V	-8		8	mV
VOUT	Output voltage	40° C < T _{ant} < 95°C	Vou⊤ ≥ 1.0V	×0.983		×1.017	V
		-40 C \leq Topt \leq 05 C	Vout < 1.0V	-17		17	mV
Ιουτ	Output Current			400			mA
ΔV out/ ΔI out	Load Regulation	$1mA \le I_{\text{OUT}} \le 400mA$			25	45	mV
VDIF	Dropout Voltage		Refer to the fol	lowing tal	ble		
lss	Supply Current	Iout=0mA			48	75	μA
Istandby	Supply Current (Standby)	Vce=0V			0.1	8.0	μA
ΔV out/ ΔV in	Line Regulation	$\label{eq:setVout+0.5V} \begin{split} & \text{Set V}_{\text{OUT}} + 0.5 \text{V} \leq \text{V}_{\text{IN}} \leq 3.6 \text{V} \\ & \text{In case that Set V}_{\text{OUT}} < 0.8 \text{V}, \\ & 1.3 \text{V} \leq \text{V}_{\text{IN}} \leq 3.6 \text{V} \end{split}$			0.10	0.25	%/V
RR	Ripple Rejection	f=10kHz, Ripple 0.2Vp-p ViN=Set Vout+1V, Iout=30mA			60		dB
Vin	Input Voltage			1.0		3.6	V
ΔV out/ ΔT opt	Output Voltage Temperature Coefficient	$-40^{\circ}C \le T_{opt} \le 85^{\circ}C$			±60		ppm ∕°C
lsc	Short Current Limit	Vout=0V			110		mA
IPD	CE Pull-down Current				0.38	0.7	μA
Vсен	CE Input Voltage "H"			0.9			V
VCEL	CE Input Voltage "L"					0.4	V
en	Output Noise	BW=10Hz to 100kHz lour=30mA, Vour=0.7V			30		μVrms
RLOW	Low Output Nch Tr. ON Resistance (of D version)	VIN=2.0V, VCE=0V			43		Ω

All test items listed under Electrical Characteristics are done under the pulse load condition (Tj≈Ta=25°C) except Output Noise, Ripple Rejection and Output Voltage Temperature Coefficient items.

Topt=25°C

RP106x

NO.EA-180-200325

The specifications surrounded by are guaranteed by Design Engineering at $-40^{\circ}C \le T_{opt} \le 85^{\circ}C$.

• Dropout Voltage by Output Voltage

Output Voltage	Dropout Voltage VDIF (V)			
Vouт (V)	Condition	Тур.	Max.	
$0.7 \leq V_{\text{OUT}} < 0.8$	Iout=400mA	0.48	0.62	
$0.8 \leq V_{\text{OUT}} < 0.9$		0.40	0.54	
$0.9 \leq V_{\text{OUT}} < 1.0$		0.36	0.47	
$1.0 \leq V_{OUT} < 1.2$		0.32	0.45	
1.2 ≤ Vout < 1.5		0.28	0.38	
1.5 ≤ Vо∪т		0.22	0.31	

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

NO.EA-180-200325

TYPICAL APPLICATION





MURATA: GRM155B31A105KE15

TECHNICAL NOTES

When using these ICs, consider the following points:

Phase Compensation

In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, use a capacitor C2 with 1.0μ F or more.

If a tantalum capacitor is used, and its ESR (Equivalent Series Resistance) of C2 is large, the loop oscillation may result. Because of this, select C2 carefully considering its frequency characteristics.

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is high, noise pickup or unstable operation may result. Connect a capacitor C1 with a capacitance value as much as 1.0μ F or more between V_{DD} and GND pin, and as close as possible to the pins.

Set external components, especially the output capacitor C2, as close as possible to the ICs, and make wiring as short as possible.

Impedance of Input pin

CE pull-down constant current circuit is built in the RP106x.

However, if the CE pin is floating and the wiring is long, the malfunction may occur by noise. Therefore, fully evaluation on the actual PCB is necessary.

NO.EA-180-200325

CONSTANT SLOPE CIRCUITS

The RP106x Series is equipped with a constant slope circuit as a soft-start circuit, which allows the output voltage to start up gradually when the CE is turned on.

The constant slope circuit minimizes the inrush current at the start-up and also prevents the overshoot of the output voltage.

The capacitor to create the start-up slope is built in the IC that does not require any external components. The start-up time and the start-up slope angle are fixed inside the IC. For more details, please refer to the graph 15 of "Inrush Current Characteristics Example".





NO.EA-180-200325

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PACKAGE INFORMATION

• Power Dissipation (WLCSP-4-P5)

Power Dissipation (P_D) depends on conditions of mounting on board. This specification is based on the measurement at the condition below:

* Measurement Conditions

	Standard Land Pattern	
Environment	Mounting on Board (Wind velocity=0m/s)	
Board Material	Glass cloth epoxy plastic (Double sided)	
Board Dimensions	40mm × 40mm × 1.6mm	
Copper Ratio	Top side : Approx. 50% , Back side : Approx. 50%	
Through-hole	ϕ 0.5mm $ imes$ 28pcs	

* Measurement Result	(Ta=25°C, Tjmax=125°C)	
	Standard Land Pattern	
Power Dissipation	278mW	
Thermal Resistance	θja = (125–25°C)/0.278W = 360°C/W	
	θ jc = 46°C/W	



RICOH

NO.EA-180-200325



• Mark Specification (WLCSP-4-P5)

0 0 : Lot No. Alphanumeric serial number.



NO.EA-180-200325

• RP106Z Series marking list table (WLCSP-4-P5)

RP106ZxxxB	
Product Name	VSET
RP106Z071B	0.7V
RP106Z081B	0.8V
RP106Z091B	0.9V
RP106Z101B	1.0V
RP106Z111B	1.1V
RP106Z121B	1.2V
RP106Z131B	1.3V
RP106Z141B	1.4V
RP106Z151B	1.5V
RP106Z161B	1.6V
RP106Z171B	1.7V
RP106Z181B	1.8V
RP106Z121B5	1.25V
RP106Z181B5	1.85V

RP106ZxxxD **Product Name** VSET RP106Z071D 0.7V RP106Z081D 0.8V RP106Z091D 0.9V RP106Z101D 1.0V RP106Z111D 1.1V RP106Z121D 1.2V RP106Z131D 1.3V RP106Z141D 1.4V RP106Z151D 1.5V RP106Z161D 1.6V RP106Z171D 1.7V RP106Z181D 1.8V RP106Z121D5 1.25V RP106Z181D5 1.85V

NO.EA-180-200325

• Power Dissipation (DFN(PLP)1212-6)

Power Dissipation (P_D) depends on conditions of mounting on board. This specification is based on the measurement at the condition below:

Measurement Conditions

	Standard Test Land Pattern	
Environment	Mounting on Board (Wind velocity=0m/s)	
Board Material	Glass cloth epoxy plastic (Double sided)	
Board Dimensions	40mm×40mm×1.6mm	
Copper Ratio	Top side: Approx. 50%, Back side: Approx. 50%	
Through-holes	φ 0.54mm×28pcs	

Measurement Result		(Ta=25°C, Tjmax=125°C)
		Standard Test Land Pattern
	Power Dissipation	400mW
	Thermal Resistance	θja=(125-25°C)/0.4W=250°C/W
		θjc=67 °C/W





Measurement Board Pattern



NO.EA-180-200325

• Package Dimensions (DFN(PLP)1212-6)



• Mark Specification (DFN(PLP)1212-6)

- O ? Product Code.......Refer to the marking list table
- 3 4 : Lot No.....Alphanumeric serial number.



NO.EA-180-200325

• RP106K Series marking list table (DFN(PLP)1212-6)

12	VSET
NA	0.7V
NB	0.8V
NC	0.9V
ND	1.0V
NE	1.1V
NF	1.2V
NG	1.3V
NH	1.4V
NJ	1.5V
NK	1.6V
NL	1.7V
NM	1.8V
NP	1.25V
NQ	1.85V
	 ①② NA NB NC ND NE NF NG NH NJ NK NL NM NP NQ

RP106KxxxD

12	Vset
PA	0.7V
PB	0.8V
PC	0.9V
PD	1.0V
PE	1.1V
PF	1.2V
PG	1.3V
PH	1.4V
PJ	1.5V
PK	1.6V
PL	1.7V
PM	1.8V
PP	1.25V
PQ	1.85V
	 ①② PA PB PC PD PE PF PG PH PJ PK PL PM PP PQ

NO.EA-180-200325

• Power Dissipation (SC-88A)

Power Dissipation (P_D) depends on conditions of mounting on board. This specification is based on the measurement at the condition below;

Measurement Conditions

	Standard Land Pattern	
Environment Mounting on Board (Wind velocity=0m/		
Board Material Glass cloth epoxy plastic (Double Laye		
Board Dimensions	40mm×40mm×1.6mm	
Copper Ratio	Top side: Approx. 50%, Back side: Approx. 50%	
Through-hole	φ0.5mm×44pcs	

Measurement Result

(Ta=25°C, Tjmax=125°C)

.

	Standard Land Pattern	Free Air
Power Dissipation	380mW	150mW
Thermal Resistance	θja=(125-25°C)/0.38W=263°C/W	θja=(125-25°C)/0.15W=667°C/W
	θjc=75°C/W	-



NO.EA-180-200325

• Package Dimensions (SC-88A)



• Mark Specification (SC-88A)

①②③④: Product Code......Refer to the marking list table
⑤⑥: Lot No.....Alphanumeric serial number.



NO.EA-180-200325

• RP106Q Series marking list table (SC-88A)

RP106QxxxB		
Product Name	1234	Vset
RP106Q072B	Q 0 0 1	0.7V
RP106Q082B	Q 0 0 2	0.8V
RP106Q092B	Q003	0.9V
RP106Q102B	Q 0 0 4	1.0V
RP106Q112B	Q005	1.1V
RP106Q122B	Q006	1.2V
RP106Q132B	Q 0 0 7	1.3V
RP106Q142B	Q008	1.4V
RP106Q152B	Q009	1.5V
RP106Q162B	Q010	1.6V
RP106Q172B	Q 0 1 1	1.7V
RP106Q182B	Q 0 1 2	1.8V
RP106Q122B5	Q 0 1 4	1.25V
RP106Q182B5	Q 0 1 5	1.85V

RP106QxxxD **Product Name** 1234VSET RP106Q072D R 0 0 1 0.7V RP106Q082D R002 0.8V RP106Q092D R003 0.9V RP106Q102D R004 1.0V RP106Q112D R005 1.1V RP106Q122D R006 1.2V RP106Q132D R007 1.3V 1.4V RP106Q142D R008 RP106Q152D R009 1.5V RP106Q162D R010 1.6V RP106Q172D R 0 1 1 1.7V 1.8V RP106Q182D R 0 1 2 RP106Q122D5 R 0 1 4 1.25V RP106Q182D5 R 0 1 5 1.85V

NO.EA-180-200325

• Power Dissipation (SOT-23-5)

Power Dissipation (P_D) depends on conditions of mounting on board. This specification is based on the measurement at the condition below:

(Power Dissipation (SOT-23-5) is substitution of SOT-23-6.)

Measurement Conditions

	Standard Test Land Pattern	
Environment	Mounting on Board (Wind velocity=0m/s)	
Board Material Glass cloth epoxy plastic (Double side		
Board Dimensions	40mm×40mm×1.6mm	
Copper Ratio	Top side: Approx. 50%, Back side: Approx. 50%	
Through-holes	φ 0.5mm×44pcs	

Measurement Result

(Ta=25°C, Tjmax=125°C)

	Standard Land Pattern	Free Air
Power Dissipation	420mW	250mW
Thermal Resistance	θja=(125-25°C)/0.42W=238°C/W	400°C/W



Power Dissipation



Measurement Board Pattern

C Mount Area (Unit: mm)

NO.EA-180-200325

• Package Dimensions (SOT-23-5)



• Mark Specification (SOT-23-5)

①②③: Product Code......Refer to the marking list table
④⑤ : Lot No.....Alphanumeric serial number.



NO.EA-180-200325

• RP106N Series marking list table (SOT-23-5)

RP106NxxxB		
Product Name	123	Vset
RP106N071B	САА	0.7V
RP106N081B	CAB	0.8V
RP106N091B	CAC	0.9V
RP106N101B	CAD	1.0V
RP106N111B	CAE	1.1V
RP106N121B	CAF	1.2V
RP106N131B	CAG	1.3V
RP106N141B	САН	1.4V
RP106N151B	CAJ	1.5V
RP106N161B	CAK	1.6V
RP106N171B	CAL	1.7V
RP106N181B	CAM	1.8V
RP106N121B5	САР	1.25V
RP106N181B5	CAQ	1.85V

RP106NxxxD

Product Name	123	Vset
RP106N071D	СВА	0.7V
RP106N081D	СВВ	0.8V
RP106N091D	СВС	0.9V
RP106N101D	CBD	1.0V
RP106N111D	CBE	1.1V
RP106N121D	CBF	1.2V
RP106N131D	CBG	1.3V
RP106N141D	СВН	1.4V
RP106N151D	СВЈ	1.5V
RP106N161D	СВК	1.6V
RP106N171D	CBL	1.7V
RP106N181D	СВМ	1.8V
RP106N121D5	СВР	1.25V
RP106N181D5	CBQ	1.85V

NO.EA-180-200325

TEST CIRCUITS



NO.EA-180-200325

TYPICAL CHARACTERISTICS

1) Output Voltage vs. Output Current (Topt=25°C)





2) Output Voltage vs. Input Voltage (Topt=25°C)







RP106x

NO.EA-180-200325











NO.EA-180-200325

4) Output Voltage vs. Temperature











RP106x

NO.EA-180-200325















<u>RP106x</u>

NO.EA-180-200325

7) Dropout Voltage vs Set Output Voltage (Topt=25°C)



8) Minimum Operating Voltage



9) Ripple Rejection vs. Input Bias Voltage (C1=none, C2=1.0µF, Ripple=0.2Vp-p, Topt=25°C)



* RP106N (SOT-23-5) is the discontinued product as of March 2020.

RP106x

NO.EA-180-200325



10) Ripple Rejection vs. Frequency (C1=none, C2=1.0μF, Ripple=0.1Vp-p, Topt=25°C) RP106x07xx RP106x12xx









<u>RP106x</u>

NO.EA-180-200325



11) Input Transient Response (C1=none, C2=1.0µF, Iout=30mA, tr=tf=5µs, Topt=25°C)

12) Load Transient Response (C1=C2=1.0µF, tr=tf=5µs, Topt=25°C)



NO.EA-180-200325



13) Turn On Speed with CE pin (C1=C2=1.0 μ F, Topt=25°C)



NO.EA-180-200325



14) Turn Off Speed with CE pin (C1=C2=1.0µF, Topt=25°C)



RP106x07xx









NO.EA-180-200325



RICOH

NO.EA-180-200325





NO.EA-180-200325

ESR vs. Output Current

When using these ICs, consider the following points:

The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown below. The conditions when the white noise level is under $40\mu V$ (Avg.) are marked as the hatched area in the graph.

Measurement conditions

If other than ceramic capacitors such as tantalum, the ESR of the capacitor might be higher than expected. This graph shows the stable area with ESR limit. In the actual evaluation, we used Murata GRM155B31A105KE15, therefore, bias characteristics of the same kind of ceramic capacitors are considered.





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