# 600 mA 6 MHz Synchronous Step-down DCIDC Converter Evaluation Board 

NO. EEV-318-K331B-190222


#### Abstract

RP508K331B-EV is the evaluation board for RP508 which has the below features, benefits and specifications.


## OUTLINE

The RP508K is a low supply current PWM/VFM step-down DC/DC converter with synchronous rectifier featuring $600 \mathrm{~mA}^{(1)}$ output current. Internally, a single converter consists of an oscillator, a reference voltage unit, an error amplifier, a switching control circuit, a mode control circuit, a soft-start circuit, an under-voltage lockout (UVLO) circuit, an over current protection circuit, a thermal shutdown circuit and switching transistors. By the adoption of the synchronous rectification circuit with built-in switching transistors, the RP508K works as efficient step-down DC/DC converter, without connecting external diodes. Using synchronous rectification not only increases circuit performance but also allows a design to reduce parts count.
Power controlling method can be selected from forced PWM control type or PWM/VFM auto switching control type by inputting a signal to the MODE pin. In low output current, forced PWM control switches at fixed frequency rate in order to reduce noise. Likewise, in low output current, PWM/VFM auto switching control automatically switches from PWM mode to VFM mode in order to achieve high efficiency.
Output voltage is internally fixed type which allows output voltages that range from 0.8 V to 3.3 V in 0.1 V step. The output voltage accuracy is as high as $\pm 1.5 \%$ or $\pm 18 \mathrm{mV}$.
Protection circuits included in the RP508K are over current protection circuit and thermal shutdown circuit. Over current protection circuit supervises the inductor peak current in each switching cycle, and if the current exceeds the Lx current limit (lıxıIm), it turns off P-channel Tr. Thermal shutdown circuit detects overheating of the converter if the output pin is shorted to the ground pin (GND) etc. and stops the converter operation to protect it from damage if the junction temperature exceeds the specified temperature.

## FEATURES





- Standby Current (Istandby) ..................................... Typ. $0 \mu \mathrm{~A}$
- Output Voltage Temperature Coefficient ( $\Delta \mathrm{Vout} / \mathrm{Ta}$ ) $\cdots \cdots$.... Typ. $\pm 100 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$
- Oscillator Frequency (fosc).................................................. 6.0 MHz
- Maximum Duty Cycle (Maxduty) ................................ 100\%
- Built-in Driver ON Resistance (Ronp, Ronn) $\cdots \cdots \cdots \cdots \cdots$.......... Typ. Pch. $0.33 \Omega$, Nch. $0.24 \Omega\left(\mathrm{~V}_{\mathrm{IN}}=3.6 \mathrm{~V}\right)$
- UVLO Detector Threshold (Vuvloo1) ........................... Typ. 2.0 V

[^0]- Soft-start Time (tstart)
- Lx Current Limit Circuit (ILxııм) .................................. Typ. 1.1 A

- Package

DFN(PLP)1212-6F

- For more details on RP508 IC, please refer to
https://www.e-devices.ricoh.co.jp/en/products/power/dcdc/rp508/rp508-ea.pdf.


## Part Number Information

| Product Name | Package |
| :---: | :---: |
| RP508Kxx1\$-TR | DFN(PLP)1212-6F |

xx: Specify the set output voltage ( $\mathrm{V}_{\text {SET }}$ ).
$x x$ : Fixed Output Voltage Type, 33: VSET= 3.3 V
\$: Specify the auto-discharge option.
B: Fixed output voltage type, auto-discharge function in shutdown mode

## PCB LAYOUT



## ABSOLUTE MAXIMUM RATINGS

| Symbol | Item | Rating | Unit |
| :---: | :---: | :---: | :---: |
| VIN | Vin Input Voltage | -0.3 to 6.5 | V |
| VLx | Lx Pin Voltage | -0.3 to VIN +0.3 | V |
| V Ce | CE Pin Input Voltage | -0.3 to 6.5 | V |
| Vmode | MODE Pin Input Voltage | -0.3 to 6.5 | V |
| Vout | Vout Pin Voltage | -0.3 to 6.5 | V |
| ILX | Lx Pin Output Current | 1300 | mA |
| PD | Power Dissipation ${ }^{(1)}$ (JEDEC STD 51-7 Test Land Pattern ) | 666 | mW |
| Tj | Junction Temperature Range | -40 to 125 | ${ }^{\circ} \mathrm{C}$ |
| Tstg | Storage Temperature Range | -55 to 125 | ${ }^{\circ} \mathrm{C}$ |

## ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

## RECOMMENDED OPERATING CONDITIONS

| Symbol | Item | Rating | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\text {IN }}$ | Input Voltage | 2.3 to 5.5 | V |
| Ta | Operating Temperature Range | -40 to 85 | ${ }^{\circ} \mathrm{C}$ |

## RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

[^1]
## APPLICATION INFORMATION

## Typical Application



RP508K Typical Application

Recommended Components ${ }^{* 1}$

| Symbol | Size |
| :---: | :---: |
| $\mathrm{CiN}^{\text {Cout }}$ | $4.7 \mu \mathrm{~F}$ |
| L | $4.7 \mu \mathrm{~F}$ |

${ }^{* 1}$ The bill of materials will be attached on the shipment of each purchased evaluation board.

Set Output Voltage Range vs. Inductance Range

| Set Output Voltage (V) | Input Voltage (V) | Inductance |  |
| :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{SET}}$ | $\mathrm{V}_{\mathrm{IN}}$ | $\mathrm{L}=\mathbf{0 . 4 7} \boldsymbol{\mu H}$ | $\mathrm{L}=\mathbf{1 . 0} \boldsymbol{\mu H}$ |
| 2.7 to 3.3 | up to 4.5 | Recommended | Acceptable |
|  | 4.5 to 5.5 | - | Recommended |

## PIN DESCRIPTION



DFN(PLP)1212-6F Pin Configuration

Pin Description

| Pin No. | Symbol | Pin Description |
| :---: | :---: | :--- |
| 1 | VOUT | Output Pin |
| 2 | MODE | Mode Control Pin <br> ("H" forced PWM control, "L" PWM/VFM auto switching control) |
| 3 | CE | Chip Enable Pin ("H" active) |
| 4 | VIN | Input Pin |
| 5 | LX | LX Switching Pin |
| 6 | GND | Ground Pin |

## TECHNICAL NOTES

The performance of power source circuits using this IC largely depends on the peripheral circuits. When selecting the peripheral components, consider the conditions of use. Do not allow each component, PCB pattern and the IC to exceed their respected rated values (voltage, current and power) when designing the peripheral circuits.

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[^0]:    ${ }^{(1)}$ This is an approximate value. The output current is dependent on conditions and external components.

[^1]:    ${ }^{(1)}$ Refer to POWER DISSIPATION for detailed information.

