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RP509Z121B-EV

1A PWM/VFM Step-down DC/DC Converter with Synchronous Rectifier Evaluation Board

No. EEV-362-Z121B-200220

RP509Z121B-EV is the evaluation board for RP509 which has the below features, benefits and specifications.

OUTLINE

The RP509Z is a low supply current PWM/VFM step-down DC/DC converter with synchronous rectifier featuring 0.5 A/1 A output current⁽¹⁾. Internally, a single converter consists of a reference voltage unit, an error amplifier, a switching control circuit, a mode control circuit, a soft-start circuit, an undervoltage lockout (UVLO) circuit, a thermal shutdown circuit, and switching transistors. The RP509Z is employing synchronous rectification for improving the efficiency of rectification by replacing diodes with built-in switching transistors. Using synchronous rectification not only increases circuit performance but also allows a design to reduce parts count. Output voltage controlling method is selectable between a PWM/VFM auto-switching control type and a forced PWM control type, which further reduces noise than a normal PWM control under a light load, and these types can be set by the MODE pin. Output voltage type. Protection circuits in the RP509Z is current limit circuit and thermal shutdown circuit. LX current limit value (Typ.) is selectable between 1.6 A and 1.0 A. The RP509Z is available in WLCSP-6-P6 which achieves high-density mounting on boards. Using capacitor of 0402-/1005-size (inch/mm) and inductor of 0603-/1608-size (inch/mm) as external parts help to save space for devices.

FEATURES

- Input Voltage Range (Maximum Rating) ······2.3 V to 5.5 V (6.5 V)
- Output Voltage Range (Fixed Output Voltage Type)0.6 V to 3.3 V, settable in 0.1 V steps
 - (Adjustable Output Voltage Type) 0.6 V to 5.5 V
- Output Voltage Accuracy (Fixed Output Voltage Type) ······· ±1.5% (V_{SET}⁽²⁾ ≥ 1.2 V), ±18 mV (V_{SET} < 1.2 V)
- Feedback Voltage Accuracy (Adjustable Output Voltage Type) ···· ±9 mV (V_{FB} = 0.6 V)
- Output Voltage/Feedback Voltage Temperature Coefficient + 100 ppm/°C
- Selectable Oscillator FrequencyTyp. 6.0 MHz
- Oscillator Maximum Duty ······Min. 100%
- Built-in Driver ON Resistance (V_{IN} = 3.6 V)······ Typ. Pch. 0.175 Ω , Nch. 0.155 Ω (RP509Z)
- Standby Current Typ. 0 μA
- UVLO Detector Threshold ······Typ. 2.0 V

⁽¹⁾ This is an approximate value. The output current is dependent on conditions and external components.

<u>RP509Z</u>

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- Soft-start Time Typ. 0.15 ms
- Package WLCSP-6-P6 (1.28 mm x 0.88 mm x 0.64 mm)
- For more details on RP509 IC, please refer to https://www.n-redc.co.jp/en/pdf/datasheet/rp509-ea.pdf.

Part Number Information

Product Name	Package
RP509Z121B	WLCSP-6-P6

- 12: Specify the set output voltage (V_{SET}) Fixed Output Voltage Type: 12 = 1.2 V
- 1: Specify the LX Current Limit (ILXLIM) 1: Typ.1.6 A
- B: Specify the version
 - B: with Auto-discharge, Oscillator Frequency = 6.0 MHz

PCB LAYOUT

Top Layer

Bottom Layer



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(GND = 0.V)

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ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

Input Voltage	ltem		Rating	Unit
Input Voltage				
		Input Voltage		V
LX Pin Voltage		-0.3 to V _{IN} +0.3	V	
CE Pin Voltage		-0.3 to 6.5	V	
MODE Pin Voltage		-0.3 to 6.5	V	
VOUT/VFB Pin Voltage		-0.3 to 6.5	V	
LX Pin Output Current		1.6	A	
Power Dissipation ⁽¹⁾	WLCSP-6-P6	JEDEC STD. 51-9 Test Land Pattern	910	mW
Tj Junction Temperature		-40 to 125	°C	
Storage Temperature Range		-55 to 125	О°	
	X Pin Voltage CE Pin Voltage MODE Pin Volta /OUT/VFB Pin X Pin Output (Power Dissipation ⁽¹⁾ Junction Tempe Storage Tempe	X Pin Voltage CE Pin Voltage MODE Pin Voltage /OUT/VFB Pin Voltage X Pin Output Current Power Dissipation ⁽¹⁾ WLCSP-6-P6 Junction Temperature Storage Temperature Range	X Pin Voltage CE Pin Voltage MODE Pin Voltage MOUT/VFB Pin Voltage X Pin Output Current Power Dissipation ⁽¹⁾ WLCSP-6-P6 JEDEC STD. 51-9 Test Land Pattern Junction Temperature Storage Temperature Range	.X Pin Voltage -0.3 to V _{IN} +0.3 CE Pin Voltage -0.3 to 6.5 MODE Pin Voltage -0.3 to 6.5 /OUT/VFB Pin Voltage -0.3 to 6.5 /X Pin Output Current 1.6 Power JEDEC STD. 51-9 Dissipation ⁽¹⁾ WLCSP-6-P6 JEDEC STD. 51-9 910 Junction Temperature -40 to 125 Storage Temperature Range -55 to 125

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Rating	Unit
V _{IN}	Input Voltage	2.3 to 5.5	V
Та	Operating Temperature Range	-40 to 85	°C

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

⁽¹⁾ Refer to *POWER DISSIPATION* for detailed information.

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APPLICATION INFORMATION

Typical Application Circuits

MODE = High: Forced PWM Control, MODE = Low: PWM/VFM Auto-switching Control



RP509ZxxXA/RP509ZxxXB (Fixed Output Voltage Type)

Recommended External Components^{*1}

Symbol	Sizo
Symbol	5126
CIN	10 µF
Соит	10 µF
L	0.47 µH

^{*1} The bill of materials will be attached on the shipment of each purchased evaluation board.

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PIN DESCRIPTION



WLCSP-6-P6 Pin Configurations

WLCSP-6-P6 Pin Description

Pin No.	Symbol	Description
A1	MODE	Mode Control Pin (High: Forced PWM Control, Low: PWM/VFM Auto-switching Control)
B1	LX	Switching Pin
C1	VOUT/VFB	Output/Feedback Voltage Pin
A2	VIN	Input Voltage Pin
B2	CE	Chip Enable Pin, Active-high
C2	GND	Ground Pin

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TECHNICAL NOTES

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed its rated voltage, rated current or rated power. When designing a peripheral circuit, please be fully aware of the following points.

- Set the external components as close as possible to the IC and minimize the wiring between the components and the IC. Especially, place a capacitor (C_{IN}) as close as possible to the VIN pin and GND.
- Ensure the VIN and GND lines are sufficiently robust. If their impedance is too high, noise pickup or unstable operation may result.
- The VIN line, the GND line, the VOUT line, an inductor, and LX should make special considerations for the large switching current flows.
- The wiring between the VOUT pin and an inductor (L) (RP509xxxXA/RP509xxxXB) should be separated from the wiring between L and Load.
- Over current protection circuit may be affected by self-heating or power dissipation environment.
- For any setting type of output voltage, the input/output voltage ratio must meet the following requirement to achieve a stable VFM mode at light load when the MODE pin is "Low" (at PWM/VFM Auto Switching):

 V_{OUT} / V_{IN} < 0.7





Available Voltage Area with Stable VFM Mode

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