## VIDEO PICTURE ENHANCER

## ■ GENERAL DESCRIPTION

The NJM2209 is the video IC for quality improvement of the video picture to get high quality by rectifying the picture contour.

- PACKAGE OUTLINE

NJM2209M
PACKAGE OUTLINE


- By Differential From, Picture Enhance


## - FEATURES

- Operating Voltage
$(+4.5 \mathrm{~V}$ to $+5.5 \mathrm{~V})$
- at Minimal External Components
- Internal Switch of Hirough/Picture Enhance
- Package Outline

DMP14

- Bipolar Technology


## ■ RECOMMENDED OPERATING CONDITION

- Operating Voltage
4.5 to 5.5 V


## - APPLICATION

- Upgrading of picture quality on VCR, personal computer and other video picture.


## ■ PIN CONFIGURATION



PIN FUNCTION

1. Video Signal Output
2. Frequency Compensation
3. N.C.
4. N.C.
5. Differential Input
6. Video Signal Input
7. $\mathrm{V}^{+}$
8. N.C.
9. Control Input
10. Phase Delay
11. N.C.
12. Differential Output
13. GND
14. N.C.

- TEST CIRCUIT


| ■ ABSOLUTE MAXIMUM RATINGS |  |  | $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$ |
| :--- | :---: | :---: | :---: |
| PARAMETER | SYMBOL | RATINGS | UNIT |
| Supply Voltage | $\mathrm{V}^{+}$ | 8 | V |
| Power Dissipation | $\mathrm{P}_{\mathrm{D}}$ | (DMP8) 300 | mW |
| Operating Temperature Range | $\mathrm{T}_{\text {opr }}$ | -20 to +75 | mW |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

- ELECTRICAL CHARACTERISTICS
$\left(\mathrm{V}^{+}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right.$, Refer to Test Cricuit)

| PARAMETER |  | SYMBOL | SIGNAL PIN | $\begin{gathered} \hline \text { TEST } \\ \text { PIN } \end{gathered}$ | $\begin{array}{\|c\|} \hline \text { CONT. } \\ \text { VOLTAGE } \end{array}$ | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Current |  | Icc |  |  | 2.8 V | No Input Signal | - | 7.5 | 10 | mA |
| Limitter Level (1) |  | LIM1 | 10 | 8 | - | SYNC level>0.35V, Input Video Signal | 0.23 | 0.27 | 0.31 | V |
| Limitter Level (2) |  | LIM2 | 3 | 1 | - | $\mathrm{f}=100 \mathrm{kHz}, 1 \mathrm{~V}_{\text {PPP }}$ Sine Wave Input | 0.21 | 0.25 | 0.29 | V |
| Control Amp Gain | H | $\mathrm{G}_{\mathrm{H}}$ | 8 | 7 | 2.8 V | $\begin{aligned} & \text { f=100kHz, } 0.1 \mathrm{Vrms.} \text {. Sine Wave Input }(\mathrm{dB}) \\ & \mathrm{G}=20 \log _{10} \mathrm{~V}_{\mathrm{ou}} N_{\mathbb{N}} \end{aligned}$ | -2 | -0.9 | 0 | dB |
|  | M | GM | 8 | 7 | 1.3 V |  | -12 | -10 | -8 | dB |
|  | L | GL | 8 | 7 | 0.45 V |  | - | - | -28 | dB |
| Add <br> Amp Gain | 3 pin input | $\mathrm{G}_{7}$ | 3 | 1 | 2.8 V | $\begin{align*} & \mathrm{f}=100 \mathrm{kHz}, 200 \mathrm{~m} \mathrm{~V}_{\text {P- }} \text { Sine Wave } \\ & \mathrm{G}=20 \log _{10} \mathrm{~V}_{\text {out }} N_{\text {IN }}  \tag{dB}\\ & \hline \end{align*}$ | -1.6 | -0.6 | 0.4 | dB |
|  | 10 pin input | $\mathrm{G}_{3}$ | 10 | 1 | 2.8 V | $1 \mathrm{~V}_{\mathrm{P}-\mathrm{p}}$ Video Signal Input <br> $\mathrm{G}=20 \log _{10} \mathrm{~V}_{\mathrm{out}} / \mathrm{V}_{\mathbb{I}}$ | -1 | 0 | +1 | dB |
| Switch Cross Talk |  | Csw | 12 | 1 | $2.8 \rightarrow 0 \mathrm{~V}$ | $\mathrm{f}=2 \mathrm{MHz}, 1 \mathrm{~V}_{\mathrm{P}-\mathrm{P}}$ Sine Wave <br> $\mathrm{C}_{\mathrm{sw}}=20 \log _{10} \mathrm{~V}(0 \mathrm{~V}) /(2.8 \mathrm{~V})$ <br> (dB) | - | -50 | - | dB |
| Through Gain |  | GT | 10 | 1 | OV | $1 \mathrm{~V}_{\mathrm{P}-\mathrm{P}}$ Video Signal Input $\mathrm{G}_{\mathrm{T}}=20 \log _{10} \mathrm{~V}_{\mathrm{ou}} N_{\mathbb{N}}$ | -1 | 0 | 1 | dB |
| Switch Control Threshold Voltage |  | $\mathrm{V}_{\text {TH }}$ | 12 | 1 |  | $\mathrm{f}=100 \mathrm{kHz}, 1 \mathrm{~V}_{\mathrm{PP}}$ Sine Wave Input $-40 \mathrm{~dB}=20 \log _{10} \mathrm{~V}_{\text {OuT }} N_{\text {IN }}$ | 0.2 | 0.3 | 0.4 | V |
| Differential Gain (Note 1) |  | DGpC | 10 | 1 | 2.8 V | DGDP Tester <br> Video Signal 1V $\mathrm{PPP}_{\text {P }}$ (Stair Step) | - | 1 | 3 | \% |
| Differential Gain (Note 2) |  | DGT | 10 | 1 | OV |  | - | 0 | 3 | \% |
| 1 PIN Voltage (Note 1) |  | $V_{\text {6PC }}$ |  | 1 | 2.8 V |  | - | 1.8 | - | V |
| 1 PIN Voltage (Note 2) |  | $\mathrm{V}_{6}$ T |  | 1 | OV |  | - | 2.0 | - | V |

## - TYPICAL APPLICATION



## - PRINCIPLES OF OPERATION, BI BLOCK DIAGRAM

The NJM2209 is a video signal IC which converts an input video signal to a compensated video signal of the picture outline by adding an input signal through a differential amplifier to the original input signal.
The compensating (enhanced) ratio is decided by pin 5 voltage and so the original signal comes when pin 5 voltage is zero.
A peaking frequency compensation of the internal
differential amplifier is changed by $\mathrm{C}, \mathrm{R}$ attached to pin 8 and $\mathrm{L}, \mathrm{R}$ to pin 7 .
The compensation signal and the original video signal are delayed the phase by low pass filter. These are done by a capacitor attached to pin 12 . The compensated ratio is originally settled by the coupling condenser between pin 7 and pin 3.

Example (Multi-Burst Enhancer)


## ■ APPLICATION

This IC requires $1 \mathrm{M} \Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.

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