

NTE7005 Integrated Circuit 2-Phase Stepping Motor Driver

Description:

The NTE7005 monolithic power IC is a dual bridge driver in a 16–Lead DIP type package. The maximum driving current and voltage is 330mA x 12V per bridge. Therefore, the best use for this part is in a two–phase bipolar stepping motor driving the head actuator of a 3, to 5.25–inch Floppy Disk Drive.

Features:

- 330mA Output Current Capability
- Dual Bridge Included
- Power Save
- Single Input Direction Control
- Low Output Saturation Voltage
- Low Supply Current
- Low Input Current
- Compatible with TTL, LSTTL, and 5V CMOS
- Thermal Shutdown

Absolute Maximum Ratings: $(T_A = +25^{\circ}C, \text{ Note 1 unless otherwise specified})$
Logic Stage Supply Voltage, V_{CC}
Seeking Supply Voltage, V_{s1}
Holding Supply Voltage, V _{s2}
Input Voltage, V _i
Peak Seeking Current (t \leq 5ms), I _o (peak)
DC Seeking Current, I _{os}
DC Holding current, I _{OH}
Power Dissipation (Note 2), P _T
Junction Temperature, T _J
Operating Junction Temperature Range, T _{opr} –20° to +125°C
Storage Temperature Range, T _{stq} –55° to +125°C
Note 1. The absolute maximum ratings are limiting values, to be applied individually, beyond which

Note 1. The absolute maximum ratings are limiting values, to be applied individually, beyond which the device may be permanently damaged. Functional operation under any of these conditions is not guaranteed. Exposing a circuit to its absolute maximum rating for extended periods of time may affect the device's reliability.

Note 2. Thermal resistance is as follows:

 $R_{th,IA}1 \le +60^{\circ}C/W$ (Soldered on a print circuit board)

R_{thJA}2 ≤ +35°C/W (Soldered on a print circuit covered with copper sufficiently)

R_{th,IA}3 ≤ +15°C/W (Soldered on Pin4, Pin5, Pin12, and Pin13 with an infinite heat sink)

Recommended Operating Conditions:

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Logic Stage Supply Voltage	V _{CC}		4.5	5.0	5.5	V
Seeking Supply Voltage	V _{s1}		10.2	12.0	13.8	V
Holding Supply Voltage	V _{s2}		4.5	5.0	5.5	V

$\underline{\textbf{Electrical Characteristics}}\text{: } (T_{A} = +25^{\circ}\text{C}, \ V_{CC} = 5\text{V}, \ V_{s2} = 5\text{V}, \ V_{s1} = 12\text{V} \text{ unless otherwise specified)}$

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
Input Low Voltage	V_{IL}			_	_	8.0	V
Input High Voltage	V _{IH}			2.0	_	_	V
Input Low Current	I _{IL}	$V_{I} = 0.8V$		_	_	±10	μΑ
Input High Current	I _{IH}	$V_{I} = 2V$ $V_{I} = 5.5V$		_	1.0	±10	μΑ
				_	0.5	1.0	mA
Supply Current	I_{CC} PS = 0.8V, $I_0 = 0$		V _{CC}	_	25	33	mA
		$I_0 = 0$	V _{s1} , Note 3	_	10	20	mA
			V _{s2} , Note 4	_	_	1.0	mA
		PS = 2V,	V _{CC}	_	25	33	mA
	$I_0 = 0$ V_{s1}, N_0	V _{s1} , Note 3	_	3	5	mA	
			V _{s2} , Note 4	_	5	10	mΑ
Output Transistor Breakdown Voltage	V _{(BR)CE}	I _C = 10mA		18	_	_	V
	R						
V _{s1} Saturation Voltage	V _{CE(sat)} 1	$PS = 0.8V, I_0 = 330mA, Note 5$		_	1.5	2.0	V
V _{s2} Saturation Voltage	V _{CE(sat)} 2	$PS = 2V, I_0 = 130mA, Note 5$		_	1.5	2.0	V
Clamp Diode Forward Voltage	V _F	I _F = 330mA	Upper	_	5	_	V
			Lower	_	1.5	_	V
Delay Time	t _{PLH}	I _o = 330mA		_	1	5	μs
	t _{PHL}			_	1	5	μs

Note 3. Sum of $V_{S1}\left(\varphi1\right)$ and $V_{S1}(\varphi2)$ current

Note 4. Sum of V_{S2} (ϕ 1) and V_{S2} (ϕ 2) current

Note 5. Sum of upper and lower saturation voltages

Truth Table (For each bridge):

Power Save Direction		ΦOut	<u></u> ΦOut	
L	L	L	H+	
L	Н	H+	L	
Н	L	L	H–	
Н	Н	H–	L	

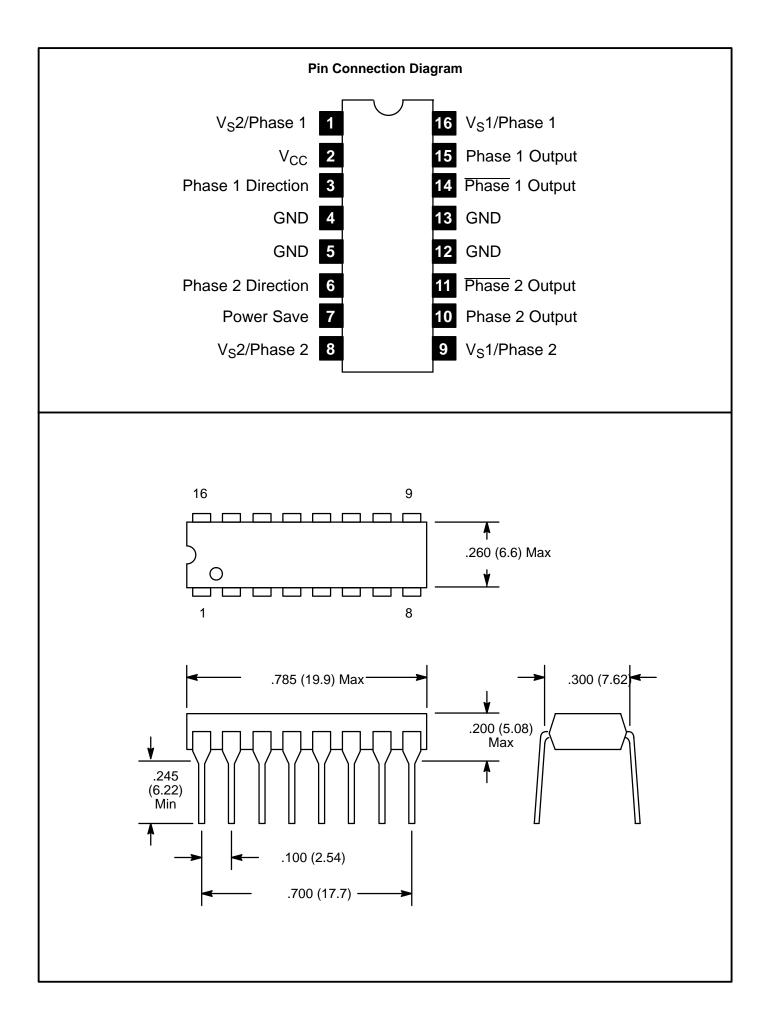
Note: L = Low voltage state

H+ = High voltage state

(Seeking transistors ON)

H- = High voltage state

(Holding transistors ON)



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