Unbuffered inverter Rev. 2 — 13 December 2016

**Product data sheet** 

### 1. General description

The 74LVC1GU04-Q100 is a single unbuffered inverter.

The input can be driven from either 3.3 V or 5 V devices. This feature allows the use of this device in a mixed 3.3 V and 5 V environment.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

### 2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
   Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 1.65 V to 5.5 V
- High noise immunity
- ESD protection:
  - MIL-STD-883, method 3015 exceeds 2000 V
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V (C = 200 pf, R = 0 Ω)
- $\pm 24$  mA output drive (V<sub>CC</sub> = 3.0 V)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Input accepts voltages up to 5 V

# 3. Ordering information

#### Table 1.Ordering information

Type number	Package	ackage					
	Temperature range	Name	Description	Version			
74LVC1GU04GW-Q100	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			
74LVC1GU04GV-Q100	–40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753			

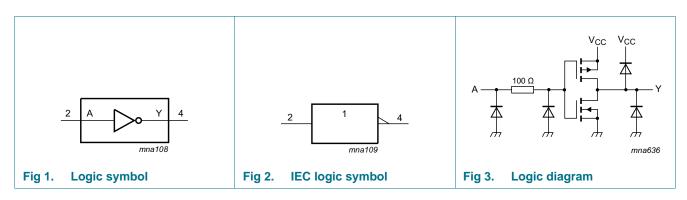
# nexperia

# 4. Marking

Table 2. Marking codes	
Type number	Marking <sup>[1]</sup>
74LVC1GU04GW-Q100	VD
74LVC1GU04GV-Q100	VU4

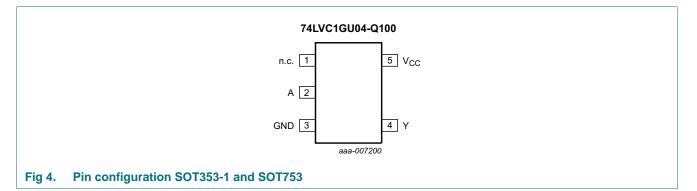
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

# 5. Functional diagram



# 6. Pinning information

### 6.1 Pinning



### 6.2 Pin description

Table 3. Pin description				
Symbol	Pin	Description		
n.c.	1	not connected		
A	2	data input		
GND	3	ground (0 V)		
Y	4	data output		
Vcc	5	supply voltage		

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# 7. Functional description

Table 4. Function table <sup>[1]</sup>	
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Input (A)	Output (Y)
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level.

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+6.5	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-	-50	mA
VI	input voltage		<u>[1]</u>	-0.5	+6.5	V
I <sub>ОК</sub>	output clamping current	$V_{O} > V_{CC}$ or $V_{O} < 0 V$		-	±50	mA
Vo	output voltage	Active mode	<u>[1][2]</u>	-0.5	V <sub>CC</sub> + 0.5	V
lo	output current	$V_{O} = 0 V$ to $V_{CC}$		-	±50	mA
I <sub>CC</sub>	supply current			-	+100	mA
I <sub>GND</sub>	ground current			-	-100	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$	<u>[3]</u>	-	250	mW
T <sub>stg</sub>	storage temperature			-65	+150	°C

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When  $V_{CC} = 0 V$  (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For TSSOP5 and SC-74A packages: above 87.5 °C the value of Ptot derates linearly with 4.0 mW/K.

# 9. Recommended operating conditions

#### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		1.65	-	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	Active mode	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	-	+125	°C
$\Delta t / \Delta V$	input transition rise and fall rate	$V_{CC}$ = 1.65 V to 2.7 V	0	-	20	ns/V
		$V_{CC} = 2.7 \text{ V to } 5.5 \text{ V}$	0	-	10	ns/V

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# **10. Static characteristics**

#### Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ <mark>[1]</mark>	Max	Unit
T <sub>amb</sub> = -	40 °C to +85 °C	1			I	
VIH	HIGH-level input voltage	V <sub>CC</sub> = 1.65 V to 5.5 V	$0.75 \times V_{CC}$	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 1.65 V to 5.5 V	-	-	$0.25\times V_{CC}$	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I <sub>O</sub> = -100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	V <sub>CC</sub> – 0.1	-	-	V
		I <sub>O</sub> = -4 mA; V <sub>CC</sub> = 1.65 V	1.2	-	-	V
		$I_0 = -8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_0 = -12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	2.2	-	-	V
		$I_0 = -24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.3	-	-	V
		$I_0 = -32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.8	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I <sub>O</sub> = 100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	-	-	0.1	V
		I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V	-	-	0.45	V
		I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V	-	-	0.3	V
		$I_0 = 12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	-	-	0.4	V
		$I_0 = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.55	V
		$I_0 = 32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.55	V
lı	input leakage current	$V_{I}$ = 5.5 V or GND; $V_{CC}$ = 0 V to 5.5 V	-	±0.1	±1	μA
I <sub>CC</sub>	supply current	V <sub>I</sub> = 5.5 V or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.65 V to 5.5 V	-	0.1	4	μA
CI	input capacitance	$V_{CC}$ = 3.3 V; $V_{I}$ = GND to $V_{CC}$	-	6	-	pF
T <sub>amb</sub> = -	40 °C to +125 °C	-				
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 1.65 V to 5.5 V	$0.8 \times V_{CC}$	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 1.65 V to 5.5 V	-	-	$0.2\times V_{CC}$	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O} = -100 \ \mu\text{A};$ V <sub>CC</sub> = 1.65 V to 5.5 V	V <sub>CC</sub> – 0.1	-	-	V
		$I_0 = -4 \text{ mA}; V_{CC} = 1.65 \text{ V}$	0.95	-	-	V
		$I_0 = -8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.7	-	-	V
		$I_{O} = -12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	1.9	-	-	V
		$I_{O} = -24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.0	-	-	V
		$I_{O} = -32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.4	-	-	V

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Table 7. S	Static characteristics	continued
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At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ <mark>[1]</mark>	Max	Unit
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I <sub>O</sub> = 100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	-	-	0.1	V
		I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V	-	-	0.7	V
		$I_0 = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.45	V
		$I_0 = 12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	-	-	0.6	V
		$I_0 = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.80	V
		$I_0 = 32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.80	V
I <sub>I</sub>	input leakage current	$V_{I}$ = 5.5 V or GND; $V_{CC}$ = 0 V to 5.5 V	-	±0.1	±1	μA
I <sub>CC</sub>	supply current	$V_{I} = 5.5 V \text{ or GND}; I_{O} = 0 A;$ $V_{CC} = 1.65 V \text{ to } 5.5 V$	-	-	4	μA

[1] All typical values are measured at V<sub>CC</sub> = 3.3 V and T<sub>amb</sub> = 25 °C.

### **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit, see Figure 8.

Symbol	Parameter	Conditions	-40	°C to +85	°C	-40 °C to	+125 °C	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max	
t <sub>pd</sub>	propagation delay	A to Y; see <u>Figure 5</u> [2]						
		$V_{CC} = 1.65 \text{ V} \text{ to } 1.95 \text{ V}$	0.3	1.7	5.0	0.3	6.5	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	0.3	1.3	4.0	0.3	5.5	ns
		$V_{CC} = 2.7 V$	0.5	1.7	5.0	0.5	6.5	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	0.5	1.6	3.7	0.5	5.0	ns
		$V_{CC} = 4.5 \text{ V} \text{ to } 5.5 \text{ V}$	0.5	1.3	3.0	0.5	4.0	ns
C <sub>PD</sub>	power dissipation capacitance	$V_{I} = GND \text{ to } V_{CC}; \qquad [3]$ $V_{CC} = 3.3 \text{ V}$	-	14.9	-	-	-	pF

[1] Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

- [2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .
- [3]  $C_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).
  - $\mathsf{P}_{\mathsf{D}} = \mathsf{C}_{\mathsf{P}\mathsf{D}} \times \mathsf{V}_{\mathsf{C}\mathsf{C}}{}^2 \times \mathsf{f}_i \times \mathsf{N} + \sum (\mathsf{C}_{\mathsf{L}} \times \mathsf{V}_{\mathsf{C}\mathsf{C}}{}^2 \times \mathsf{f}_o) \text{ where:}$

 $f_i$  = input frequency in MHz;

 $f_o$  = output frequency in MHz;

 $C_L$  = output load capacitance in pF;

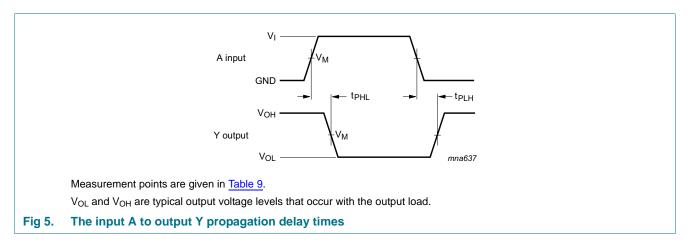
 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}{}^2 \times f_o)$  = sum of outputs.

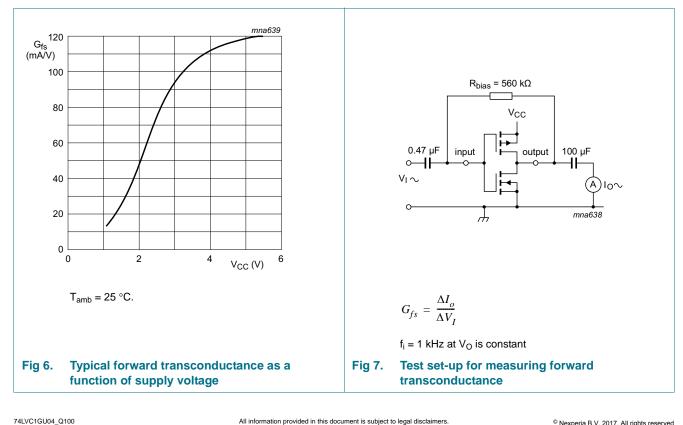
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# 12. Waveforms



#### Table 9. **Measurement points**

Supply voltage	Input	Output
V <sub>cc</sub>	V <sub>M</sub>	V <sub>M</sub>
1.65 V to 1.95 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
2.3 V to 2.7 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
2.7 V	1.5 V	1.5 V
3.0 V to 3.6 V	1.5 V	1.5 V
4.5 V to 5.5 V	$0.5  imes V_{CC}$	$0.5 \times V_{CC}$

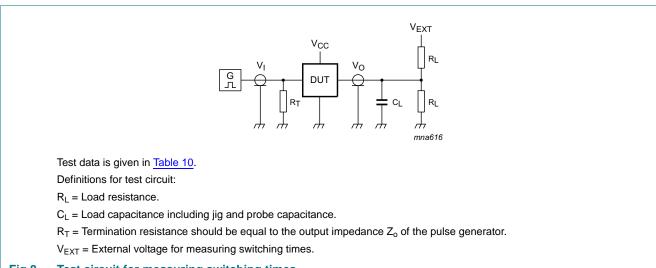


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#### Fig 8. Test circuit for measuring switching times

#### Table 10. Test data

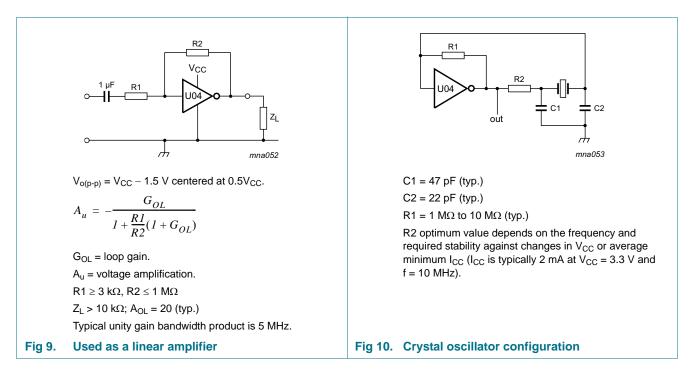
Supply voltage Input		Load		V <sub>EXT</sub>	
V <sub>cc</sub>	VI	$t_r = t_f$	CL	RL	t <sub>PLH</sub> , t <sub>PHL</sub>
1.65 V to 1.95 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	1 kΩ	open
2.3 V to 2.7 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	500 Ω	open
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
4.5 V to 5.5 V	V <sub>CC</sub>	$\leq$ 2.5 ns	50 pF	500 Ω	open

# **13. Application information**

Some applications are:

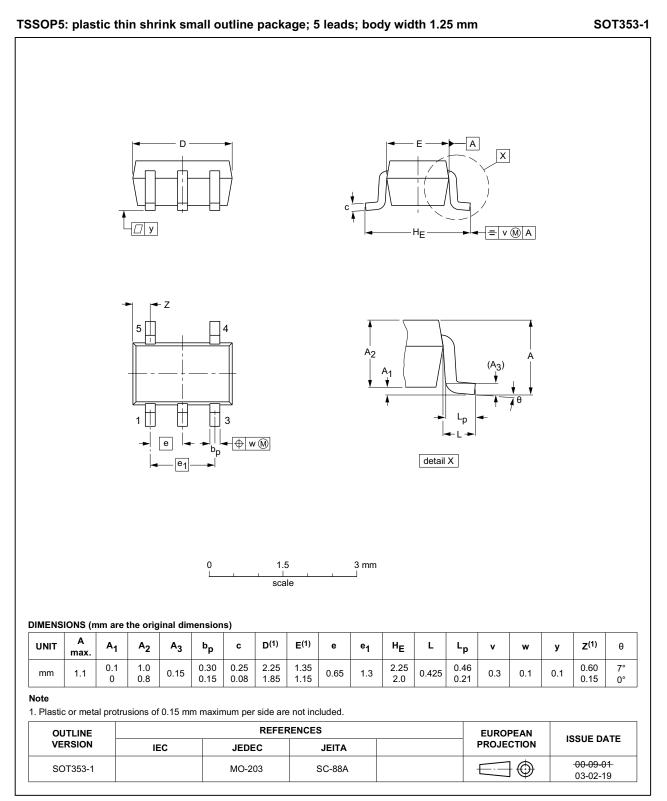
- Linear amplifier (see Figure 9)
- In crystal oscillator design (see Figure 10)

Remark: All values given are typical unless otherwise specified.



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# 14. Package outline



#### Fig 11. Package outline SOT353-1 (TSSOP5)

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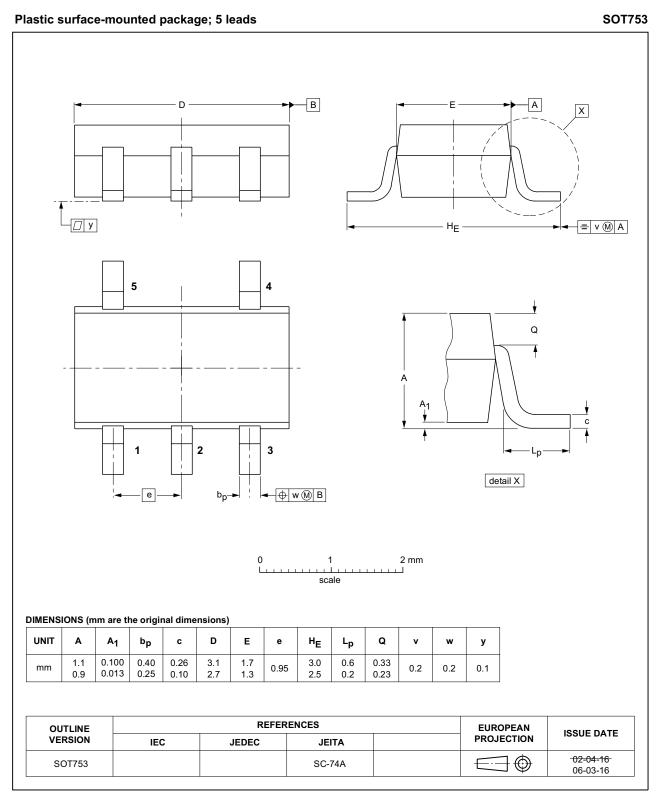


Fig 12. Package outline SOT753 (SC-74A)

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# **15. Abbreviations**

Table 11. Abbreviations		
Acronym	Description	
CMOS	Complementary Metal Oxide Semiconductor	
DUT	Device Under Test	
ESD	ElectroStatic Discharge	
HBM	Human Body Model	
MIL	Military	
MM	Machine Model	

# 16. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LVC1GU04_Q100 v.2	20161213	Product data sheet	-	74LVC1GU04_Q100 v.1
Modifications:	• <u>Table 7</u> : The maximum limits for leakage current and supply current have changed.			
74LVC1GU04_Q100 v.1	20130514	Product data sheet	-	-

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Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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