

6.2 — 1 February 2019 Product data sheet

## 1 Product profile

### 1.1 General description

Two planar PIN diodes in common cathode configuration in a SOT23 small plastic SMD package.

#### 1.2 Features and benefits

- High voltage, current controlled
- RF resistor for RF attenuators and switches
- Low diode capacitance
- · Low diode forward resistance
- · Low series inductance
- For applications up to 3 GHz
- · AEC-Q101 qualified

## 1.3 Applications

RF attenuators and switches

# 2 Pinning information

Table 1. Discrete pinning

Pin	Description	Simplified outline	Symbol
1	anode (a1)		_
2	anode (a2)	3	3
3	common cathode	top view	1 2 sym136



Silicon PIN diode

# 3 Ordering information

**Table 2. Ordering information** 

Type number	Package						
	Name	Description	Version				
BAP64-05	-	plastic surface-mounted package; 3 leads	SOT23				

# 4 Marking

Table 3. Marking

Table of Marking			
Type number	Marking	Description	
BAP64-05	5K%	% = t : made in Malaysia	
		% = W : made in China	

# 5 Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Values are specified per diode.

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	reverse voltage		-	175	V
I <sub>F</sub>	forward current		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>sp</sub> ≤ 90 °C	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-65	+150	°C

## 6 Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		220	K/W

### 7 Characteristics

### **Table 6. Characteristics**

Values are specified per diode;  $T_i$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 50 mA	-	0.95	1.1	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 60 V	-	-	10	μA
		V <sub>R</sub> = 20 V	-	-	1	μA
C <sub>d</sub>	diode capacitance	see Figure 1; f = 1 MHz;				
		V <sub>R</sub> = 0 V	-	0.52	-	pF

BAP64-05

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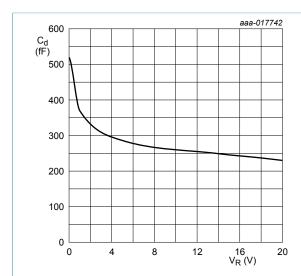
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## Silicon PIN diode

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
		V <sub>R</sub> = 1 V		-	0.37	-	pF
		V <sub>R</sub> = 20 V		-	0.23	0.35	pF
r <sub>D</sub>	diode forward resistance	see Figure 2; f = 100 MHz;	[1]				
		I <sub>F</sub> = 0.5 mA		-	20	40	Ω
		I <sub>F</sub> = 1 mA		-	10	20	Ω
		I <sub>F</sub> = 10 mA		-	2.0	3.8	Ω
		I <sub>F</sub> = 100 mA		-	0.7	1.35	Ω
ΤL	charge carrier life time	when switched from I <sub>F</sub> = 10 mA to I <sub>R</sub> = 6 mA; R <sub>L</sub> = 100 $\Omega$ ; measured at I <sub>R</sub> = 3 mA		-	1.55	-	μs
Ls	series inductance			-	1.4	-	nH

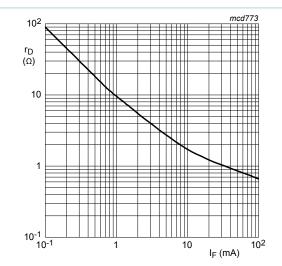
<sup>[1]</sup> Guaranteed on AQL basis: inspection level S4, AQL 1.0.

## 7.1 Graphical data



 $f = 1 \text{ MHz}; T_j = 25 °C.$ 

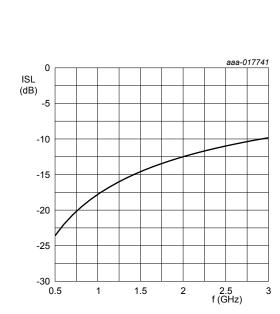
Figure 1. Diode capacitance as a function of reverse voltage; typical values



f = 100 MHz;  $T_i = 25 \,^{\circ}\text{C}$ .

Figure 2. Forward resistance as a function of forward current; typical values

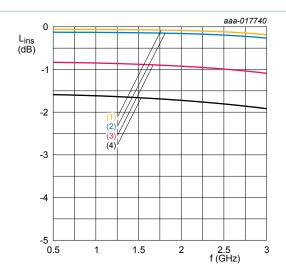
Silicon PIN diode



 $T_{amb} = 25 \, ^{\circ}C$ 

Diode zero biased and inserted in series with a 50  $\Omega$  stripline circuit

Figure 3. Isolation of the diode as a function of frequency; typical values



T<sub>amb</sub> = 25 °C

1. I<sub>F</sub> = 100 mA

2.  $I_F = 10 \text{ mA}$ 

3.  $I_F = 1 \text{ mA}$ 

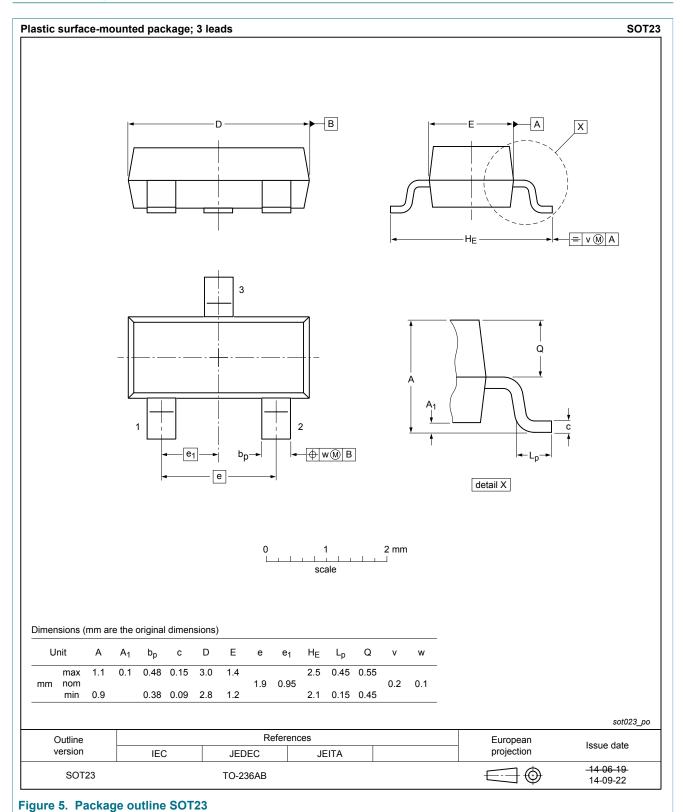
4.  $I_F = 0.5 \text{ mA}$ 

Diode inserted in series with a 50  $\Omega$  stripline circuit and biased via the analyzer Tee network

Figure 4. Insertion loss of the diode as a function of frequency; typical values

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# 8 Package outline



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## 9 Abbreviations

Table 7. Abbreviations

Acronym	Description
AQL	acceptable quality level
PIN	P-type, Intrinsic, N-type
SMD	surface mounted device
S4	special inspection level 4

# 10 Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BAP64-05 v.6.2	20190201	Product data sheet	-	BAP64-05 v.6.1		
Modifications:	changed condition	on for reverse current for V <sub>R</sub> fro	m 175 V to 60 V	'		
BAP64-05 v.6.1	20181211	Product data sheet	-	BAP64-05 v.6		
Modifications:	adapted marking	code		,		
BAP64-05 v.6	20181126	Product data sheet	-	BAP64-05 v.5		
Modifications:		enefits" has been updated. mation" pages have been upda	ted.			
BAP64-05 v.5	20150428	Product data sheet	-	BAP64-05 v.4.1		
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>AEC-Q101 qualified</li> </ul>					
BAP64-05 v.4 (9397 750 06284)	19990819	Product specification	-	BAP64-05_N v.3		
BAP64-05_N v.3 (9397 750 06089)	19990616	Preliminary specification	-	BAP64-05 v.2		
BAP64-05 v.2 (9397 750 05561)	19990510	Preliminary specification	-	BAP64-05_N v.1		
BAP64-05_N v.1 (9397 750 05494)	19981204	Objective specification	-	-		

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#### 11.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
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## **Contents**

1	Product profile	1
1.1	General description	
1.2	Features and benefits	
1.3	Applications	1
2	Pinning information	1
3	Ordering information	
4	Marking	
5	Limiting values	2
6	Thermal characteristics	2
7	Characteristics	
7.1	Graphical data	3
8	Package outline	
9	Abbreviations	
10	Revision history	
11	Legal information	

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