**Product data sheet** 

# 1 Product profile

### 1.1 General description

Planar PIN diode in a SOD523 ultra small SMD plastic package.

#### 1.2 Features and benefits

- · High voltage, current controlled
- · RF resistor for RF switches
- · Low diode capacitance
- · Low diode forward resistance
- · Very low series inductance
- AEC-Q101 qualified

### 1.3 Applications

- · RF attenuators and switches
- Bandswitch for TV tuners
- Series diode for mobile communication transmit/receive switch



Silicon PIN diode

# 2 Pinning information

Table 1. Discrete pinning

Pin	Description	Simplified outline	Graphic symbol
1	cathode		
2	anode	1 2	<b>₩</b>
		Top view	

# 3 Ordering information

**Table 2. Ordering information** 

Type number	Package				
	Name	Description	Version		
BAP65-02	-	plastic surface-mounted package; 2 leads	SOD523		

# 4 Marking

Table 3. Marking code

- table of marking occur					
	Type number	Marking code			
	BAP65-02	K6			

# 5 Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	continuous reverse voltage		-	30	V
I <sub>F</sub>	continuous forward current		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>sp</sub> ≤ 90 °C	-	715	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-65	+150	°C
T <sub>amb</sub>	ambient temperature		-40	+85	°C

## 6 Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		85	K/W

BAP65-02

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## 7 Characteristics

#### **Table 6. Characteristics**

 $T_i$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 50 mA	-	0.9	1.1	V	
I <sub>R</sub>	reverse leakage current	V <sub>R</sub> = 20 V	-	-	20	nA	
C <sub>d</sub>	diode capacitance	f = 1 MHz (see <u>Figure 1</u> )					
		V <sub>R</sub> = 0 V	-	0.65	-	pF	
		V <sub>R</sub> = 1 V	-	0.55	0.9	pF	
		V <sub>R</sub> = 3 V	-	0.5	0.8	pF	
		V <sub>R</sub> = 20 V	-	0.375	-	pF	
r <sub>D</sub>	diode forward resistance	f = 100 MHz (see Figure 2)					
		I <sub>F</sub> = 1 mA	-	1	-	Ω	
		I <sub>F</sub> = 5 mA	[1]	0.65	0.95	Ω	
		I <sub>F</sub> = 10 mA	[1]	0.56	0.9	Ω	
		I <sub>F</sub> = 100 mA	-	0.35	-	Ω	
ISL	isolation	V <sub>R</sub> = 0 V (see <u>Figure 4</u> )			1		
		f = 900 MHz	-	10	-	dB	
		f = 1800 MHz	-	5.8	-	dB	
		f = 2450 MHz	-	4.4	-	dB	
L <sub>ins</sub>	insertion loss	See Figure 3.					
		I <sub>F</sub> = 1 mA					
		f = 900 MHz	-	0.11	-	dB	
		f = 1800 MHz	-	0.13	-	dB	
		f = 2450 MHz	-	0.16	-	dB	
		I <sub>F</sub> = 5 mA					
		f = 900 MHz	-	0.08	-	dB	
		f = 1800 MHz	-	0.11	-	dB	
		f = 2450 MHz	-	0.13	-	dB	
		I <sub>F</sub> = 10 mA					
		f = 900 MHz	-	0.07	-	dB	
		f = 1800 MHz	-	0.1	-	dB	
		f = 2450 MHz	-	0.13	-	dB	
-ins	insertion loss	I <sub>F</sub> = 100 mA		II.	1		
		f = 900 MHz	-	0.07	-	dB	
		f = 1800 MHz	-	0.10	-	dB	
		f = 2450 MHz	-	0.128	-	dB	

## Silicon PIN diode

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
τι	charge carrier life time	when switched from $I_F$ = 10 mA to $I_R$ = 6 mA; $R_L$ = 100 $\Omega$ ; measured at $I_R$ = 3 mA	-	0.17	-	μs
L <sub>S</sub>	series inductance	I <sub>F</sub> = 100 mA; f = 100 MHz	-	0.6	-	nH

<sup>[1]</sup> Guaranteed on AQL basis; inspection level S4, AQL 1.0

Silicon PIN diode

# 8 Graphical data

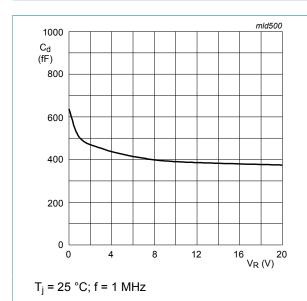
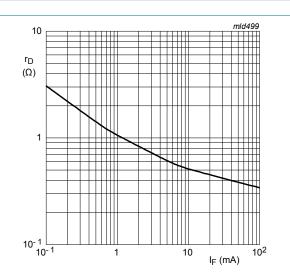
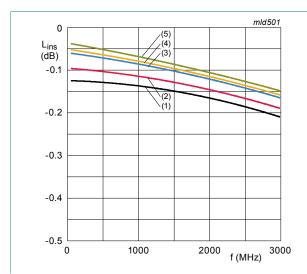


Figure 1. Diode capacitance as a function of reverse voltage (typical values)



 $T_i = 25 \, ^{\circ}C; f = 100 \, MHz.$ 

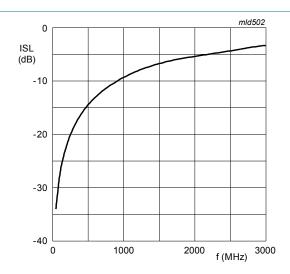
Figure 2. Diode forward resistance as a function of forward current (typical values)



Diode inserted in series with a 50  $\Omega$  strip line circuit and biased via the analyzer T-network. T<sub>amb</sub> = 25 °C.

- (1)  $I_F = 0.5 \text{ mA}$
- (2)  $I_F = 1 \text{ mA}$
- (3)  $I_F = 5 \text{ mA}$
- (4)  $I_F = 10 \text{ mA}$
- (5)  $I_F = 100 \text{ mA}$

Figure 3. Insertion loss of the diode as a function of frequency (typical values)



Diode zero-biased and inserted in series with a 50  $\Omega$  strip line circuit.  $T_{amb}$  = 25  $^{\circ}\text{C}.$ 

Figure 4. Isolation of the diode as a function of frequency (typical values)

Silicon PIN diode

# 9 Package outline

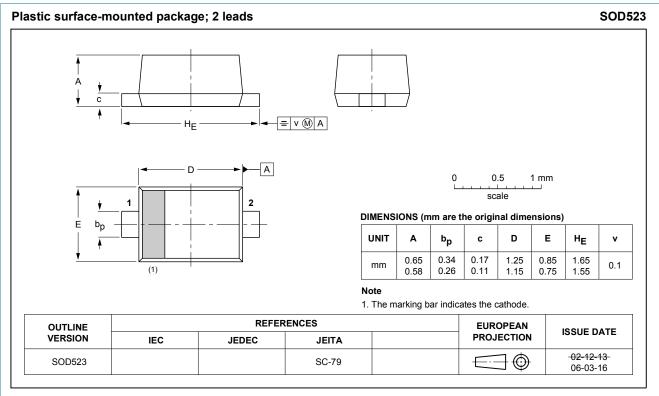


Figure 5. Package outline SOD523

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# 10 Revision history

#### Table 7. Revision history

Tubio 7: Ttoviolon inotory				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BAP65-02 v.6.1	20190128	Product data sheet	-	BAP65-02 v.6
Modifications:	Changed title to 5	Silicon PIN diode		
BAP65-02 v.6	20181211	Product data sheet	-	BAP65-02 v.5
Modifications:		ures and benefits" has be nation" pages have been	•	
BAP65-02 v.5	20100928	Product data sheet	-	BAP65-02 v.4
BAP65-02_N v.4	20080108	Product data sheet	-	BAP65-02 v.3
BAP65-02 v.3 (9397 750 08364)	20010511	Product data sheet	-	BAP65-02 v.2
BAP65-02 v.2 (9397 750 08237)	20010507	Product data sheet	-	BAP65-02 v.1
BAP65-02 v.1 (9397 750 07724)	20001220	Product data sheet	-	-

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### 11 Legal information

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Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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