45 V, 500 mA NPN general-purpose transistors Rev. 7 — 18 June 2018

**Product data sheet** 

#### **Product profile** 1

### 1.1 General description

NPN general-purpose transistors in a small SOT23 Surface-Mounted Device (SMD) plastic package.

#### Table 1. Product overview

Type number	Package	Package I				
	Nexperia	JEDEC	JEITA			
BC817	SOT23	TO-236AB	-	BC807		
BC817-16				BC807-16		
BC817-25				BC807-25		
BC817-40				BC807-40		

#### 1.2 Features and benefits

- High current
- Three current gain selections
- AEC-Q101 qualified

#### **1.3 Applications**

· General-purpose switching and amplification



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### 1.4 Quick reference data

#### Table 2. Quick reference data

*T<sub>amb</sub>* = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
lc	collector current			-	-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	1	Α
h <sub>FE</sub>	BC817	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	100	-	600	
	BC817-16		[1]	100	-	250	
	BC817-25		[1]	160	-	400	
	BC817-40		[1]	250	-	600	

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

### 2 Pinning information

Table 3. Pinning				
Pin	Symbol	Description	Simplified outline	Graphic symbol
SOT23				
1	В	base		
2	E	emitter	3	С
3	С	collector		в
				E
			1 2	sym123

## **3 Ordering information**

#### Table 4. Ordering information

Type number	Package					
	Name	Description	Version			
BC817	TO-236AB	Plastic surface-mounted package; 3 leads	SOT23			
BC817-16						
BC817-25						
BC817-40						

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#### Marking 4

Table 5. Marking						
Type number		Marking code				
BC817	[1]	6D%				
BC817-16	[1]	6A%				
		6B%				
BC817-40	[1]	6C%				

[1] % = placeholder for manufacturing site code

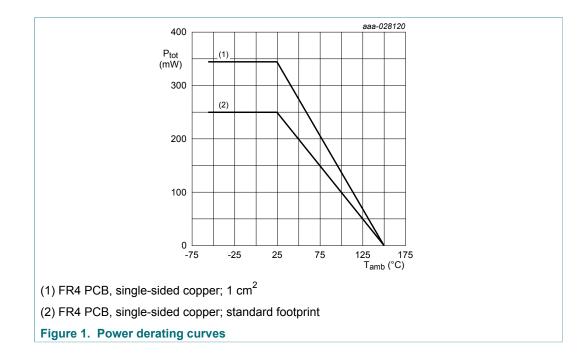
#### **Limiting values** 5

#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	open emitter		50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	single pulse; $t_p \le 1 \text{ ms}$ .		1	А
I <sub>BM</sub>	peak base current	single pulse; $t_p \le 1 \text{ ms}$	single pulse; $t_p \le 1 \text{ ms}$ -		200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1] [2]	-	250	mW
			[3] [2]	-	345	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
 Valid for all available selection groups.
 Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



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#### **Thermal characteristics** 6

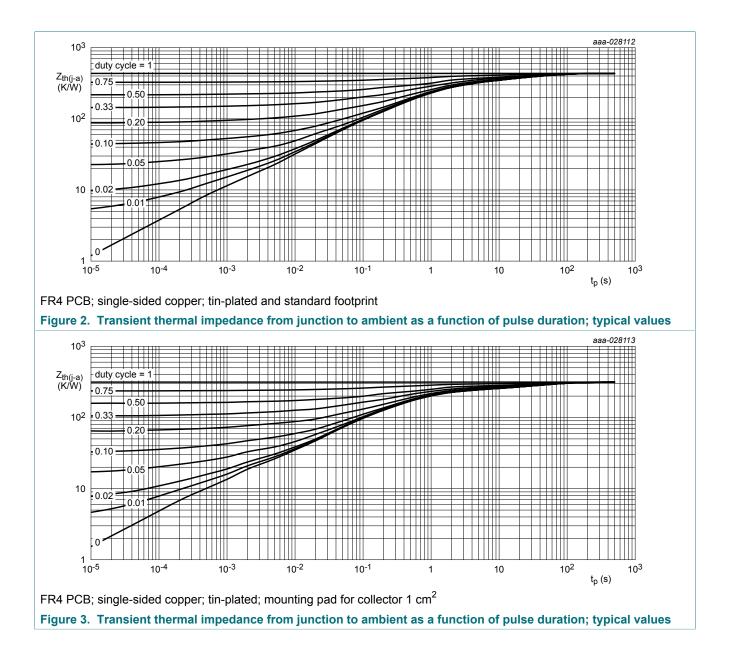
#### **Table 7. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction	in free air	[1] [2]	-	-	500	K/W
	to ambient		[3] [2]	-	-	362	K/W

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

Valid for all available selection groups.

[2] Valid for all available selection groups.
 [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



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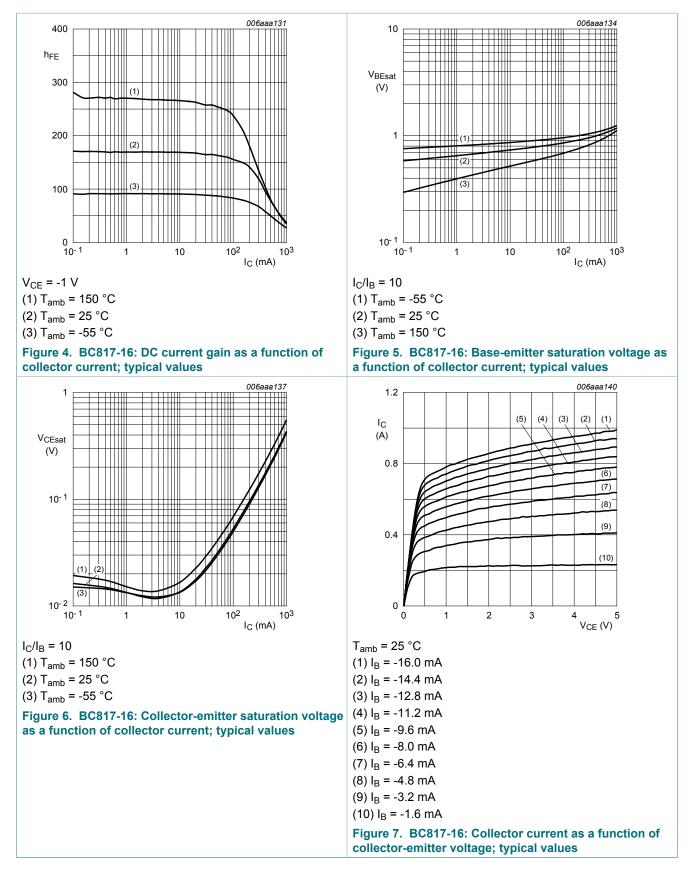
#### **Characteristics** 7

#### Table 8. Characteristics

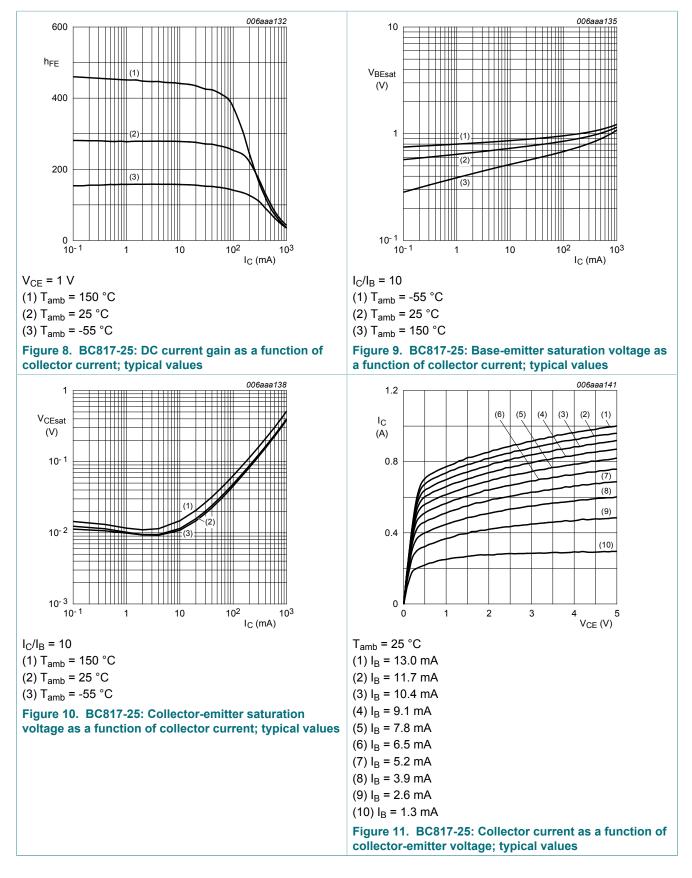
 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μΑ; I <sub>E</sub> = 0 Α		50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0 A		45	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μΑ; I <sub>C</sub> = 0 Α		5	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 20 V; I <sub>E</sub> = 0 A		-	-	100	nA
	cut-off current	V <sub>CB</sub> = 20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A		-	-	100	nA
h <sub>FE</sub>	DC current gain			I			
	BC817	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	100	-	600	
	BC817-16	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	100	-	250	
	BC817-25	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	160	-	400	
	BC817-40	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA	[1]	250	-	600	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	700	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1] [2]	-	-	1.2	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz		100	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	3	-	pF

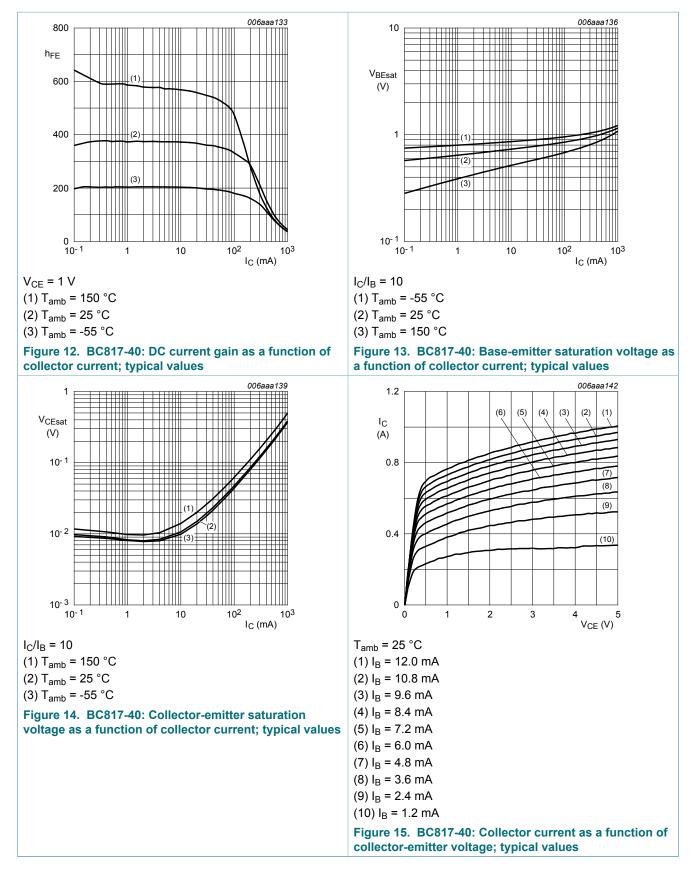
# **BC817 series**



# **BC817 series**



# **BC817 series**



45 V, 500 mA NPN general-purpose transistors

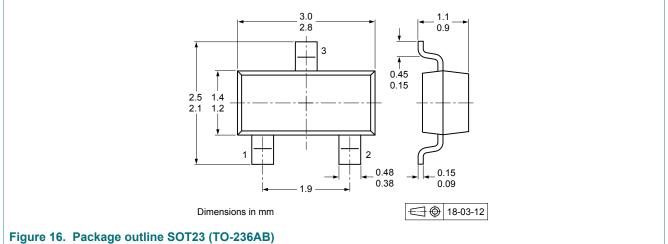
### 8 Test information

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

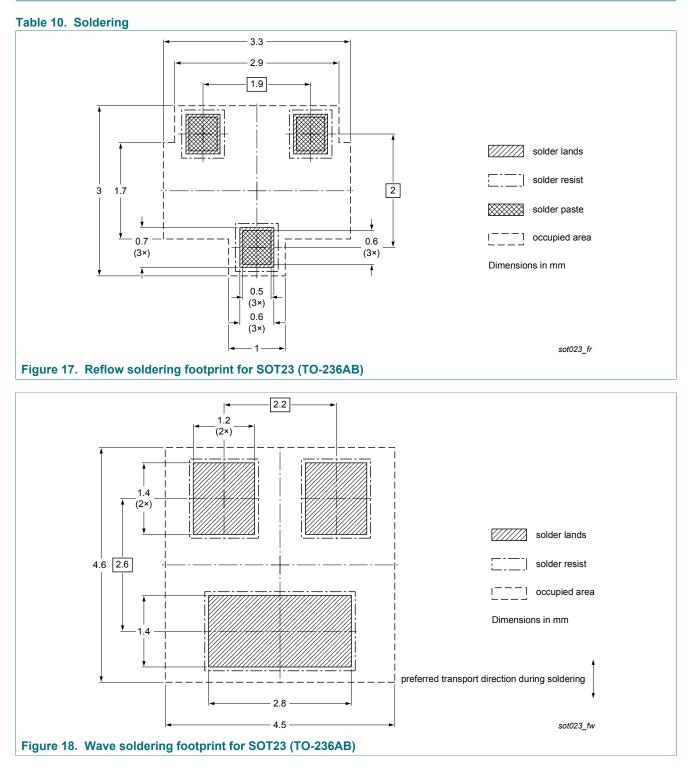
### 9 Package outline

#### Table 9. Package outline



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### **10 Soldering**



BC817\_SER
Product data sheet

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### **11 Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BC817 v.7	20180618	Product data sheet	-	BC817_BC817W_BC337 v.6		
Modifications:	Nexperia. • Legal text • Removed • Added Fig Fig 2. and • Graphs in • Added se	s have been adapted to the ne basic types: BC327 and BC80 g 1. Power derating curves in s I Fig 3. in section "Thermal cha section "Characteristics" are s ctions 8 "Test information" and Section "Packing information"	istics" are sorted in new order. nation" and 9 "Soldering".			
BC817_BC817W_BC337 v.6	20091117	Product data sheet	-	BC817_BC817W_BC337 v.5		
BC817_BC817W_BC337 v.5	20050221	Product data sheet	CPCN200302007F CPCN200405006F	BC817 v.4; BC817W_SER v.4; BC337 v.3		
BC817 v.4	20040116	Product Specification	-	BC817 v.3		
BC817W_SER v.4	20040225	Product Specification	-	BC817W_SER v.3		
BC337 v.3	19990415	Product Specification		BC337 338 CNV v.2		

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### **12 Legal information**

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

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BC817 SER **Product data sheet** 

#### 45 V, 500 mA NPN general-purpose transistors

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## **BC817 series**

45 V, 500 mA NPN general-purpose transistors

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Date of release: 18 June 2018 Document identifier: BC817\_SER

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