

Product data sheet

1. Product profile

1.1 General description

The BF1107 is a depletion type field-effect transistor in a SOT23 package. The low loss and high isolation capabilities of this MOSFET provide excellent RF switching functions. Integrated diodes between gate and source and between gate and drain protect against excessive input voltage surges. Drain and source are interchangeable.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

Currentless RF switch

1.3 Applications

- Various RF switching applications such as:
 - Passive loop through for VCR tuner
 - Transceiver switching

1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
L _{ins(on)}	on-state insertion loss	$V_{SG} = V_{DG} = 0 V;$ f = 50 MHz to 860 MHz				
		$R_{S} = R_{L} = 50 \ \Omega$	-	-	2.5	dB
		$R_{S} = R_{L} = 75 \ \Omega$	-	-	3.5	dB
ISL _{off}	off-state isolation	$V_{SG} = V_{DG} = 5 V;$ f = 50 MHz to 860 MHz				
		$R_S = R_L = 50 \Omega$	30	-	-	dB
		$R_{S} = R_{L} = 75 \Omega$	30	-	-	dB
R _{DSon}	drain-source on-state resistance	$V_{GS} = 0 V; I_D = 1 mA$	-	12	20	Ω
V _{GS(p)}	gate-source pinch-off voltage	V_{DS} = 1 V; I_D = 20 μ A	-	-3	-4.5	V



2. Pinning information

Table 2.	Discrete pinning			
Pin	Description		Simplified outline	Symbol
1	drain	<u>[1]</u>		
2	source	<u>[1]</u>		1
3	gate			3 — — 2 sym120

[1] Drain and source are interchangeable

3. Ordering information

Table 3. Orde	ering infor	mation		
Type number Package				
	Name	Description	Version	
BF1107	-	plastic surface-mounted package; 3 leads	SOT23	

4. Marking

Table 4. Marking	
Type number	Marking code
BF1107	S3p

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

		0) (,		
Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage		-	3	V
V _{SD}	source-drain voltage		-	3	V
V_{DG}	drain-gate voltage		-	7	V
V _{SG}	source-gate voltage		-	7	V
I _D	drain current		-	10	mA
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C

6. Thermal characteristics

Table 6.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		[1] 260	K/W

[1] Soldering point of the gate lead.

7. Static characteristics

Table 7. <i>T_j</i> = 25 ° <i>C</i> .	Static characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)GSS}	gate-source breakdown voltage	$V_{DS} = 0 \text{ V}; \text{ I}_{GS} = 0.1 \text{ mA}$	7	-	-	V
V _{GS(p)}	gate-source pinch-off voltage	V_{DS} = 1 V; I _D = 20 µA	-	-3	-4.5	V
I _{DSX}	drain cut-off current	V_{GS} = -5 V; V_{DS} = 2 V	-	-	10	μΑ
I _{GSS}	gate leakage current	$V_{GS} = -5 \text{ V}; V_{DS} = 0 \text{ V}$	-	-	100	nA

8. Dynamic characteristics

Table 8. Dynamic characteristics

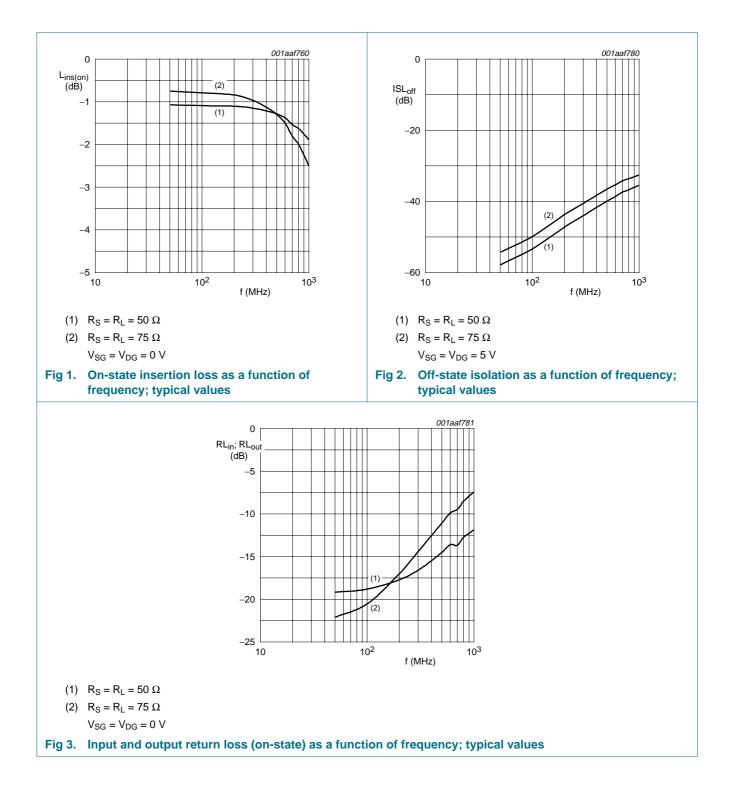
Common gate; $T_{amb} = 25 \circ C$.

Parameter	Conditions	Min	Тур	Max	Unit
L _{ins(on)} on-state insertion loss	V_{SG} = V_{DG} = 0 V; f = 50 MHz to 860 MHz				
	$R_{S} = R_{L} = 50 \ \Omega$	-	-	2.5	dB
	$R_{S} = R_{L} = 75 \ \Omega$	-	-	3.5	dB
off-state isolation	V_{SG} = V_{DG} = 5 V; f = 50 MHz to 860 MHz				
	$R_{S} = R_{L} = 50 \ \Omega$	30	-	-	dB
	$R_{S} = R_{L} = 75 \ \Omega$	30	-	-	dB
drain-source on-state resistance	$V_{GS} = 0 \text{ V}; \text{ I}_D = 1 \text{ mA}$	-	12	20	Ω
input capacitance at gate	f = 1 MHz				
	$V_{SG} = V_{DG} = 5 V$	-	0.9	-	pF
	$V_{SG} = V_{DG} = 0 V$	-	1.5	2	pF
output capacitance at gate	f = 1 MHz				
	$V_{SG} = V_{DG} = 5 V$	-	0.9	-	pF
	$V_{SG} = V_{DG} = 0 V$	-	1.5	2	рF
	on-state insertion loss off-state isolation drain-source on-state resistance input capacitance at gate	$\begin{array}{l} \text{on-state insertion loss} & \frac{V_{SG} = V_{DG} = 0 \text{ V; } \text{f} = 50 \text{ MHz to 860 \text{ MHz}}}{R_S = R_L = 50 \ \Omega} \\ R_S = R_L = 75 \ \Omega} \\ \\ \text{off-state isolation} & \frac{V_{SG} = V_{DG} = 5 \text{ V; } \text{f} = 50 \text{ MHz to 860 \text{ MHz}}}{R_S = R_L = 50 \ \Omega} \\ R_S = R_L = 50 \ \Omega} \\ R_S = R_L = 75 \ \Omega} \\ \\ \text{drain-source on-state} \\ \text{resistance}} & \frac{V_{GS} = 0 \text{ V; } \text{I}_D = 1 \text{ mA}}{R_S = R_L = 75 \ \Omega} \\ \\ \text{drain-source on-state} \\ \text{resistance}} & \frac{\text{f} = 1 \text{ MHz}}{V_{SG} = V_{DG} = 5 \text{ V}} \\ \hline V_{SG} = V_{DG} = 5 \text{ V} \\ \hline V_{SG} = V_{DG} = 5 \text{ V} \\ \hline V_{SG} = V_{DG} = 5 \text{ V}} \\ \end{array}$	$\label{eq:state insertion loss} \begin{split} V_{SG} = V_{DG} = 0 \ V; \ f = 50 \ MHz \ to \ 860 \ MHz} & R_S = R_L = 50 \ \Omega & -1 \\ R_S = R_L = 75 \ \Omega & -1 \\ R_S = R_L = 75 \ \Omega & -1 \\ \hline R_S = R_L = 75 \ \Omega & R_S = R_L = 50 \ \Omega & R_S = R_L = 75 \ \Omega & R_S = 0 \ V; \ I_D = 1 \ MA & R_S = R_L = 75 \ \Omega & R_S = 0 \ V; \ I_D = 1 \ MHz & R_S = R_L = 75 \ R_S = V_{DG} = 5 \ V & R_S = 0 \ V; \ R_S = 0 \ V; \ R_S = V_{DG} = 5 \ V & R_S = 0 \ V; \ R_S = 0 \ R_S = 0 \ V; \ R_S = 0 \$	$ \begin{array}{c} \text{on-state insertion loss} & \frac{V_{SG} = V_{DG} = 0 \ \ V; \ f = 50 \ \ \ MHz \ to \ 860 \ \ MHz} \\ \hline R_S = R_L = 50 \ \ \Omega & - & - \\ \hline R_S = R_L = 75 \ \Omega & - & - \\ \hline R_S = R_L = 75 \ \Omega & MHz \ \ to \ 860 \ \ \ MHz} \\ \hline R_S = R_L = 50 \ \ \Omega & - \\ \hline R_S = R_L = 50 \ \Omega & 30 & - \\ \hline R_S = R_L = 75 \ \Omega & 30 & - \\ \hline R_S = R_L = 75 \ \Omega & 30 & - \\ \hline R_S = R_L = 75 \ \Omega & 30 & - \\ \hline R_S = R_L = 75 \ \Omega & 30 & - \\ \hline R_S = R_L = 75 \ \Omega & 30 & - \\ \hline R_S = R_L = 75 \ \Omega & - \\ \hline R_S = R_L = 75 \ R$	$ \begin{array}{ c c c c } \mbox{on-state insertion loss} & V_{SG} = V_{DG} = 0 \ V; \ f = 50 \ MHz \ to \ 860 \ MHz \\ \hline R_S = R_L = 50 \ \Omega & - & - & 2.5 \\ \hline R_S = R_L = 75 \ \Omega & - & - & 3.5 \\ \hline \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$

NXP Semiconductors

N-channel single gate MOSFET

BF1107



N-channel single gate MOSFET

BF1107

9. Package outline

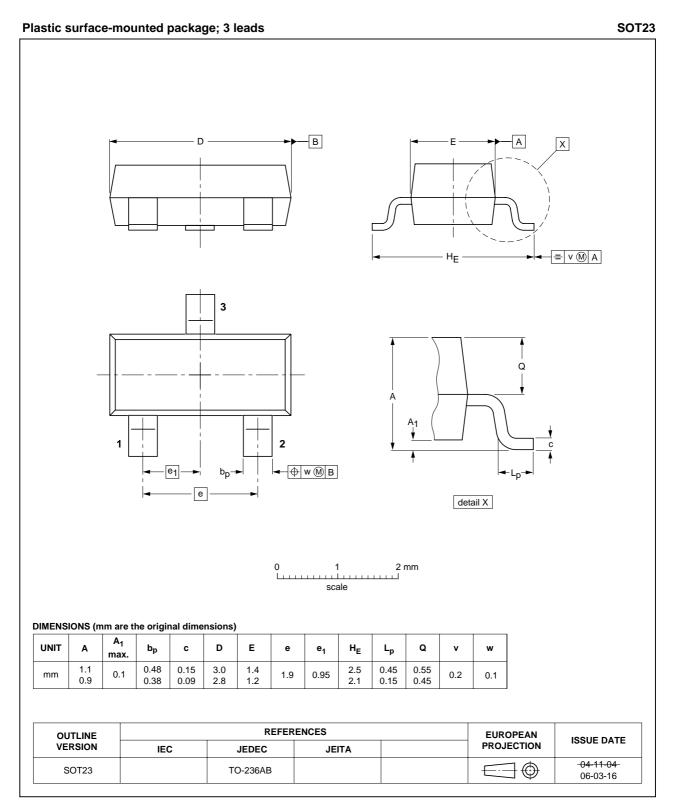


Fig 4. Package outline SOT23

10. Abbreviations

Table 9. Ab	breviations
Acronym	Description
MOSFET	Metal-Oxide Semiconductor Field-Effect Transistor
RF	Radio Frequency
VCR	Videocassette Recorder

11. Revision history

Table 10.Revision history

Release date 20070109	Data sheet status Product data sheet	Change notice	Supersedes
20070109	Product data sheat		
	r Touuci uala sheel	-	BF1107_1107W_3
	of this data sheet has been r of NXP Semiconductors.	edesigned to comply v	vith the new identity
 Legal texts 	have been adapted to the ne	w company name whe	ere appropriate.
•	•	mply with the current	guidelines of NXP
 Product type 	e BF1107W has been remov	ed from this data shee	et.
19990514	Product data sheet	-	BF1107_2
19980622	Product data sheet	-	BF1107_N_1
19980407	Preliminary data sheet	-	-
	 Legal texts Symbol not Semicondu Product typ 19990514 19980622 	 Symbol notation has been adapted to conservation service of the service	 Legal texts have been adapted to the new company name whe Symbol notation has been adapted to comply with the current semiconductors. Product type BF1107W has been removed from this data sheet 19990514 Product data sheet - 19980622 Product data sheet -

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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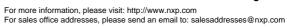
14. Contents

1	Product profile 1
1.1	General description
1.2	Features 1
1.3	Applications 1
1.4	Quick reference data
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values 2
6	Thermal characteristics 3
7	Static characteristics 3
8	Dynamic characteristics 3
9	Package outline 5
10	Abbreviations 6
11	Revision history 6
12	Legal information 7
12.1	Data sheet status 7
12.2	Definitions
12.3	Disclaimers
12.4	Trademarks 7
13	Contact information 7
14	Contents 8

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