DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1999 Jun 29

2000 Jan 05



BF862

FEATURES

- High transition frequency for excellent sensitivity in AM car radios
- High transfer admittance.

APPLICATIONS

• Pre-amplifiers in AM car radios.

DESCRIPTION

Silicon N-channel symmetrical junction field-effect transistor in a SOT23 package. Drain and source are interchangeable.

PINNING SOT23

PIN	DESCRIPTION
1	source
2	drain
3	gate



Marking code: 2Ap.

Fig.1 Simplified outline and symbol.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{DS}	drain-source voltage		_	-	20	V
V _{GSoff}	gate-source cut-off voltage		-0.3	-0.8	-1.2	V
I _{DSS}	drain-source current		10	-	25	mA
P _{tot}	total power dissipation	$T_s \le 90 \ ^\circ C$	-	-	300	mW
y _{fs}	transfer admittance		35	45	-	mS
Tj	junction temperature		_	_	150	°C

CAUTION

This product is supplied in anti-static packing to prevent damage caused by electrostatic discharge during transport and handling.

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{DS}	drain-source voltage		_	20	V
V _{DG}	drain-gate voltage		_	20	V
V _{GS}	gate-source voltage		-	-20	V
I _{DS}	drain-source current		-	40	mA
I _G	forward gate current		_	10	mA
P _{tot}	total power dissipation	$T_s \le 90 \ ^\circ C$; note 1	-	300	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C

Note

1. Main heat transfer is via the gate lead.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	note 1	200	K/W

Note

1. Soldering point of the gate lead.



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STATIC CHARACTERISTICS

 $T_j = 25 \ ^{\circ}C$; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{(BR)GSS}	gate-source breakdown voltage	$I_{GS} = -1 \ \mu A; V_{DS} = 0$	-20	-	-	V
V _{GS}	gate-source forward voltage	$V_{DS} = 0; I_G = 1 \text{ mA}$	-	-	1	V
V _{GSoff}	gate-source cut-off voltage	V_{DS} = 8 V; I_D = 1 μ A	-0.3	-0.8	-1.2	V
I _{GSS}	reverse gate current	$V_{GS} = -15 \text{ V}; V_{DS} = 0$	-	-	-1	nA
I _{DSS}	drain-source current	$V_{GS} = 0; V_{DS} = 8 V$	10	_	25	mA

DYNAMIC CHARACTERISTICS

Common source; $T_{amb} = 25 \text{ °C}$; $V_{GS} = 0$; $V_{DS} = 8 \text{ V}$; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
y _{fs}	common source forward transfer admittance	T _j = 25 °C	35	45	_	mS
9 _{os}	common source output conductance	T _j = 25 °C	_	180	400	μS
C _{iss}	input capacitance	f = 1 MHz	-	10	-	pF
C _{rss}	reverse transfer capacitance	f = 1 MHz	_	1.9	-	pF
e _n	equivalent noise input voltage	f = 100 kHz	-	0.8	-	nV/√Hz
f _T	transition frequency		_	715	-	MHz

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N-channel junction FET







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N-channel junction FET

PACKAGE OUTLINE



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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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