

1. General description

Planar passivated SCR with faster switching performance and sensitive gate in a SOT223 surface mounted plastic package. This SCR with enhanced commutation performance is also designed to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

2. Features and benefits

- Fast commutation performance for higher frequency operation
- · Full wave rectified AC applications
- Sensitive gate
- Direct triggering from microcontrollers, low power drivers and logic ICs

3. Applications

- Earth leakage circuit breakers (ELCB/GFI)
- Ignition circuits (gas appliances, small engines and HID lighting)

4. Quick reference data

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{RRM}	repetitive peak reverse voltage		-	-	600	V
I _{T(AV)}	average on-state current	half sine wave; $T_{sp} \le 112 \text{ °C}$; Fig. 1	-	-	0.63	А
I _{T(RMS)}	RMS on-state current	half sine wave; T _{sp} ≤ 112 °C; <u>Fig. 2;</u> <u>Fig. 3</u>	-	-	1	A
I _{TSM}	non-repetitive peak on- state current	half sine wave; T _{j(init)} = 25 °C; t _p = 10 ms; <u>Fig. 4; Fig. 5</u>	-	-	8	A
		half sine wave; T _{j(init)} = 25 °C; t _p = 8.3 ms	-	-	9	A
Tj	junction temperature		-	-	125	°C
Static chara	acteristics	1				
I _{GT}	gate trigger current	V _D = 12 V; I _T = 10 mA; T _j = 25 °C; Fig. 9	70	200	450	μA
Dynamic ch	naracteristics	·				
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T _j = 125 °C; R _{GK} = 1 kΩ; (V_{DM} = 67% of V_{DRM}); exponential waveform; Fig. 11	350	800	-	V/µs

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V_{DM} = 402 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit; Fig. 11	-	25	-	V/µs

5. Pinning information

Table 2.	Pinning in	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode	4	А - Д К
2	А	anode		Ğ sym037
3	G	gate		symosi
4	mb	mb; connected to anode	☐1	

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
BT168GWF	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223				

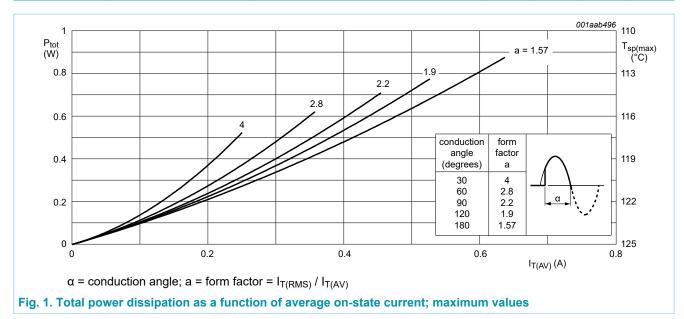


7. Limiting values

Table 4. Limiting values

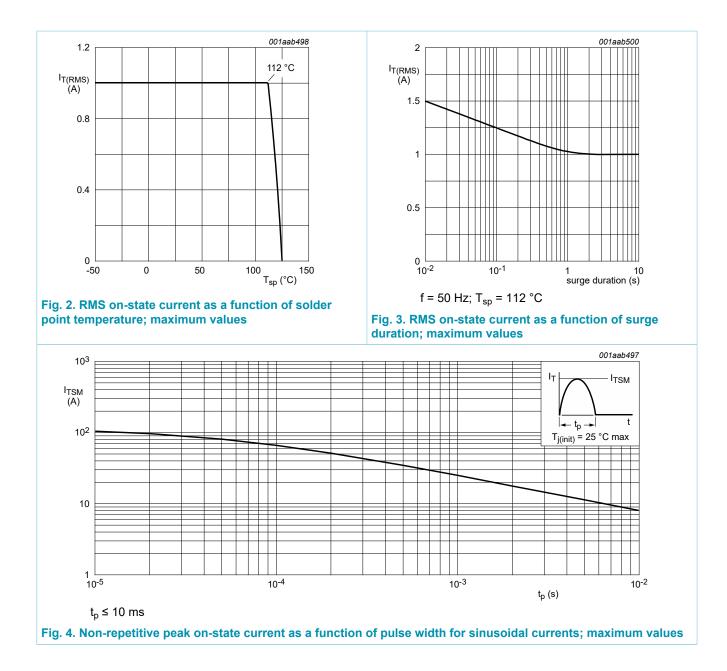
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	600	V
V _{RRM}	repetitive peak reverse voltage		-	600	V
I _{T(AV)}	average on-state current	half sine wave; T _{sp} ≤ 112 °C; <u>Fig. 1</u>	-	0.63	А
I _{T(RMS)}	RMS on-state current	half sine wave; $T_{sp} \le 112 \text{ °C}$; Fig. 2; Fig. 3	-	1	А
I _{TSM}	non-repetitive peak on- state current	half sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 10 \text{ ms}$; Fig. 4; Fig. 5	-	8	A
		half sine wave; T _{j(init)} = 25 °C; t _p = 8.3 ms	-	9	А
l ² t	I ² t for fusing	t _p = 10 ms; SIN	-	0.32	A²s
dl _T /dt	rate of rise of on-state current	I _T = 2 A; I _G = 10 mA; dI _G /dt = 100 mA/μs	-	50	A/µs
I _{GM}	peak gate current		-	1	А
V _{RGM}	peak reverse gate voltage		-	5	V
P _{GM}	peak gate power		-	2	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.1	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C



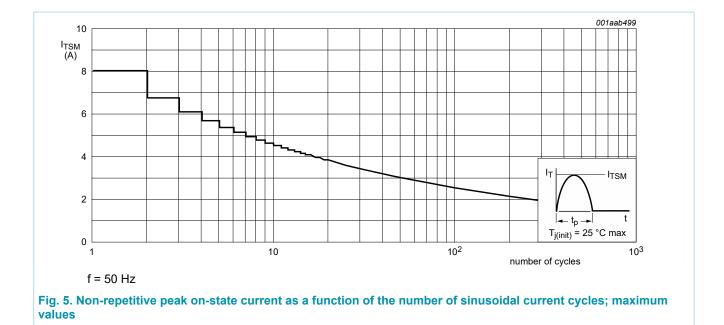
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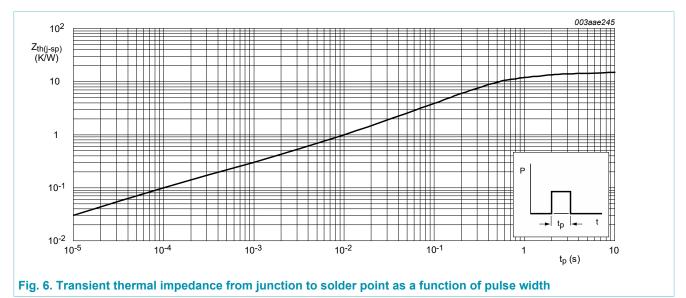
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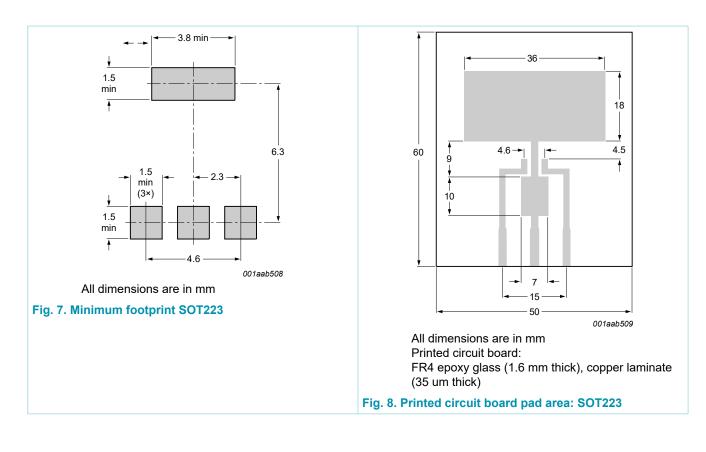
8. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point	Fig. 6	-	-	15	K/W
R _{th(j-a)}	thermal resistance from junction to	printed-circuit board mounted; minimum footprint; Fig. 7	-	156	-	K/W
	ambient free air	printed-circuit board mounted; pad area; <u>Fig. 8</u>	-	70	-	K/W



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Product data sheet

9. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	· · · · · ·				
I _{GT}	gate trigger current	V _D = 12 V; I _T = 10 mA; T _j = 25 °C; <u>Fig. 9</u>	70	200	450	μA
IL	latching current	$\label{eq:VD} \begin{array}{l} V_{D} = 12 \; V; \; I_{G} = 0.5 \; mA; \; T_{j} = 25 \; ^{\circ}C; \\ R_{GK(ext)} = 1 \; k \Omega \end{array}$	3	7.5	13	mA
I _H	holding current	V_D = 12 V; T _j = 25 °C; R _{GK(ext)} = 1 k Ω	0.5	4.1	10	mA
V _T	on-state voltage	I _T = 1.2 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.35	1.7	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 10 mA; T _j = 25 °C	-	0.5	0.8	V
		V _D = 600 V; I _T = 10 mA; T _j = 125 °C	0.2	0.3	-	V
ID	off-state current	V _D = 600 V; R _{GK(ext)} = 1 kΩ; T _j = 125 °C	-	0.05	0.1	mA
I _R	reverse current	V_R = 600 V; T _j = 125 °C; $R_{GK(ext)}$ = 1 k Ω	-	0.05	0.1	mA
Dynamic ch	aracteristics	· · · · · · · · · · · · · · · · · · ·				
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T _j = 125 °C; R _{GK} = 1 kΩ; (V_{DM} = 67% of V_{DRM}); exponential waveform; Fig. 11	350	800	-	V/µs
		V_{DM} = 402 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit; Fig. 11	 -	25	-	V/µs

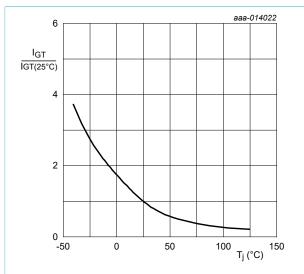


Fig. 9. Normalized gate trigger current as a function of junction temperature

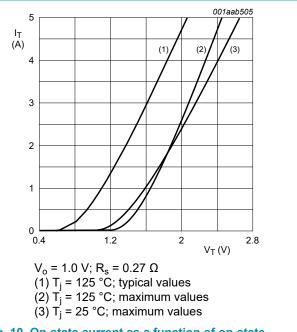
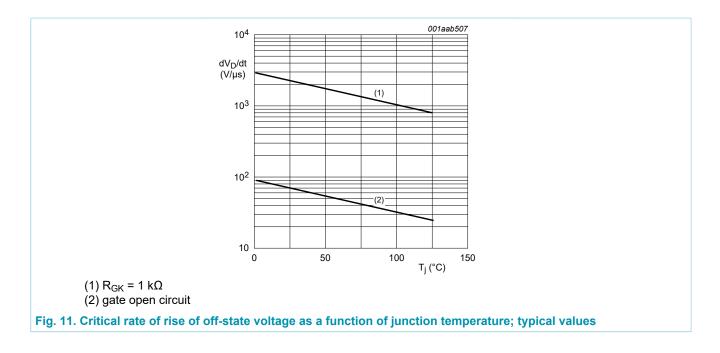


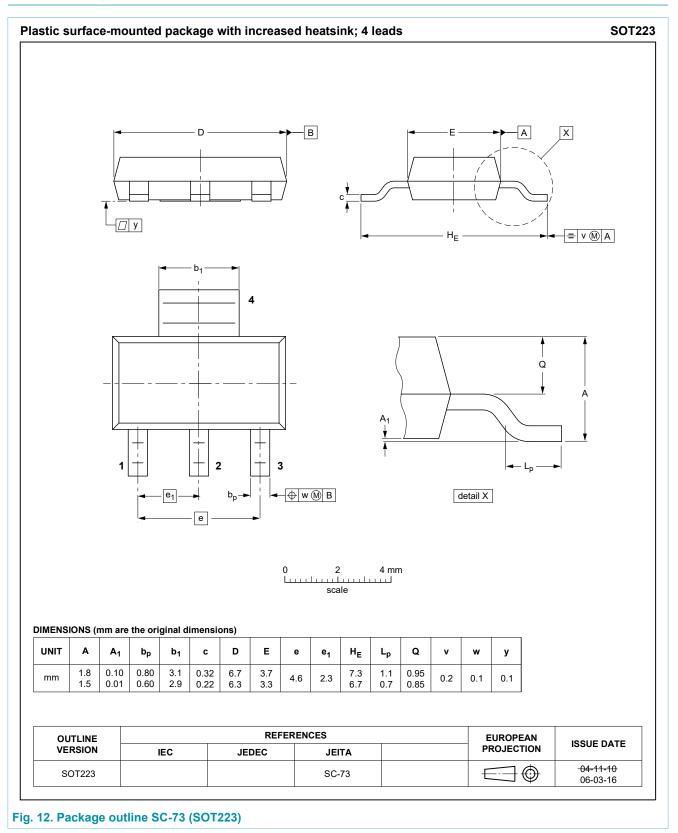
Fig. 10. On-state current as a function of on-state voltage

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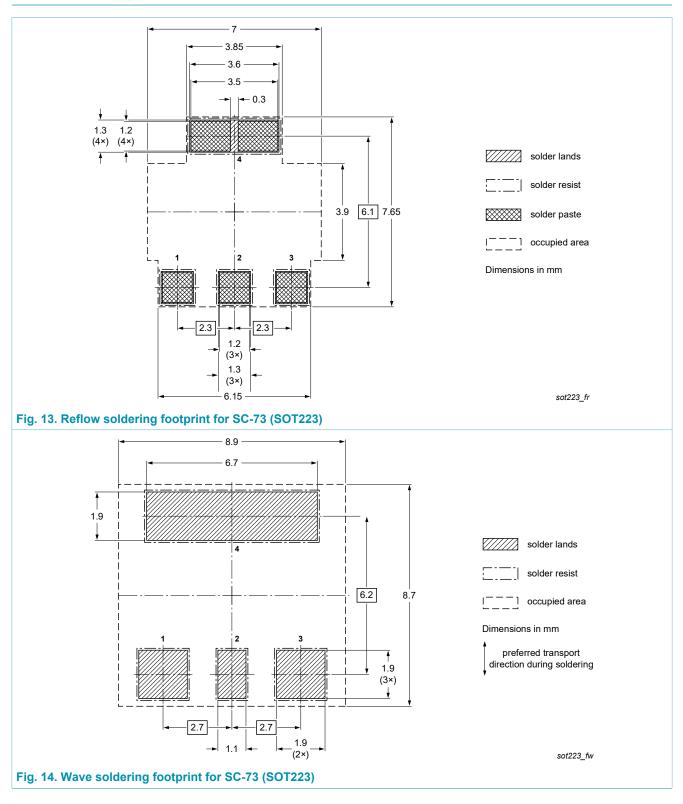
10. Package outline



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11. Soldering



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12. Legal information

Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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