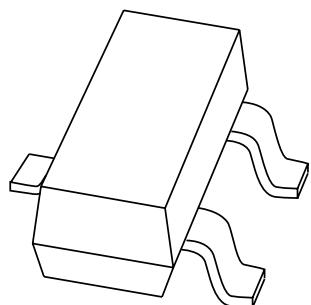


DATA SHEET



PBR941B
UHF wideband transistor

Preliminary specification

2001 Jan 18

**Philips
Semiconductors**



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UHF wideband transistor**PBR941B****FEATURES**

- Small size
- Low noise
- Low distortion
- High gain
- Gold metallization ensures excellent reliability.

APPLICATIONS

- Communication and instrumentation systems.

DESCRIPTION

Silicon NPN transistor in a surface mount 3-pin SOT23 package. The transistor is primarily intended for wideband applications in the GHz range in the RF front end of analog and digital cellular telephones, cordless phones, radar detectors, pagers and satellite TV-tuners.

PINNING SOT23

PIN	DESCRIPTION
1	base
2	emitter
3	collector

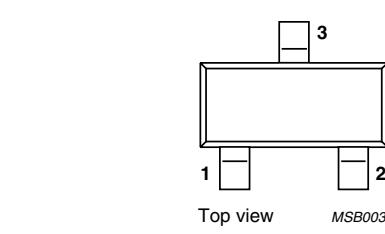


Fig.1 Simplified outline (SOT23).

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
C_{re}	feedback capacitance	$I_C = 0$; $V_{CB} = 6$ V; $f = 1$ MHz	–	0.3	–	pF
f_T	transition frequency	$I_C = 15$ mA; $V_{CE} = 6$ V; $f_m = 1$ GHz	7	9	–	GHz
G_{UM}	maximum unilateral power gain	$I_C = 15$ mA; $V_{CE} = 6$ V; $T_{amb} = 25$ °C; $f = 1$ GHz	–	16	–	dB
NF	noise figure	$\Gamma_S = \Gamma_{opt}$; $I_C = 5$ mA; $V_{CE} = 6$ V; $f = 1$ GHz	–	1.5	2.5	dB
P_{tot}	total power dissipation	$T_s = 60$ °C; note 1	–	–	360	mW
$R_{th\ j-s}$	thermal resistance from junction to soldering point		–	–	320	K/W

Note

1. T_s is the temperature at the soldering point of the collector pin.

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	20	V
V_{CEO}	collector-emitter voltage	open base	–	10	V
V_{EBO}	emitter-base voltage	open collector	–	1.5	V
I_C	collector current (DC)		–	50	mA
$I_{C(AV)}$	average collector current		–	50	mA
P_{tot}	total power dissipation	$T_s = 60^\circ\text{C}$; note 1	–	360	mW
T_{stg}	storage temperature		–65	+150	$^\circ\text{C}$
T_j	junction temperature		–	150	$^\circ\text{C}$

Note

1. T_s is the temperature at the soldering point of the collector pin.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th\ j-s}$	thermal resistance from junction to soldering point	320	K/W

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CHARACTERISTICS $T_j = 25^\circ\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
DC characteristics						
$V_{(\text{BR})\text{CBO}}$	collector-base breakdown voltage	$I_C = 100 \mu\text{A}; I_E = 0$	20	—	—	V
$V_{(\text{BR})\text{CEO}}$	collector-emitter breakdown voltage	$I_C = 100 \mu\text{A}; I_B = 0$	10	—	—	V
$V_{(\text{BR})\text{EBO}}$	emitter-base breakdown voltage	$I_E = 10 \mu\text{A}; I_C = 0$	1.5	—	—	V
V_{BEF}	forward base-emitter voltage	$I_E = 25 \text{ mA}$	—	—	1.05	V
I_{CBO}	collector-base leakage current	$V_{\text{CB}} = 10 \text{ V}; I_E = 0$	—	—	100	nA
I_{EBO}	emitter-base leakage current	$V_{\text{EB}} = 1 \text{ V}; I_C = 0$	—	—	100	nA
h_{FE}	DC current gain	$I_C = 5 \text{ mA}; V_{\text{CE}} = 6 \text{ V}$	100	150	200	
		$I_C = 15 \text{ mA}; V_{\text{CE}} = 6 \text{ V}$	—	150	—	
AC characteristics						
C_{re}	feedback capacitance	$I_C = 0; V_{\text{CB}} = 6 \text{ V}; f = 1 \text{ MHz}$	—	0.3	—	pF
f_T	transition frequency	$I_C = 15 \text{ mA}; V_{\text{CE}} = 6 \text{ V}; f_m = 1 \text{ GHz}$	7	9	—	GHz
$ s_{21} ^2$	insertion gain	$I_C = 15 \text{ mA}; V_{\text{CE}} = 6 \text{ V}; f = 1 \text{ GHz}$	13	15	—	dB
G_{UM}	maximum unilateral power gain; note 1	$I_C = 15 \text{ mA}; V_{\text{CE}} = 6 \text{ V};$ $T_{\text{amb}} = 25^\circ\text{C}; f = 1 \text{ GHz}$	—	16	—	dB
		$I_C = 15 \text{ mA}; V_{\text{CE}} = 6 \text{ V};$ $T_{\text{amb}} = 25^\circ\text{C}; f = 2 \text{ GHz}$	—	10	—	dB
NF	noise figure	$\Gamma_S = \Gamma_{\text{opt}}; I_C = 5 \text{ mA}; V_{\text{CE}} = 6 \text{ V};$ $f = 1 \text{ GHz}$	—	1.5	2.5	dB
		$\Gamma_S = \Gamma_{\text{opt}}; I_C = 5 \text{ mA}; V_{\text{CE}} = 6 \text{ V};$ $f = 2 \text{ GHz}$	—	2.1	—	dB

Note

1. G_{UM} is the maximum unilateral power gain, assuming s_{12} is zero. $G_{\text{UM}} = 10 \log \frac{|s_{21}|^2}{(1 - |s_{11}|^2)(1 - |s_{22}|^2)} \text{ dB}$

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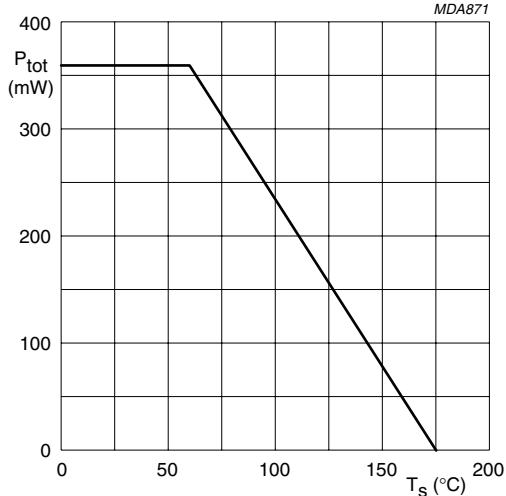
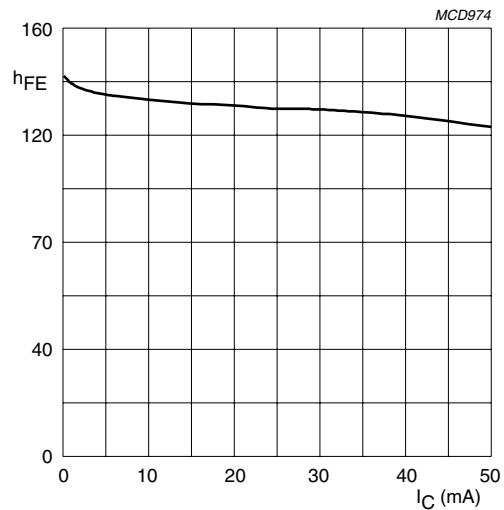
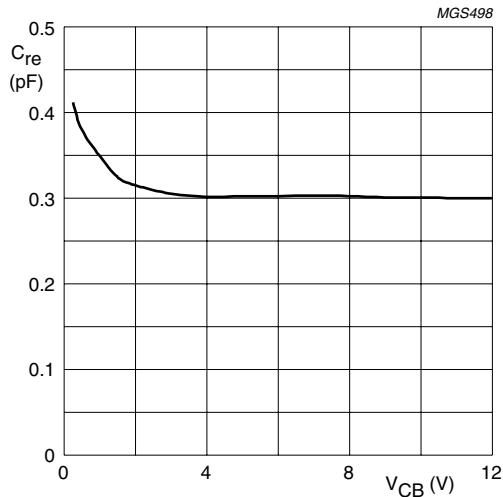


Fig.2 Power derating as a function of soldering point temperature.



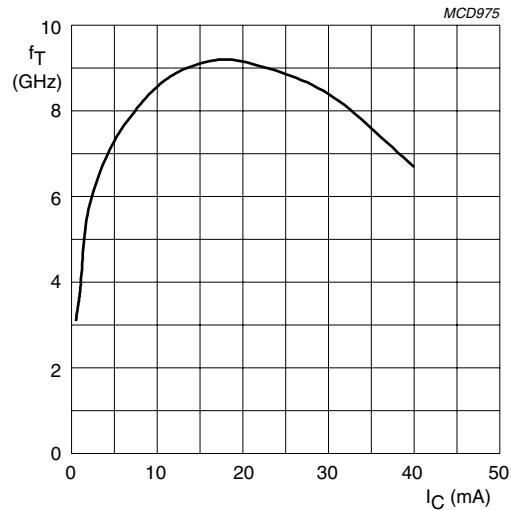
$V_{CE} = 6$ V.

Fig.3 DC current gain as a function of collector current; typical values.



$I_C = I_c = 0$; $f = 1$ MHz.

Fig.4 Feedback capacitance as a function of collector-base voltage; typical values.

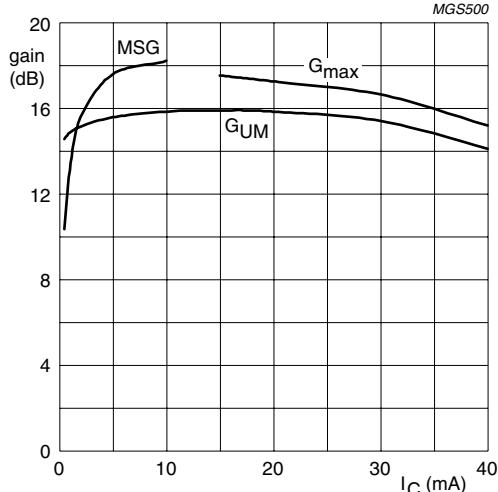


$V_{CE} = 6$ V; $f_m = 1$ GHz; $T_{amb} = 25$ °C.

Fig.5 Transition frequency as a function of collector current; typical values.

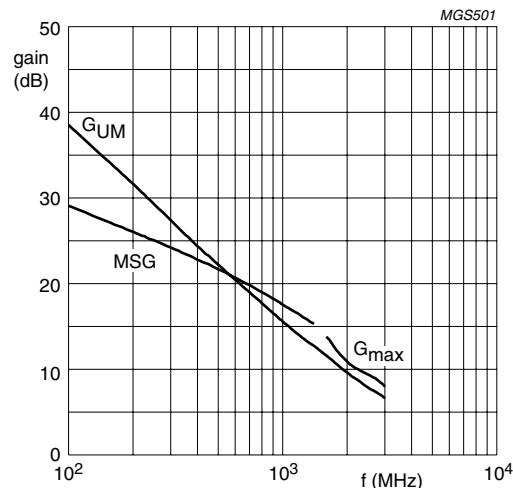
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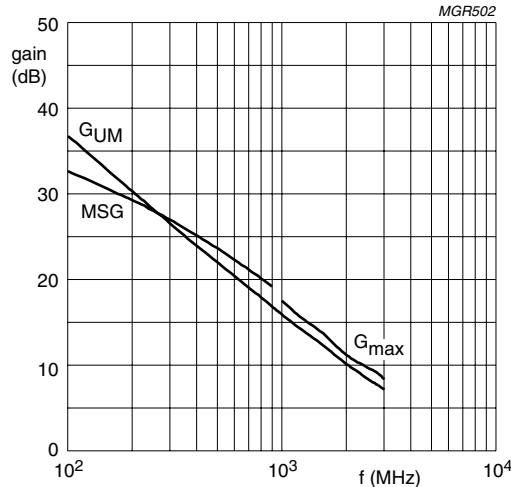
$f = 1 \text{ GHz}$; $V_{CE} = 6 \text{ V}$.
 G_{UM} = maximum unilateral power gain.
 MSG = maximum stable gain.
 G_{max} = maximum available gain.

Fig.6 Gain as a function of collector current; typical values.



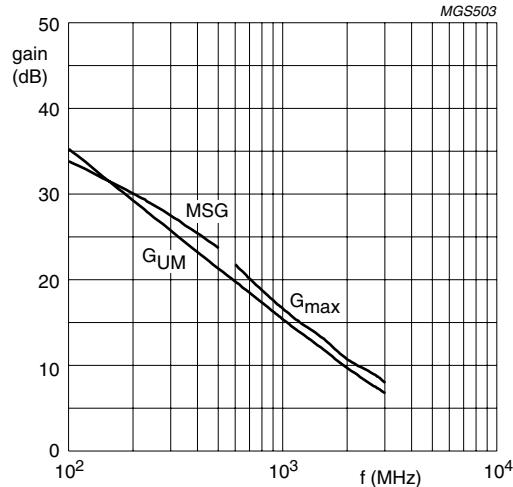
$I_C = 5 \text{ mA}$; $V_{CE} = 6 \text{ V}$.
 G_{UM} = maximum unilateral power gain.
 MSG = maximum stable gain.
 G_{max} = maximum available gain.

Fig.7 Gain as a function of frequency; typical values.



$I_C = 15 \text{ mA}$; $V_{CE} = 6 \text{ V}$.
 G_{UM} = maximum unilateral power gain.
 MSG = maximum stable gain.
 G_{max} = maximum available gain.

Fig.8 Gain as a function of frequency; typical values.

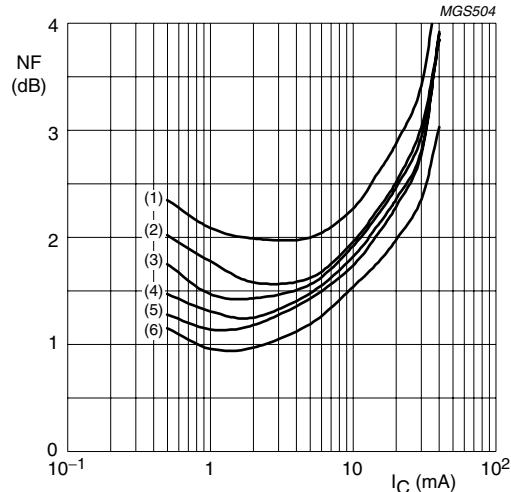


$I_C = 30 \text{ mA}$; $V_{CE} = 6 \text{ V}$.
 G_{UM} = maximum unilateral power gain.
 MSG = maximum stable gain.
 G_{max} = maximum available gain.

Fig.9 Gain as a function of frequency; typical values.

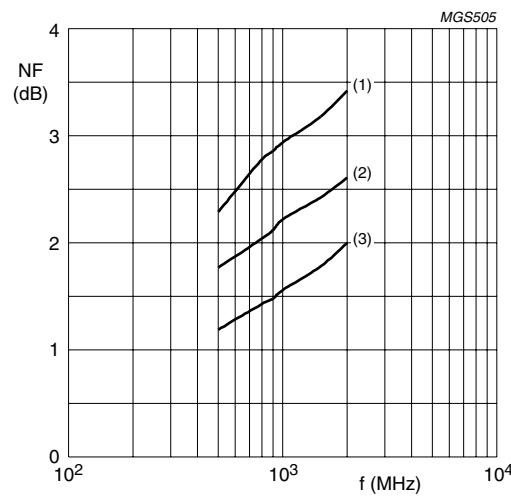
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 $V_{CE} = 6$ V.

- (1) $f = 2$ GHz. (4) $f = 900$ MHz.
- (2) $f = 1.5$ GHz. (5) $f = 800$ MHz.
- (3) $f = 1$ GHz. (6) $f = 500$ MHz.

Fig.10 Minimum noise figure as a function of collector current; typical values.

 $V_{CE} = 6$ V.

- (1) $I_C = 30$ mA.
- (2) $I_C = 15$ mA.
- (3) $I_C = 5$ mA.

Fig.11 Minimum noise figure as a function of frequency; typical values.

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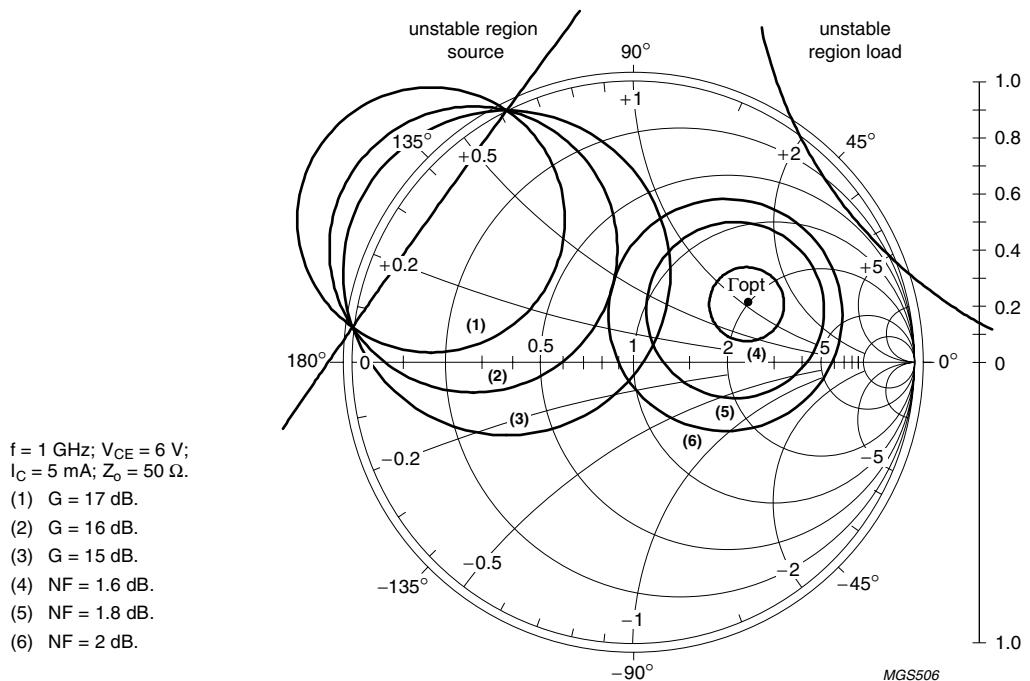


Fig.12 Common emitter available gain, noise and stability circles; typical values.

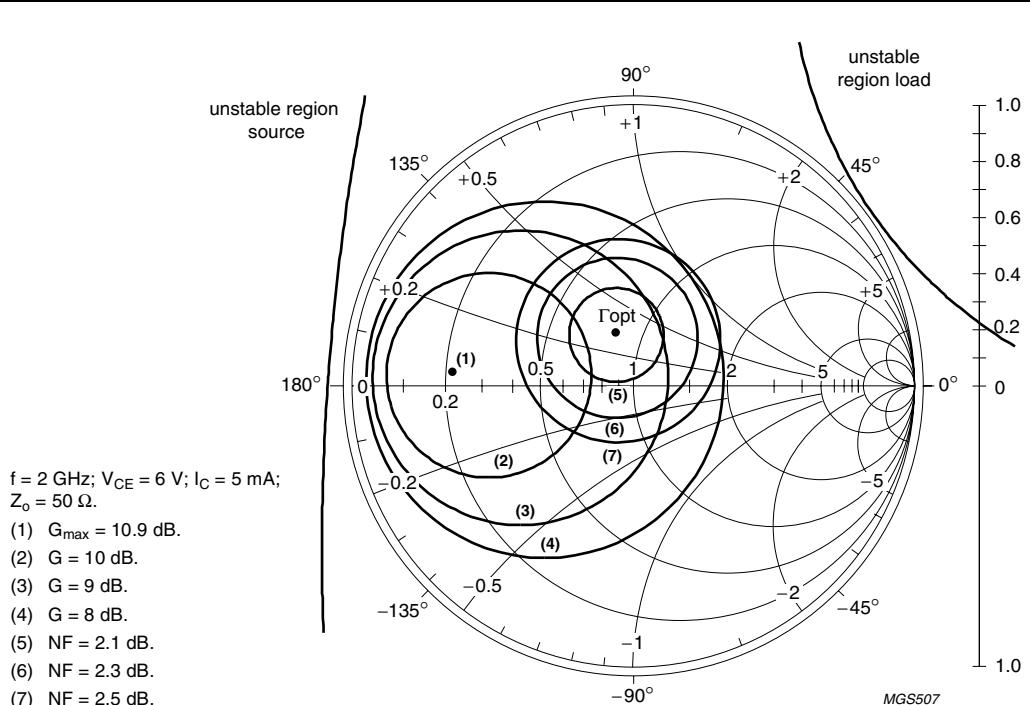
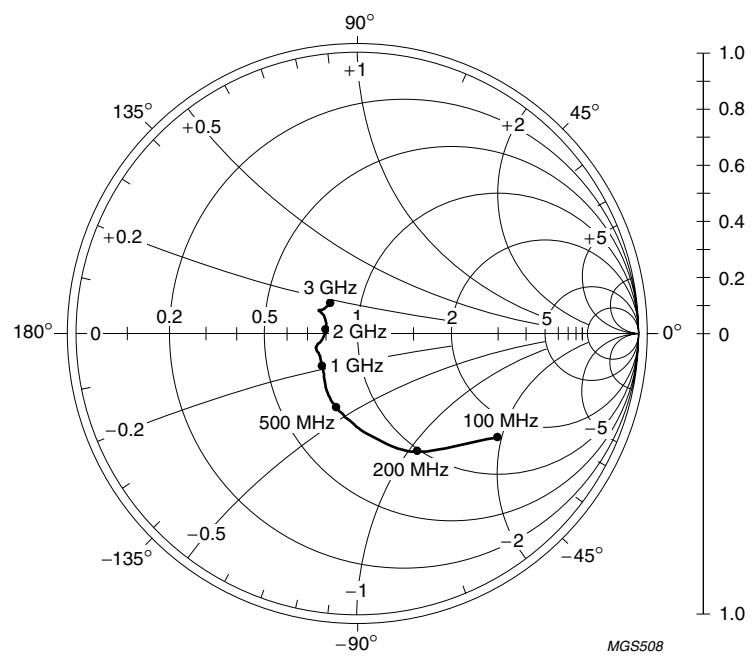
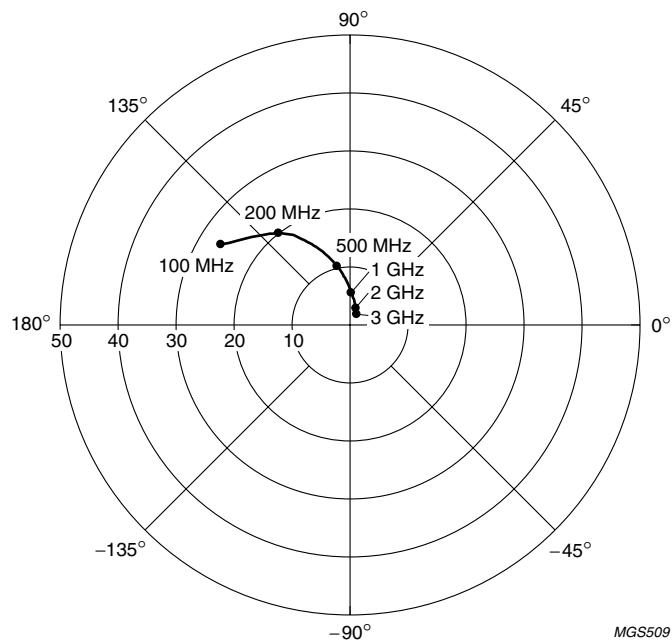


Fig.13 Common emitter available gain, noise and stability circles; typical values.

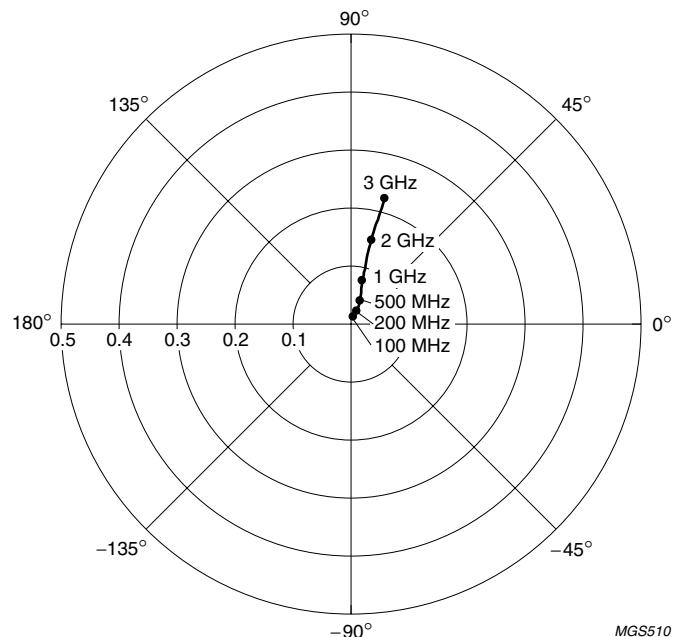
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 $V_{CE} = 6 \text{ V}; I_C = 15 \text{ mA}; Z_0 = 50 \Omega$.Fig.14 Common emitter input reflection coefficient (s_{11}); typical values. $V_{CE} = 6 \text{ V}; I_C = 15 \text{ mA}$.Fig.15 Common emitter forward transmission coefficient (s_{21}); typical values.

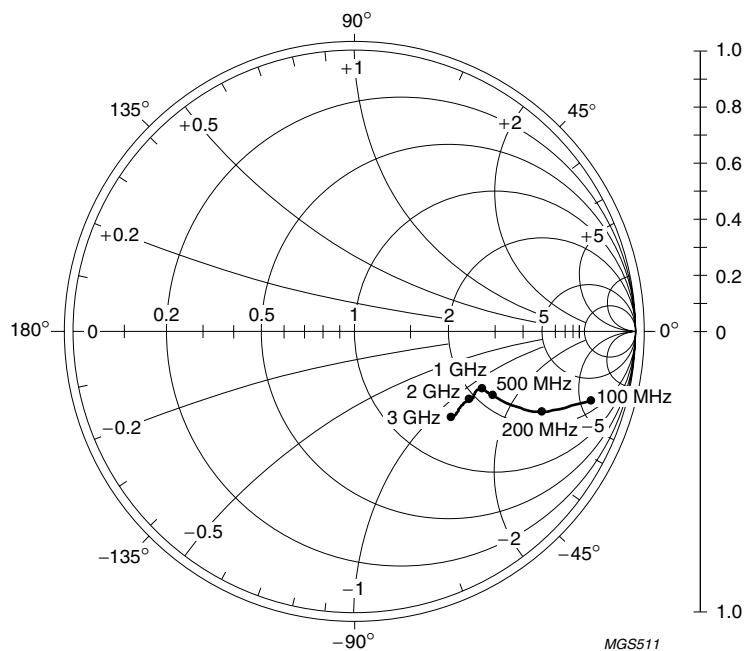
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$V_{CE} = 6 \text{ V}$; $I_C = 15 \text{ mA}$.

Fig.16 Common emitter reverse transmission coefficient (s_{12}); typical values.



$V_{CE} = 6 \text{ V}$; $I_C = 15 \text{ mA}$; $Z_0 = 50 \Omega$.

Fig.17 Common emitter output reflection coefficient (s_{22}); typical values.

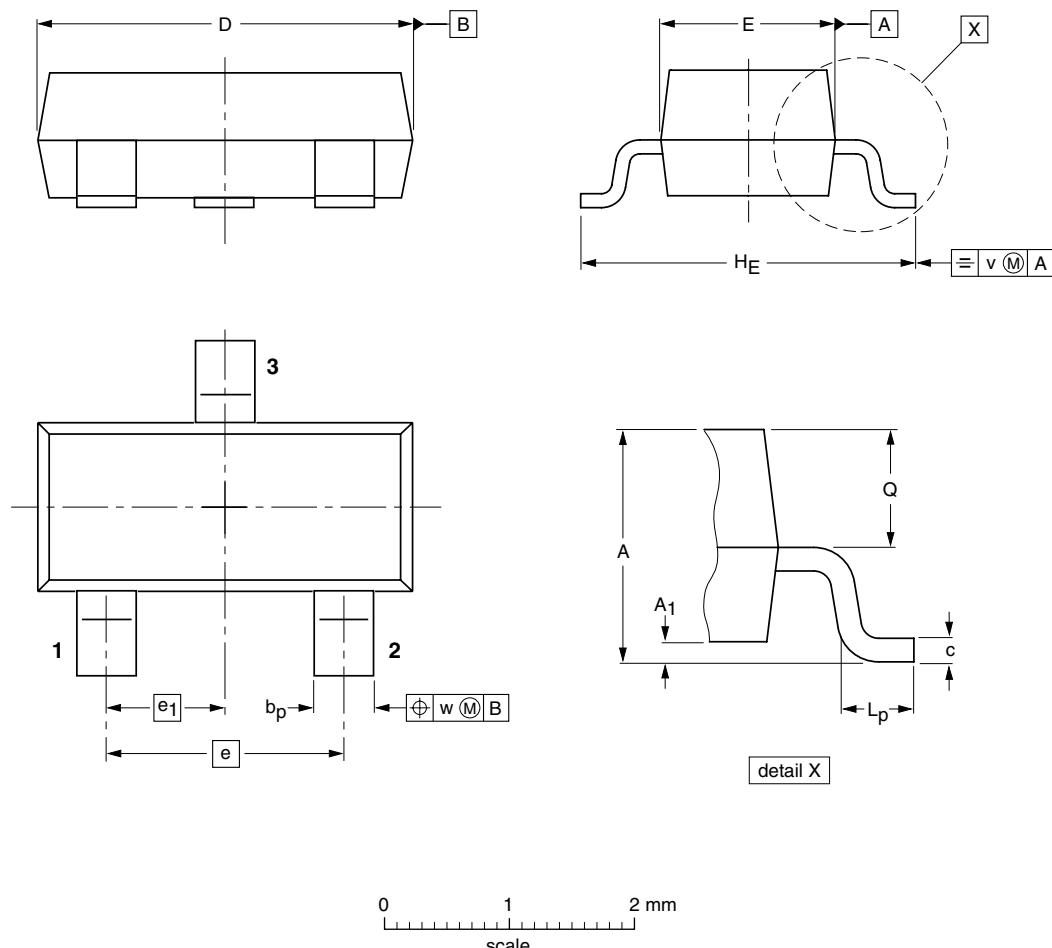
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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max.	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23		TO-236AB				-97-02-28 99-09-13

UHF wideband transistor

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DATA SHEET STATUS

DATA SHEET STATUS	PRODUCT STATUS	DEFINITIONS (1)
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Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
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Printed in The Netherlands

603508/03/pp13

Date of release: 2001 Jan 18

Document order number: 9397 750 07945



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