## High-Speed, Flexible Control With A Wide Array of Features

- Equipped with 2 or 4 Counters and counts over a maximum binary range of 32-bits.
- Accepting input pulse frequencies of up to 500 kHz allows precise control of fast motions.
- The Unit is equipped with 4 Digital Inputs, 4 Digital Outputs and 28 Soft Outputs.
- A maximum response time of 0.5 ms guarantees high-speed closed loop control of applications.



## Features

- Input frequencies to 500 kHz (with line driver input).
- 32-bit counting range.
- 2 or 4 axes operation available.
- $5,12,24 \mathrm{~V}$ line driver inputs available. ( 5 and 12 V line driver input is only available for 1 axis with the CS1W-CT021 and 2 axes with the CS1WCT041).
- Supports simple, ring, and linear counting modes.
- Supports offset phase input, up and down pulse input, and pulse + direction input.
- Supports 4 external control outputs and a total of 16 functions can be set including open gate, close gate, preset, reset, capture, stop/capture/ reset combinations, and reset enable.
- One Module supports 4 external outputs and 28 internal outputs with counter value zone comparisons, target comparisons, delays, holds, programmable outputs, and hysteresis settings.
- Pulse rate measurement function and data logging.
- Counter outputs and external control inputs can be used to trigger interrupt tasks in the CPU.
- Settings can be changed during Module operation.


## System Configuration



## Ordering Information

## International Standards

- The standards are abbreviated as follows: U: UL, U1: UL (Class I Division 2 Products for Hazardous Locations), C: CSA, UC: cULus, UC1: cULus (Class I Division 2 Products for Hazardous Locations), CU: cUL, N: NK, L: Lloyd, and CE: EC Directives.
- Contact your OMRON representative for further details and applicable conditions for these standards.

| Unit type | Product name | Specifications |  |  | No. of unitnumbers allocated | Current consumption (A) |  | Model | Standards |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Countable channels | Encoder A and B inputs, pulse input $Z$ signals | Max. counting rate |  | 5 V | 26 V |  |  |
| CS1 <br> Special I/O Unit | Highspeed Counter Units | 2 | Open collector Input voltage: 5 V DC, 12 V DC, or 24 V DC (only 1 axis for 5 V or 12 V input) | 50 kHz | 4 | 0.36 | - | CS1W-CT021 | $\begin{aligned} & \text { UC, N, L, } \\ & \text { CE } \end{aligned}$ |
|  |  |  | RS-422 line driver | 500 kHz |  |  |  |  |  |
|  |  | 4 | Open collector Input voltage: 5 V DC, 12 V DC, or 24 V DC (up to 2 axes for 5 V or 12 V input) | 50 kHz |  | 0.45 | - | CS1W-CT041 |  |
|  |  |  | RS-422 line driver | 500 kHz |  |  |  |  |  |

## Accessories

The High-Speed Counter Unit includes the 40-pin solder-type connectors C500-CE404 (socket: Fujitsu FCN-361J040-AU, cover: Fujitsu FCN-360C040-J2).

## Applicable Connectors

| Name | Connection | Specifications | Model |
| :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { 40-pin } \\ & \text { Connectors } \end{aligned}$ | Soldered | FCN-361J040-AU Connector FCN-360C040-J2 Connector Cover Right angle type, included with Unit | C500-CE404 |
|  | Crimped | FCN-363J040 Housing FCN-363J-AU Contactor FCN-360C040-J2 Connector Cover Right angle type | C500-CE405 |
|  | Pressure welded | FCN-367J040-AU/F, w/o connector cover | C500-CE403 |
|  | Soldered | 40-pin, soldered, w/connector cover | C500-CE401 |
|  | Crimped | 40-pin, crimped w/connector cover | C500-CE402 |

Connector-Terminal Conversion Unit and Connection Cables

| Product Name |  |  | Specifications |  | Model |
| :---: | :---: | :---: | :---: | :---: | :---: |
| For Connecting with Terminal Block | Connecting Cables |  | Normal wiring | Cable length: 0.5 m | XW2Z-050B |
|  |  |  | Cable length: 1.0 m | XW2Z-100B |
|  |  |  | Cable length: 1.5 m | XW2Z-150B |
|  |  |  | Cable length: 2.0 m | XW2Z-200B |
|  |  |  | Cable length: 3.0 m | XW2Z-300B |
|  |  |  | Cable length: 5.0 m | XW2Z-500B |
|  | Connector-Terminal Conversion Unit | Through |  | 40-pin M2.4 screw terminals |  | XW2B-40G4 |
|  |  |  |  | 40-pin M3.5 screw terminals |  | XW2B-40G5 |
|  |  | Slim |  | 40-pin M3 screw terminals |  | XW2D-40G6 |

Mountable Racks

| Model | CS1 System |  |  | CS1D System |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | CPU Rack | Expansion <br> Backplane | Long-distance <br> Expansion Racks | CPU Rack | Expansion <br> Backplane |
| CS1W-CT021 <br> CS1W-CT041 | Yes | Yes | Yes | Yes | Yes |

## Specifications

## General Specifications

| Item | CS1W-CT021/CT041 |
| :--- | :--- |
| Unit type | CS1 Special I/O Unit |
| General Specifications | Conform to general specifications for SYSMAC CS1-series |
| Ambient Operating Temperature | 0 to $55^{\circ} \mathrm{C}$ |
| Ambient Storage Temperature | -20 to $75^{\circ} \mathrm{C}$ |
| Ambient Operating Humidity | 10 to $90 \%$ without condensation |
| Internal Current Consumption | $450 \mathrm{~mA}(\mathrm{CS1W}$-CT041), 360 mA (CS1W-CT021) (at 5V via backplane) |
| Dimensions | $35 \times 130 \times 100 \mathrm{~mm}(\mathrm{~W} \times \mathrm{H} \times \mathrm{D})$ |
| Weight | 245 g |
| Mounting Position | CS1 CPU Rack or CS1 Expansion Rack <br> (Cannot be mounted to a C200H Expansion I/O Rack or a SYSMAC BUS Slave Rack). |
| Maximum Number of CT021/CT041 Units <br> per Rack | Equal to the number of slots of the Rack (see Note 1) |
| Maximum Number of CT021/CT041 Units <br> per basic CS1-system | 24 |
| Data Exchange with CPU Unit | •I/O Refresh Data Area (CIO-bits 200000 to 295915, CIO-words 2000 to 2959): Note 2) <br> $\bullet$ <br> Special I/O Unit DM-Area (D-words 20000 to 29599): 400 DM-words per Unit are transmitted form the CPU to the <br> Unit at Power Up or when the Unit is restarted (see Note 3) |

Note: 1. The maximum number of Units per Rack is also depending on the maximum supply current of the Power Supply Unit and the current consumption of other Units on the Rack.
2. Both the CS1W-CT021/CT041 Special I/O Units are allocated words for 4 Units in the Special I/O Unit (CIO) Area.
3. Both the CS1W-CT021/CT041 Special I/O Units are allocated words for 4 Units in the Special I/O Unit DM-Area. From the 400 DM-words that are allocated to the CT041 only the first 203 words are used to make the DM-settings. The remaining 197 DM-words can be used as work-words in the PLC Ladder Program. For the CT021 the first 113 words are used to make the DM-settings and the remaining 287 words can be used as work-words.

## Functional Specifications

| Item | CS1W-CT021/CT041 |
| :---: | :---: |
| Number of Counters | - 2 run-time configurable Counters for the CS1W-CT021 <br> - 4 run-time configurable Counters for the CS1W-CT041 |
| Counter Type | - Simple Counter <br> - Circular Counter <br> - Linear Counter <br> The Counter Type can be chosen by DIP switch at the back of the Unit. By default the Counters are set to Simple Counter. |
| Maximum Input Frequency | 500 kHz |
| Maximum Response Time | 0.5 ms (refer to High-speed Counter Units Operation Manual (Cat. No. W902)) |
| Signals per Counter | Phase A, B and Z |
| Digital I/O | - 4 Digital Inputs ( $10, \mathrm{I}$, I2 and I3): <br> Every Digital Input can be assigned to a Counter. In this way one Counter can be controlled by a maximum of 4 Digital Inputs. <br> - 4 Digital Outputs (O0, O1, O2 and O3): <br> The Unit Output Pattern represents the 4 Digital Outputs and 28 Soft Outputs. |
| Input Signal Types | - Phase Differential (multiplication $\times 1$ ), (multiplication $\times 2$ ) * and (multiplication $\times 4$ ) * <br> - Up/Down * <br> - Pulse \& Direction * |
| Counter Control using CIOsoftware bits | - Open Gate / Start Counter: Counter is enabled to count pulses <br> - Close Gate / Stop Counter: Counter is disabled to count pulses <br> - Preset Counter: Preset Value can be set in CIO <br> - Reset Counter to zero <br> - Capture Counter Value: Captured Counter Value can be read using IORD-instruction |
| Digital Input Functionality | - Gate * <br> - Reset * <br> - Preset * <br> - Capture * <br> - Stop/Capture-Continue * <br> - Stop/Capture-Reset/Continue * <br> - Capture/Reset * <br> - Enable Reset * <br> - Disable Reset * <br> For every Function the corresponding action can be triggered on a rising- or on a falling edge. |
| Output Control Mode | - Automatic Output Control in: <br> - Range Mode * <br> - Comparison Mode * <br> - Rate Range <br> - Manual Output Control |
| Output State Control | On changing the Operating Mode of the PLC from RUN/MONITOR $\rightarrow$ PROGRAM, an I/O Bus Error or an Overflow/Underflow Error, the Digital Outputs can be configured to: <br> - Continue automatic updating Output States <br> - Freeze Output States * <br> - Predefine Output States * |
| Output Driver Configuration | The Output Driver of every Digital Output can be configured as: <br> - NPN <br> - PNP * |


| Item | CS1W-CT021/CT041 |
| :---: | :---: |
| Reset Signals | Every Counter can be reset to zero by (a combination of) the following sources: <br> - Software Counter Reset Bit <br> - Digital Input * <br> - Z-Input * |
| Extra Functions | - Programmable Output Pulse *: <br> To every Digital Output an ON-delay and/or a Pulse Duration [1 to 9999 ms ] can be applied. <br> - Rate Measurement * : For every Counter the Pulse Rate can be measured by defining a Time-Window [1 to 9999 ms ]. Up to a maximum of 64 Rate Values are stored in the Rate History Log File. Rate Values from the Rate History Log File can be read using the IORD-instruction. Additionally for every Counter two Rate Ranges can be defined that control the Outputs according to the measured Rate Value. <br> - Hysteresis *: To prevent Outputs from being switched On and Off by very small fluctuations in the Counter Value around Range Limits, for every Counter an Hysteresis-value [1 to 255] can be defined (the Unit must in Range Mode). |
| Noise Filtering Counter Inputs and Digital Inputs | To suppress noise on the signal lines of the Counter Inputs (A and B) and the Digital Inputs (IO, I1, I2 and I3) a Noise Filter can be configured: <br> - 10 kHz * <br> - 50 kHz (default) <br> - 500 kHz * <br> For the Digital Inputs the 500 kHz filter can not be selected. The Z-Input Signals of every Counter are filtered with a fixed Noise Filter of 1 kHz . |
| Initial Counter Value | - The Initial Counter Value * is transferred to the Unit when the Unit is Powered Up or Restarted. The Initial Counter Value is very useful to overcome problems in case of power failure. |
| IORD- and IOWR-instructions | Run-time configuration and operation of the High-speed Counter Unit is possible by using IORD- and IOWR-instructions. The following data can be read or written: <br> - DM-configuration data * <br> - Range- and Comparison Data * <br> - Captured Counter Value <br> - Rate History Log File Data * <br> - Counter Value <br> - (Re) Configure High-speed Counter Unit * <br> - Error Clear |
| Interrupts of Outputs | - The Digital Outputs and the Soft Outputs of the Unit Output Pattern can all be configured to generate interrupts to the CS1-CPU * . |
| Interrupts of Digital Inputs | - The Digital Inputs can all be configured to generate interrupts to the CS1-CPU * . |
| Error History Log Function | - Stores up to 30 error log records |

*1. This specification item is only supported for Circular and Linear Counters (not for Simple Counters). For a complete overview of the differences between Simple and Circular/Linear Counters refer to High-speed Counter Units Operation Manual (Cat. No. W902).
*2. If an IOWR- or IORD-instruction is used during operation, comparison will stop during instruction execution. Care must be taken, therefore, with the timing of executing instructions.

## Input Specifications

| Item | Counter Inputs A and B |  |  |  | Digital Inputs ( $10,11,12$ and 13 ) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage | $\begin{array}{\|l\|} \hline 24 \mathrm{VDC} \\ \text { (19.6 to } 26.4 \mathrm{~V} \text { ) } \\ \hline \end{array}$ | $\begin{array}{\|l\|} \hline 12 \mathrm{VDC} \\ \text { ( } 9.8 \text { to } 13.2 \mathrm{~V} \text { ) } \\ \hline \end{array}$ | $\begin{array}{\|l\|} \hline 5 \mathrm{VDC} \\ \text { (4.5 to } 5.5 \mathrm{~V}) \\ \hline \end{array}$ | Line Driver | $\begin{array}{\|l\|} \hline 24 \mathrm{VDC} \\ \text { (19.6 to } 26.4 \mathrm{~V} \text { ) } \end{array}$ |
| Input Current (typical) | 8 mA | 8 mA | 7 mA | 11 mA <br> Connectable to RS-422 compatible Line Drivers. | 7.6 mA |
| ON Voltage (min.) | 19.6 V | 9.8 V | 4.5 V |  | 19.6 V |
| OFF Voltage (max.) | 4 V | 2.5 V | 1.5 V |  | 4 V |


| Item | Counter Input Z |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| Input Voltage | 24 VDC <br> $(18.6 \mathrm{to} 26.4 \mathrm{~V})$ | 12 VDC <br> $(9.8$ to 13.2 V$)$ | 5 VDC <br> $(4.5 \mathrm{to} \mathrm{5.5V})$ | Line Driver |
| Input Current <br> (typical) | 7.3 mA | 6.6 mA | 6 mA | 11mA |
| ON Voltage <br> (min.) | 18.6 V | 9.8 V | 4.5 V | Connectable to RS-422 <br> compatible Line Drivers. |
| OFF Voltage <br> (max.) | 4 V | 2.5 V | 1.5 V |  |

Note: 1. The Counter Inputs ( $\mathrm{A}, \mathrm{B}, \mathrm{Z}$ ) are insulated from each other and from the Digital Inputs. The Digital Inputs are also insulated from each other. All Counter Inputs and Digital Inputs are reverse polarity protected and insulated from the I/O-bus.
2. For every pair of Digital Inputs ( $I 0 \& I 1, I 2 \& I 3$ ) a noise filter can be configured ( 10 kHz or 50 kHz (default)). Every Z-Input has a defined noise filter of 1 kHz .


| Filter Selection | Timing requirement ( $\mu \mathrm{s}$ ) |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | A | B | C | D | E | F | G | H | I | J |
| 10 kHz | <3 | $>50$ | $>100$ | >23 | $>10$ | $>100$ | >50 | >100 | >23 | $>10$ |
| 50 kHz | <3 | $>10$ | >20 | >4.5 | $>10$ | >20 | $>10$ | >20 | $>4.5$ | $>10$ |
| 500 kHz | - | - | - | - | - | >2 | $>1$ | >4 | $>1$ | $>10$ |

Note: As a general guideline it can be stated that if you want the timing requirements for the Counter Inputs to satisfy the above mentioned specifications, you must pay attention to the type of output driver of the encoder being used, the length of the encoder cable and the frequency of the count pulses generated. For example, if you use an E6B2-type Open Collector encoder (e.g. E6B2-CWZ6C) at 24 V with 10 m cable, you can typically generate count pulses up to 20 kHz . Therefore, if you want to generate count pulses with higher frequencies, you should use a different type of encoder (e.g. E6B2-CWZ1X with Line Driver output or a fast push-pull 24 V encoder, e.g. E6C3-CWZ5GH (Complementary output)) or reduce the length of the encoder cable.

## Output Specifications

| Item | Specification |
| :--- | :--- |
| Driver Type | Open Collector (NPN/PNP selectable) |
| Operating Voltage Range | 12 to $24 \mathrm{~V}(10.2$ to 26.4 V$) 30 \mathrm{~mA}$ max. at 26.4 VDC (for PNP output, without load current) |
| Maximum Switching Capacity | 46 mA at 10.2 V to 100 mA at 26.4V (400 mA maximum/common) <br> (see picture below) |
| Minimum Switching Current | 5 mA |
| Output ON-delay | $100 \mu \mathrm{~s} \mathrm{max}$. |
| Output OFF-delay | $100 \mu \mathrm{~s} \mathrm{max}$. |
| Leakage Current | 0.1 mA max. |
| Residual Voltage | 1.5 V max. |
| Short Circuit Protection | None |

Note: 1. Every Digital Output has 2 Output drivers available: NPN and PNP (available as separate pins on the front connectors). Every Output can be separately (DM-) configured for NPN or PNP. By default the Outputs are configured as NPN-output.
2. The Digital Outputs are insulated from the I/O-bus but not from each other. They are not short circuit protected.
3. The Output current must not exceed 400 mA per common (i.e. per 4 Digital Out-puts) otherwise the unit will be damaged.
4. The Outputs can be automatically or manually controlled (DM-setting) by using Manual Output Control in CIO.
5. The state control of the 32 Outputs, in case the operating mode of the PLC-CPU is changed from RUN/MONITOR $\rightarrow$ PROGRAM, an I/ O Bus error or an Overflow/Underflow error occurs, can be configured.

## Maximum Switching Capacity

The maximum switching current depends upon the power supply voltage, as shown below.


External Interface


## Indicators

| CTO21 |  |  |  | CS |
| :---: | :---: | :---: | :---: | :---: |
| RUN | ERC |  | ERH |  |
| CH1 | A | B | Z | 10 |
| CH2 | A | B | Z | I1 |
|  |  |  | O1 | O1 |
|  |  |  | I3 | O2 |
|  |  |  |  |  |



The indicators on the LED-display show the operating status of the Unit. The following table shows the meaning of the indicators.

| LED | Color | State | Description |
| :---: | :---: | :---: | :---: |
| RUN | Green | ON | Unit is in operation (i.e. Unit has initialised normally after (re-) starting the Unit). |
|  |  | OFF | Unit is not in operation <br> (i.e. Unit was not able to initialise normally after (re-) starting the Unit or the power to the Unit is switched OFF). |
| ERC | Red | ON | Unit has operational failure due to a detected error. |
|  |  | OFF | Unit has no operational failure. |
| ERH | Red | ON | CPU Unit has operational failure. |
|  |  | OFF | CPU Unit has no operational failure. |
| CH1/2/3/4 | Green | ON | Channel $1 / 2 / 3 / 4$ (i.e. Counter $1 / 2 / 3 / 4$ ) is ready to count (the corresponding counting gate is open). |
|  |  | OFF | Counter $1 / 2 / 3 / 4$ is not ready to count (the corresponding counting gate is closed). |
| A/B/Z | Yellow | ON | Physical Input $A / B / Z$ has turned $O N$. <br> (Every Counter is characterised by the signals $\mathrm{A} / \mathrm{B} / \mathrm{Z}$.) |
|  |  | OFF | Physical Input $A / B / Z$ is turned OFF. |
| 10/11/12/13 | Yellow | ON | Digital Input (10/11/12/13) is turned ON. |
|  |  | OFF | Digital Input (10/11/12//3) is turned OFF. |
| 00/01/02/03 | Yellow | ON | Digital Output (00/01/O2/O3) is turned ON. |
|  |  | OFF | Digital Output (00/O1/O2/O3) is turned OFF. |

## Using Connector-Terminal Conversion Unit

Wiring requires the dedicated connection cables.
Connector-Terminal Conversion Unit and Connection Cables are sold separately.


Note: 2 units are required for 4 axes control.

CS1W-CT021
CS1W-CT041


## Related Manuals

| Cat. No. |  | Model |  | Name |  | Contents |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| English | Japanese |  |  |  |  |  |  |
| W902 | SBCC-829 | CS1W-CT021 <br> CS1W-CT041 | High-speed Counter Units <br> Operation Manual | Provides information on operating and installing High-speed Counter Units, <br> including details. On basic settings, memory operation, direct operation from <br> CPU and other functions. |  |  |  |

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