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February 2008

# 74LVX00 Low Voltage Quad 2-Input NAND Gate

#### **Features**

#### ■ Input voltage level translation from 5V to 3V

- Ideal for low power/low noise 3.3V applications
- Guaranteed simultaneous switching noise level and dynamic threshold performance

### **General Description**

The LVX00 contains four 2-input NAND gates. The inputs tolerate voltages up to 7V allowing the interface of 5V systems to 3V systems.

# **Ordering Information**

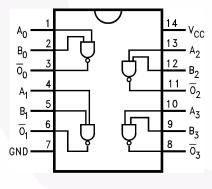
Order Number	Package Number	Package Description
74LVX00M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
74LVX00SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74LVX00MTC	MTC14	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.

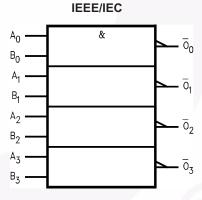


All packages are lead free per JEDEC: J-STD-020B standard.

# **Connection Diagram**



### **Logic Symbol**



### **Pin Description**

Pin Names	Description
A <sub>n</sub> , B <sub>n</sub>	Inputs
$\overline{O}_n$	Outputs

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	-0.5V to +7.0V
I <sub>IK</sub>	DC Input Diode Current, V <sub>I</sub> = -0.5V	–20mA
V <sub>I</sub>	DC Input Voltage	-0.5V to 7V
I <sub>OK</sub>	DC Output Diode Current	
	$V_{O} = -0.5V$	–20mA
	$V_{O} = V_{CC} + 0.5V$	+20mA
V <sub>O</sub>	DC Output Voltage	-0.5V to V <sub>CC</sub> + 0.5V
I <sub>O</sub>	DC Output Source or Sink Current	±25mA
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current	±50mA
T <sub>STG</sub>	Storage Temperature	−65°C to +150°C
Р	Power Dissipation	180mW

# Recommended Operating Conditions<sup>(1)</sup>

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	2.0V to 3.6V
V <sub>I</sub>	Input Voltage	0V to 5.5V
Vo	Output Voltage	0V to V <sub>CC</sub>
T <sub>A</sub>	Operating Temperature	–40°C to +85°C
Δt / ΔV	Input Rise and Fall Time	0ns/V to 100ns/V

#### Note:

1. Unused inputs must be held HIGH or LOW. They may not float.

# **DC Electrical Characteristics**

				T <sub>A</sub> = +25°C			40°C to 5°C		
Symbol	Parameter	V <sub>CC</sub>	Conditions	Min.	Тур.	Max.	Min.	Max.	Units
V <sub>IH</sub>	HIGH Level Input	2.0		1.5			1.5		V
	Voltage	3.0		2.0			2.0		
		3.6		2.4			2.4		
V <sub>IL</sub>	LOW Level Input	2.0				0.5		0.5	V
	Voltage	3.0				8.0		0.8	
		3.6				0.8		0.8	
V <sub>OH</sub>	HIGH Level Output Voltage	2.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OH} = -50 \mu A$	1.9	2.0		1.9		V
		3.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OH} = -50 \mu A$	2.9	3.0		2.9		
			$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OH} = -4\text{mA}$	2.58			2.48		
V <sub>OL</sub>	LOW Level Output Voltage	2.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OL} = 50 \mu A$		0.0	0.1		0.1	V
		3.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OL} = 50 \mu A$		0.0	0.1		0.1	
			$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OL} = 4\text{mA}$			0.36		0.44	
I <sub>IN</sub>	Input Leakage Current	3.6	V <sub>IN</sub> = 5.5V or GND			±0.1		±1.0	μA
I <sub>CC</sub>	Quiescent Supply Current	3.6	$V_{IN} = V_{CC}$ or GND			2.0		20.0	μA

# Noise Characteristics<sup>(2)</sup>

				$T_A = 25^{\circ}C$		
Symbol	Parameter	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Тур.	Limit	Units
V <sub>OLP</sub>	Quiet Output Maximum Dynamic V <sub>OL</sub>	3.3	50	0.3	0.5	V
V <sub>OLV</sub>	Quiet Output Minimum Dynamic V <sub>OL</sub>	3.3	50	-0.3	-0.5	V
V <sub>IHD</sub>	Minimum HIGH Level Dynamic Input Voltage	3.3	50		2.0	V
V <sub>ILD</sub>	Maximum LOW Level Dynamic Input Voltage	3.3	50		0.8	V

#### Note:

2. Input  $t_r = t_f = 3ns$ 

### **AC Electrical Characteristics**

				T <sub>A</sub> = +25°C		T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C		
Symbol	Parameter	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min.	Тур.	Max.	Min.	Max.	Units	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay Time	2.7	15		5.4	10.1	1.0	12.5	ns	
			50		7.9	13.6	1.0	16.0		
		3.3 ± 0.3	15		4.1	6.2	1.0	7.5		
			50		6.6	9.7	1.0	11.0		
tosch, toshc	Output to Output Skew <sup>(3)</sup>	2.7	50			1.5		1.5	ns	
		3.3				1.5		1.5		

#### Note:

3. Parameter guaranteed by design  $t_{OSLH} = |t_{PLHm} - t_{PLHn}|$ ,  $t_{OSHL} = |t_{PHLm} - t_{PHLn}|$ 

# Capacitance

		T <sub>A</sub> =		_ +25°C		T <sub>A</sub> = -40°C to +85°C	
Symbol	Parameter	Min.	Тур.	Max.	Min.	Max.	Units
C <sub>IN</sub>	Input Capacitance		4	10		10	pF
C <sub>PD</sub>	Power Dissipation Capacitance <sup>(4)</sup>		19				pF

#### Note:

4. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:  $I_{CC(opr.)} = \frac{C_{PD} \times V_{CC} \times f_{IN} \times I_{CC}}{4 \text{ (per Gate)}}$ 





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