# **Bus Switch**

The 7SB385 Bus Switch is an advanced high-speed line switch in ultra-small footprint.

# Features

- High Speed:  $t_{PD} = 0.25 \text{ ns} (Max) @ V_{CC} = 4.5 \text{ V}$
- 3  $\Omega$  Switch Connection Between 2 Ports
- Power Down Protection Provided on Inputs
- Ultra-Small Packages
- These are Pb-Free Devices

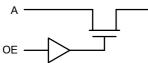
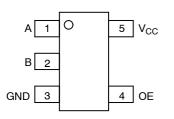


Figure 1. Logic Diagram



А		6	V <sub>CC</sub>
В	2	5	NC
GND	3	4	OE

Figure 3. ULLGA6/UDFN6

(Top View)

**-** В

Figure 2. TSOP-5/SC-88A (Top View)

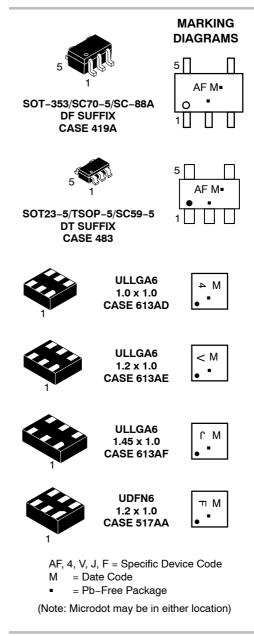
**Function Table** 

Input OE	Function
L	Disconnect
Н	B = A



# **ON Semiconductor®**

http://onsemi.com



## **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

### **Table 1. MAXIMUM RATINGS**

Symbol	Paramet	ter	Value	Unit
V <sub>CC</sub>	DC Supply Voltage		–0.5 to +7.0	V
V <sub>IN</sub>	Control Pin Input Voltage		–0.5 to +7.0	V
V <sub>I/O</sub>	Switch Input / Output Voltage		–0.5 to +7.0	V
I <sub>IK</sub>	Control Pin DC Input Diode Current	V <sub>IN</sub> < GND	-50	mA
I <sub>OK</sub>	Switch I/O Port DC Diode Current	V <sub>I/O</sub> < GND	-50	mA
Ι <sub>Ο</sub>	On-State Switch Current		±128	mA
	Continuous Current Through $V_{CC}$ or GND		±150	mA
I <sub>CC</sub>	DC Supply Current per Supply Pin		±150	mA
I <sub>GND</sub>	DC Ground Current per Ground Pin		±150	mA
T <sub>STG</sub>	Storage Temperature Range		–65 to +150	°C
ΤL	Lead Temperature, 1 mm from Case for 10	Seconds	260	°C
TJ	Junction Temperature Under Bias		150	°C
$\theta_{JA}$	Thermal Resistance	SC70-5/SC-88A (Note 1)	350	°C/W
		TSOP-5	230	
		ULLGA6/UDFN6	496	
PD	Power Dissipation in Still Air at 85°C	SC70-5/SC-88A (Note 1)	150	mW
		TSOP-5	200	
		ULLGA6/UDFN6	252	
MSL	Moisture Sensitivity		Level 1	
F <sub>R</sub>	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
V <sub>ESD</sub>	ESD Withstand Voltage	Human Body Mode (Note 2)	>2000	V
		Machine Mode (Note 3)	>200	
		Charged Device Mode (Note 4)	N/A	
I <sub>LATCHUP</sub>	Latchup Performance Above V <sub>CC</sub> and Below	w GND at 85°C (Note 5)	±100	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.
 Tested to EIA/ JESD22-A114-A

Tested to EIA/ JESD22-A115-A
 Tested to JESD22-C101-A

5. Tested to EIA / JESD78.

#### **Table 2. RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage	4.0	5.5	V
VI	Control Pin Input Voltage	0	5.5	V
V <sub>I/O</sub>	Switch Input / Output Voltage	0	5.5	V
T <sub>A</sub>	Operating Free-Air Temperature	-55	+125	°C
$\Delta t / \Delta V$	Input Transition Rise or Fall Rate Control Input Switch I/O	0 0	5 DC	nS/V

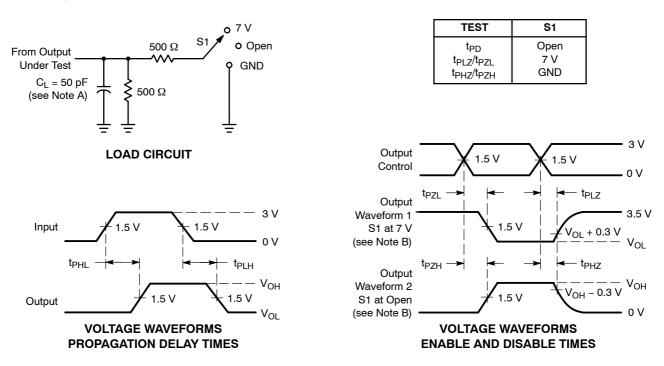
				-	Γ <sub>A</sub> = 25°(	0	T <sub>A</sub> = -55°C	to +125°C	
Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit
V <sub>IK</sub>	Clamp Diode Voltage	I <sub>IN</sub> = -18 mA	4.5			-1.2		-1.2	V
V <sub>IH</sub>	High-Level Input Voltage (Control)		4.0 to 5.5	2.0			2.0		V
V <sub>IL</sub>	Low-Level Input Voltage (Control)		4.0 to 5.5			0.8		0.8	V
l <sub>IN</sub>	Input Leakage Current	$0 \le V_{IN} \le 5.5 \text{ V}$	5.5			±0.1		±1.0	μΑ
I <sub>OFF</sub>	Power Off Leakage Current	$V_{I/O} = 0$ to 5.5 V	0			±0.1		±1.0	μA
Icc	Quiescent Supply Current	I <sub>O</sub> = 0, V <sub>IN</sub> = V <sub>CC</sub> or 0 V	5.5			±0.1		±1.0	μΑ
$\Delta I_{CC}$	Increase in Supply Current (Control Pin)	One input at 3.4 V; Other inputs at V <sub>CC</sub> or GND	5.5					2.5	mA
R <sub>ON</sub>	Switch ON Resistance	V <sub>I/O</sub> = 0, I <sub>I/O</sub> = 64 mA I <sub>I/O</sub> = 30 mA	4.5		3 3	7 7		7 7	Ω
		V <sub>I/O</sub> = 2.4, I <sub>I/O</sub> = 15 mA	4.5		6	15		15	
		V <sub>I/O</sub> = 2.4, I <sub>I/O</sub> = 15 mA	4.0		10	20		20	

## Table 3. DC ELECTRICAL CHARACTERISTICS

## Table 4. AC ELECTRICAL CHARACTERISTICS

				L L	Γ <sub>A</sub> = 25°	c		-55°C 25°C	
Symbol	Parameter	V <sub>CC</sub> (V)	Test Condition	Min	Тур	Max	Min	Max	Unit
t <sub>PD</sub>	Propagation Delay,	4.0 to 5.5	See Figure 3			0.25		0.25	ns
	A to B or B to A					0.25		0.25	
t <sub>EN</sub>	Output Enable Time	4.5 to 5.5		0.8	2.5	4.2	0.8	4.2	ns
		4.0		0.8	3.0	4.6	0.8	4.6	
t <sub>DIS</sub>	Output Disable Time	4.5 to 5.5		0.8	3.1	4.8	0.8	4.8	ns
		4.0		0.8	2.9	4.4	0.8	4.4	
C <sub>IN</sub>	Control Input Capacitance	5.0	V <sub>IN</sub> = 3 V or 0		2.0				pF
C <sub>IO(ON)</sub>	Switch On Capacitance	5.0	Switch ON		10				pF
C <sub>IO(OFF)</sub>	Switch Off Capacitance	5.0	Switch OFF		3.5				pF

### AC Loading and Waveforms



A. CL includes probe and jig capacitance.

B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.

C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz, Z<sub>O</sub> = 50  $\Omega$ , t<sub>f</sub>  $\leq$  2.5 ns. t<sub>f</sub>  $\leq$  2.5 ns.

D. The output is measured with one input transition per measurement.

E.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .

F.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .

G.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

#### Figure 4. Load Circuit and Voltage Waveforms

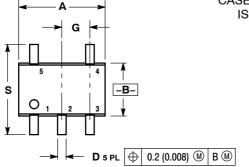
#### **DEVICE ORDERING INFORMATION**

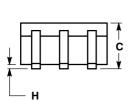
Device	Package	Shipping <sup>†</sup>
7SB385DTT1G	TSOP-5 (Pb-Free)	3000 / Tape & Reel
7SB385DFT2G	SC-88A (Pb-Free)	3000 / Tape & Reel
7SB385AMX1TCG	ULLGA6 – 0.5 mm Pitch (Pb–Free)	3000 / Tape & Reel
7SB385BMX1TCG	ULLGA6 – 0.4 mm Pitch (Pb–Free)	3000 / Tape & Reel
7SB385CMX1TCG	ULLGA6 – 0.35 mm Pitch (Pb–Free)	3000 / Tape & Reel
7SB385MUTCG	UDFN6 – 0.4 mm Pitch (Pb–Free)	3000 / Tape & Reel

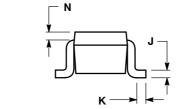
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# **PACKAGE DIMENSIONS**

SC-88A, SOT-353, SC-70 CASE 419A-02 ISSUE J





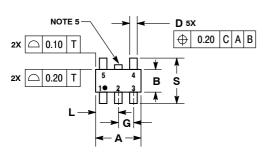


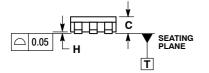
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. 419A-01 OBSOLETE. NEW STANDARD 419A-02. 4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	INC	HES	MILLIN	ETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
C	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026	BSC	0.65 BSC		
н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
K	0.004	0.012	0.10	0.30	
N	0.008 REF		0.20	REF	
S	0.079	0.087	2.00	2.20	

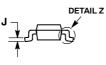
## PACKAGE DIMENSIONS

#### TSOP-5 CASE 483-02 **ISSUE H**







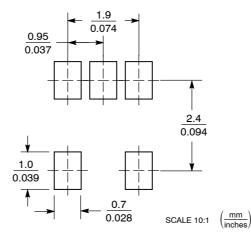


NOTES:

- NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF DAGE MATERIA
- OF BASE MATERIAL.
  DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE
- MOLD FLASH, FIGHTER, FIGHTER,

	MILLIN	IETERS			
DIM	MIN	MAX			
Α	3.00	BSC			
в	1.50 BSC				
С	0.90	1.10			
D	0.25	0.50			
G	0.95	BSC			
н	0.01	0.10			
J	0.10	0.26			
к	0.20	0.60			
L	1.25 1.55				
м	0° 10°				
S	2.50	3.00			

#### **SOLDERING FOOTPRINT\***

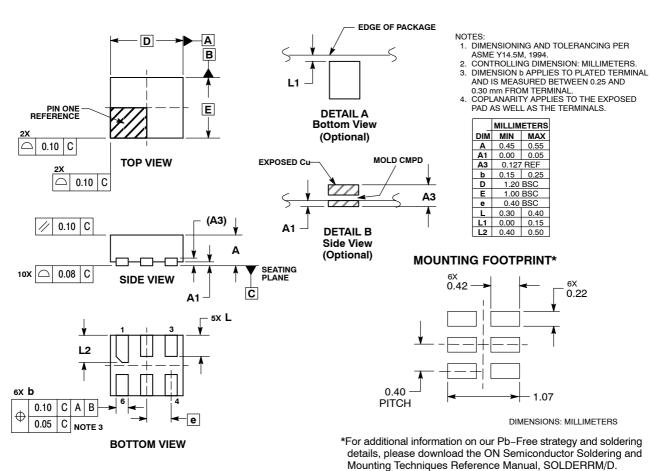


\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

### PACKAGE DIMENSIONS

## UDFN6 1.2x1.0, 0.4P CASE 517AA-01

ISSUE C



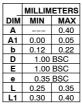
## PACKAGE DIMENSIONS

ULLGA6 1.0x1.0, 0.35P

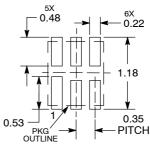
CASE 613AD-01 **ISSUE A** 

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP. 4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

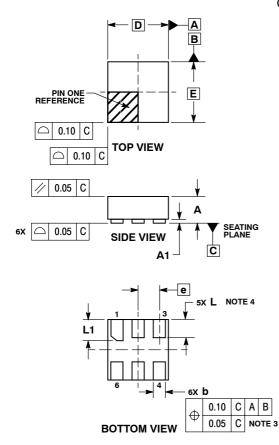


MOUNTING FOOTPRINT SOLDERMASK DEFINED\*



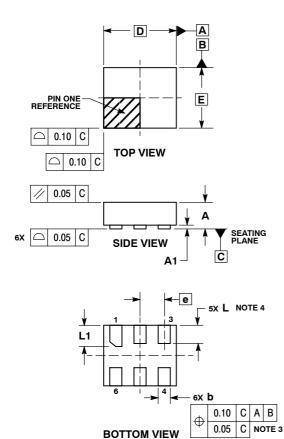
DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



## PACKAGE DIMENSIONS

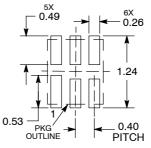
ULLGA6 1.2x1.0, 0.4P CASE 613AE-01 ISSUE A



- NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND AND IS MEASURED BETWEEN 0.15 AND
- AND IS MEASON THE TERMINAL TIP. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED. 4.

	MILLIM	MILLIMETERS				
DIM	MIN	MAX				
Α		0.40				
A1	0.00	0.05				
b	0.15	0.25				
D	1.20	BSC				
Е	1.00	BSC				
е	0.40	BSC				
L	0.25	0.35				
L1	0.35	0.45				

#### MOUNTING FOOTPRINT SOLDERMASK DEFINED\*

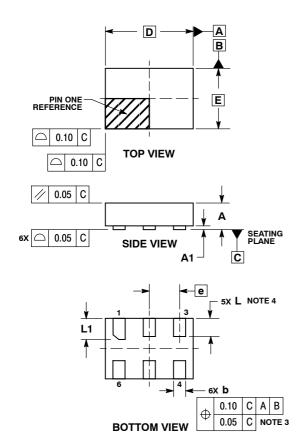


DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### PACKAGE DIMENSIONS

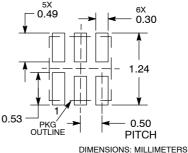
ULLGA6 1.45x1.0, 0.5P CASE 613AF-01 **ISSUE A** 



- NOTES: 1. DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. 2 DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP. 3
- A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED. 4

	MILLIM	ETERS		
DIM	MIN MAX			
Α		0.40		
A1	0.00	0.05		
b	0.15	0.25		
D	1.45	BSC		
E	1.00	BSC		
е	0.50 BSC			
L	0.25	0.35		
L1	0.30	0.40		

**MOUNTING FOOTPRINT** SOLDERMASK DEFINED\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All or operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Ce Phone: 81-3-5773-3850

#### ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Bus Transmitters category:

Click to view products by ON Semiconductor manufacturer:

Other Similar products are found below :

NLV7SB3257DTT1G 7SB384MUTCG 7SB384DTT1G 74LVCH162244ADL.112 74LVC541APW.118 74LVC240APW.112 74VHCT245AFT(BJ) 7SB3126DFT2G 7SB3257DFT2G 7SB384DFT2G 7SB385DFT2G MC1488D1 SN65LVDS84AQDGGR SN75LVDS83BDGGR SN75LVDS84ADGGR 7SB3257MUTCG SN65LVDS84AQDGG SN75LVDS83ADGG SN75LVDS83ADGGR SN75LVDS83BZQLR SN75LVDS83DGG SN75LVDS83DGGR SN75LVDS84ADGG SN75LVDS84DGG