## BAW56L, SBAW56L

# **Dual Switching Diode Common Anode**

#### **Features**

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

### **MAXIMUM RATINGS** (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	70	V
Forward Current	IF	200	mA
Forward Surge Current (60 Hz @ 1 cycle)	I <sub>FSM</sub>	2.0	А
Non-Repetitive Peak Forward Current t = 1 μs (Note 3)	I <sub>FSM</sub>	4.0	А
Repetitive Peak Forward Current Pulse Wave = 1 sec, Duty Cycle = 66%	I <sub>FRM</sub>	500	mA

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1.  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in. 99.5% alumina.
- 3. Square Wave;  $T_i = 25^{\circ}C$ .

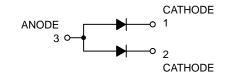


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SOT-23 (TO-236) CASE 318 STYLE 12



#### **MARKING DIAGRAM**



A1 = Device Code M = Date Code\* ■ Pb–Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BAW56LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SBAW56LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BAW56LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel
SBAW56LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

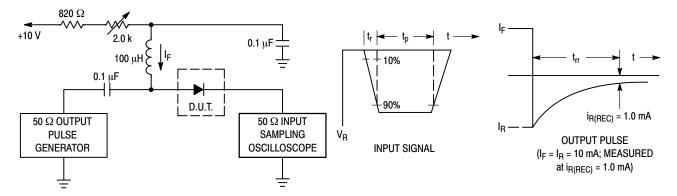
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## BAW56L, SBAW56L

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted) (Each Diode)

Characteristic	Symbol	Min	Max	Unit
Reverse Breakdown Voltage (I <sub>(BR)</sub> = 100 μA)	V <sub>(BR)</sub>	70	_	V
Reverse Voltage Leakage Current $(V_R = 25 \text{ V}, T_J = 150^{\circ}\text{C})$ $(V_R = 70 \text{ V})$ $(V_R = 70 \text{ V}, T_J = 150^{\circ}\text{C})$	I <sub>R</sub>	- - -	30 2.5 50	μΑ
Diode Capacitance $(V_R = 0 \text{ V}, f = 1.0 \text{ MHz})$	C <sub>D</sub>	_	2.0	pF
Forward Voltage (I <sub>F</sub> = 1.0 mA) (I <sub>F</sub> = 10 mA) (I <sub>F</sub> = 50 mA) (I <sub>F</sub> = 150 mA)	V <sub>F</sub>	- - - -	715 855 1000 1250	mV
Reverse Recovery Time (I <sub>F</sub> = I <sub>R</sub> = 10 mA, I <sub>R(REC)</sub> = 1.0 mA) (Figure 1) R <sub>L</sub> = 100 $\Omega$	t <sub>rr</sub>	_	6.0	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



Notes: 1. A 2.0  $k\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA.

Figure 1. Recovery Time Equivalent Test Circuit

<sup>2.</sup> Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA.

<sup>3.</sup> t<sub>n</sub> » t<sub>ri</sub>

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## **Curves Applicable to Each Cathode**

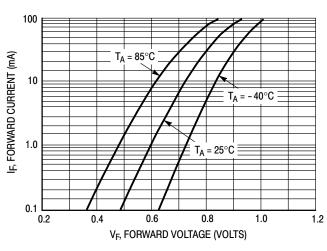
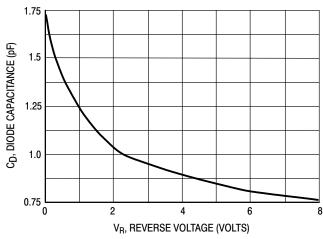
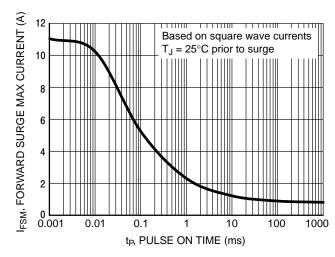


Figure 2. Forward Voltage

Figure 3. Leakage Current







**Figure 5. Forward Surge Current** 



SOT-23 (TO-236) CASE 318-08 **ISSUE AS** 

**DATE 30 JAN 2018** 

# SCALE 4:1 D - 3X b

**TOP VIEW** 







#### **RECOMMENDED SOLDERING FOOTPRINT**



DIMENSIONS: MILLIMETERS

#### NOTES:

- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
  MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,

	PROT	RUSIONS, OR GATE BURRS.	
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	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

= Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE
OT (1 F O			

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STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12:	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. SOURCE	PIN 1. CATHODE
<ol><li>ANODE</li></ol>	<ol><li>SOURCE</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>CATHODE</li></ol>	2. DRAIN	2. GATE
<ol><li>CATHODE</li></ol>	3. GATE	<ol><li>CATHODE-ANODE</li></ol>	<ol><li>ANODE</li></ol>	3. GATE	<ol><li>ANODE</li></ol>

STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	PIN 1. CATHODE	PIN 1. CATHODE
<ol><li>CATHODE</li></ol>	<ol><li>CATHODE</li></ol>	2. ANODE	<ol><li>CATHODE</li></ol>	2. ANODE	<ol><li>ANODE</li></ol>
<ol><li>ANODE</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>ANODE</li></ol>	<ol><li>CATHODE-ANOD</li></ol>	E 3. GATE

STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
<ol><li>SOURCE</li></ol>	<ol><li>OUTPUT</li></ol>	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3 DRAIN	3 INPLIT	3 CATHODE	3. SOURCE	3. GATE	<ol><li>NO CONNECTION</li></ol>

STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE	
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