BAW56M3T5G

Dual Switching Diode Common Anode

The BAW56M3T5G device is a spin-off of our popular SOT-23 three-leaded device. It is designed for switching applications and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

Features

- Reduces Board Space
- This is a Halide-Free Device
- This is a Pb-Free Device

MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	75	Vdc
Forward Current	ΙF	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C Derate above 25°C	P _D	265 2.1	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	470	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	640 5.1	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	195	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

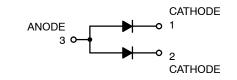
- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = 0.4 \times 0.3 \times 0.024 in. 99.5% alumina.



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70 V DUAL COMMON ANODE SWITCHING DIODE



MARKING DIAGRAM



AN = Specific Device Code M = Date Code

ORDERING INFORMATION

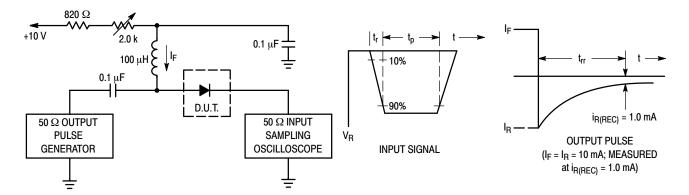
Device	Package	Shipping [†]
BAW56M3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BAW56M3T5G

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Each Diode)

Characteristic		Symbol	Min	Max	Unit
Reverse Breakdown Voltage	(I _(BR) = 100 μA)	V _(BR)	70	-	V
Reverse Voltage Leakage Current	(V _R = 25 V, T _J = 150°C) (V _R = 70 V) (V _R = 70 V, T _J = 150°C)	I _R	- - -	30 2.5 50	μΑ
Diode Capacitance	(V _R = 0 V, f = 1.0 MHz)	C _D	-	2.0	pF
Forward Voltage	$(I_F = 1.0 \text{ mA})$ $(I_F = 10 \text{ mA})$ $(I_F = 50 \text{ mA})$ $(I_F = 150 \text{ mA})$	V _F	- - - -	715 855 1000 1250	mV
Reverse Recovery Time $(I_F = I_R = 10 \text{ mA}, I_{R(REC)} = 1.0 \text{ mA}) \text{ (Figure 1)}$	R _L = 100 Ω	t _{rr}	-	6.0	ns



Notes: 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (I $_{F})$ of 10 mA.

Figure 1. Recovery Time Equivalent Test Circuit

^{2.} Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.

^{3.} t_p » t_{rr}

BAW56M3T5G

Curves Applicable to Each Cathode

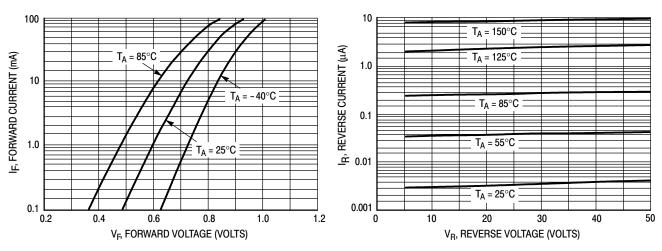


Figure 2. Forward Voltage

Figure 3. Leakage Current

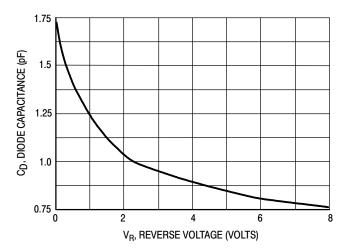


Figure 4. Capacitance



SOT-723 CASE 631AA-01 ISSUE D

DATE 10 AUG 2009

NOTES:

- NOTES.

 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

 2. CONTROLLING DIMENSION: MILLIMETERS.

 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD
- FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.45	0.50	0.55
b	0.15	0.21	0.27
b1	0.25	0.31	0.37
С	0.07	0.12	0.17
D	1.15	1.20	1.25
E	0.75	0.80	0.85
е	0.40 BSC		
ΗE	1.15	1.20	1.25
L	0.29 REF		
12	0.15	0.20	0.25

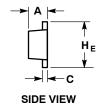
L2 0.15 0.20 0.25 **GENERIC** MARKING DIAGRAM*

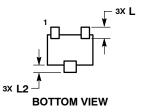


= Specific Device Code XX Μ = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

-X-2X b ⊕ 0.08 X Y **TOP VIEW**

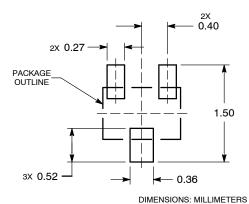




STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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