Preferred Device

General Purpose Transistor

NPN Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-723 package which is designed for low power surface mount applications.

• This is a Pb–Free Device

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Max	Unit
Collector–Emitter Voltage	V _{CEO}	45	V
Collector-Base Voltage	V _{CBO}	50	V
Emitter-Base Voltage	V _{EBO}	6.0	V
Collector Current – Continuous	Ι _C	100	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^{\circ}C$ Derated above 25°C	P _D	260 2.0	mW mW/°C	
Thermal Resistance, Junction–to–Ambient (Note 1)	$R_{ hetaJA}$	480	°C/W	
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^{\circ}C$ Derated above 25°C	P _D	600 4.8	mW mW/°C	
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	205	°C/W	
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C	

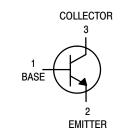
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected. 1. FR-4 @ Minimum Pad

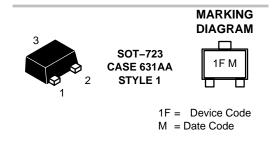
2. FR-4 @ 1.0 × 1.0 Inch Pad



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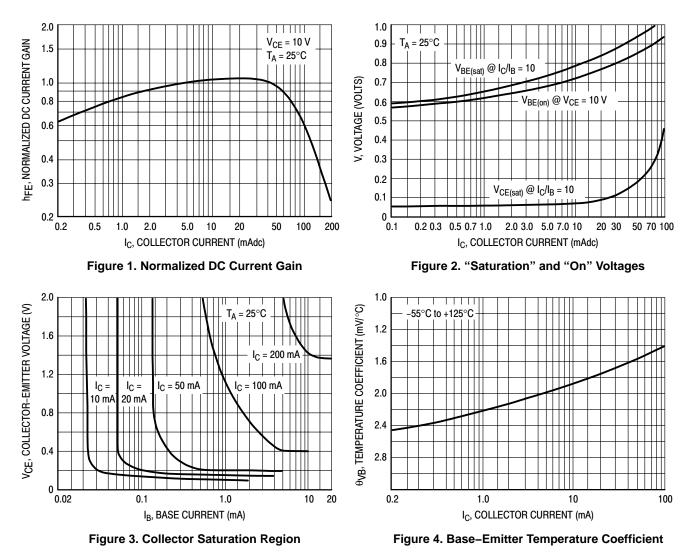
ORDERING INFORMATION

Device	Package	Shipping [†]	
BC847BM3T5G	SOT-723	8000/Tape & Reel	

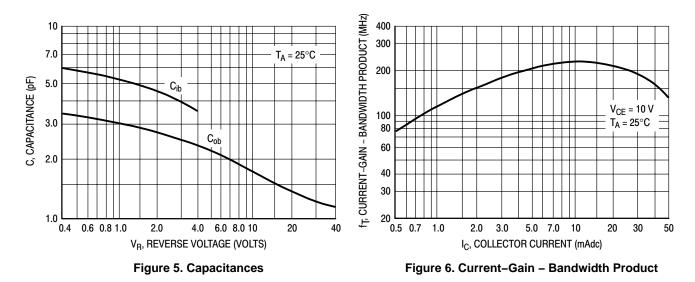
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

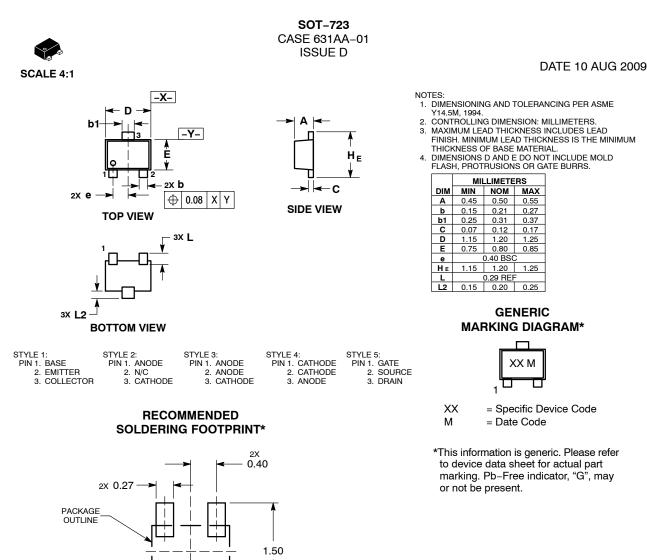
Symbol	Min	Тур	Max	Unit
V _{(BR)CEO}	45	-	-	V
V _{(BR)CES}	50	-	-	V
V _{(BR)CBO}	50	-	_	V
V _{(BR)EBO}	6.0	-	_	V
I _{CBO}	-		15 5.0	nA μA
•		•	•	•
h _{FE}	_ 200	150 290	_ 450	-
V _{CE(sat)}			0.25 0.6	V
V _{BE(sat)}	-	0.7 0.9	-	V
V _{BE(on)}	580 -	660 -	700 770	mV
		•	•	
f _T	100	-	-	MHz
C _{obo}	-	-	4.5	pF
NF	_	_	10	dB
	V(BR)CEO V(BR)CES V(BR)CBO V(BR)EBO ICBO VCE(sat) VBE(sat) VBE(on) fT Cobo	V(BR)CEO 45 V(BR)CES 50 V(BR)CBO 50 V(BR)EBO 6.0 ICBO - hFE - VCE(sat) - VBE(sat) - VBE(on) 580 - - fT 100 Cobo -	V(BR)CEO 45 - V(BR)CES 50 - V(BR)CBO 50 - V(BR)EBO 6.0 - ICBO - - ICBO - - VCE(sat) - - VBE(sat) - 0.7 VBE(on) 580 660 - - - fT 100 - Cobo - -	V(BR)CEO 45 - - V(BR)CES 50 - - V(BR)CBO 50 - - V(BR)CBO 50 - - V(BR)CBO 6.0 - - V(BR)EBO 6.0 - - ICBO - - 15 - 200 290 450 VCE(sat) - - 0.25 VBE(sat) - 0.7 - VBE(on) 580 660 700 f 100 - - 770 K NF - - 4.5











3X 0.52 - - 0.36 DIMENSIONS: MILLIMETERS

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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 DESCRIPTION:
 SOT-723
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