DAN222M3T5G

Common Cathode Silicon Dual Switching Diode

This Common Cathode Silicon Epitaxial Planar Dual Diode is designed for use in ultra high speed switching applications. This device is housed in the SOT-723 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Fast t_{rr}
- Low C_D
- Available in 4 mm Tape and Reel
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25° C)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	80	V
Peak Reverse Voltage	V _{RM}	80	V
Forward Current	١ _F	100	mA

THERMAL CHARACTERISTICS

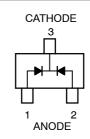
Rating	Symbol	Мах	Unit
Power Dissipation	PD	260	mW
Junction Temperature	TJ	150	°C
Storage Temperature Range	T _{stg}	–55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability. 1. $t = 1.0 \ \mu S$.

ON

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N9 = Specific Device Code M = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
DAN222M3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

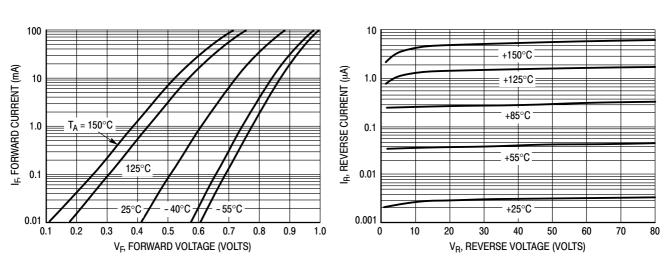
DAN222M3T5G

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

Characteristic	Symbol	Condition	Min	Max	Unit
Reverse Voltage Leakage Current (Note 2)	I _R	V _R = 70 V	-	0.1	μA
Forward Voltage	V _F	I _F = 100 mA	-	1.2	V
Reverse Breakdown Voltage	V _R	I _R = 100 μA	80	-	V
Diode Capacitance	CD	V _R = 6.0 V, f = 1.0 MHz	-	3.5	pF
Reverse Recovery Time (Note 3)	t _{rr}	$I_{F} = 5.0 \text{ mA}, V_{R} = 6.0 \text{ V}, R_{L} = 100 \Omega, I_{rr} = 0.1 I_{R}$	-	4.0	ns

2. For each diode while other is not forward biased.

3. t_{rr} Test Circuit on following page.



TYPICAL ELECTRICAL CHARACTERISTICS

Figure 1. Forward Voltage

Figure 2. Reverse Current

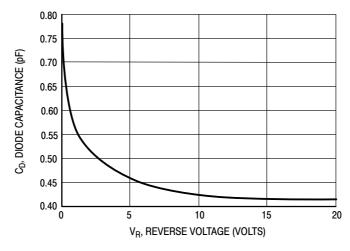
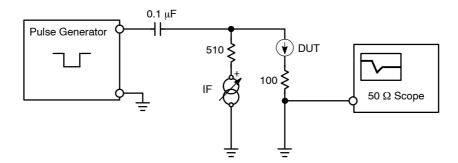
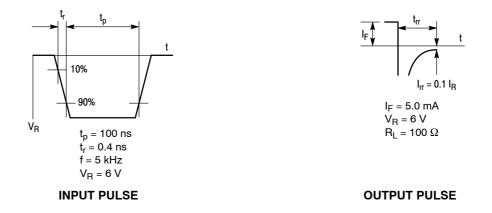


Figure 3. Diode Capacitance

DAN222M3T5G



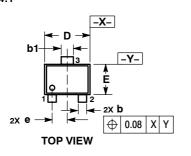
RECOVERY TIME EQUIVALENT TEST CIRCUIT

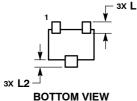


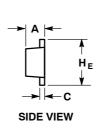












SOT-723 CASE 631AA-01 ISSUE D

DATE 10 AUG 2009

NOTES: UTES:
 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

FLIGHNESS OF DASE MATERIAL. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. 4

	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.45	0.50	0.55	
b	0.15	0.21	0.27	
b1	0.25	0.31	0.37	
С	0.07	0.12	0.17	
D	1.15	1.20	1.25	
ш	0.75	0.80	0.85	
e	0.40 BSC			
ΗE	1.15	1.20	1.25	
L	0.29 REF			
L2	0.15	0.20	0.25	

GENERIC **MARKING DIAGRAM***

	XX M
1	

XX = Specific Device Code Μ = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

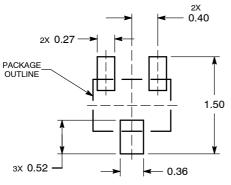


STYLE 2:	STYLE 3:	;
PIN 1. ANODE	PIN 1. ANODE	
2. N/C	2. ANODE	
CATHODE	3. CATHODE	

STYLE 4:	STYLE 5:
PIN 1. CATHODE	PIN 1. GATE
2. CATHODE	2. SOUR
3. ANODE	3. DRAIN

2. SOURCE 3. DRAIN

RECOMMENDED **SOLDERING FOOTPRINT***



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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