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ECH8693R

Power MOSFET for 1-2 Cells Lithium-ion Battery Protection 24 V, 7 mΩ, 14 A, Dual N-Channel



This Power MOSFET features a low on-state resistance. This device is suitable for applications such as power switches of portable machines. Best suited for 1-2 cells Lithium-ion Battery applications.

Features

- Low On-Resistance
- 2.5 V drive
- Common-Drain Type
- ESD Diode-Protected Gate
- Built-in Gate Protection Resistor
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

• 1-2 cells Lithium-ion Battery Charging and Discharging Switch

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	24	V
Gate to Source Voltage	VGSS	±12.5	V
Drain Current (DC)	ID	14	А
Drain Current (Pulse) PW \leq 10 μ s, duty cycle \leq 1%	IDP	60	А
Power Dissipation Surface mounted on ceramic substrate (900 mm ² × 0.8 mm) 1 unit	PD	1.4	W
Total Dissipation Surface mounted on ceramic substrate (900 mm ² × 0.8 mm)	PŢ	1.5	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

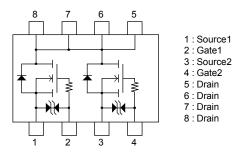
THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient Surface mounted on ceramic substrate (900 mm ² × 0.8 mm) 1 unit	$R_{\theta J A}$	89.2	°C/W

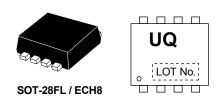
VDSS	R _{DS} (on) Max	ID Max
24 V	7 mΩ @ 4.5 V	
	7.5 mΩ @ 4.0 V	44.4
	9.1 mΩ @ 3.1 V	14 A
	10.5 mΩ @ 2.5 V	

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ELECTRICAL CONNECTION N-Channel



MARKING



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

Devenueter	Currential	Conditions	Value			
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	I _D = 1 mA, V _{GS} = 0 V	24			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} = 20 V, V _{GS} = 0 V			1	μA
Gate to Source Leakage Current	IGSS	V_{GS} = ±8 V, V_{DS} = 0 V			±1	μA
Gate Threshold Voltage	VGS(th)	V _{DS} = 10 V, I _D = 1 mA	0.5		1.3	V
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 5 A		8		S
		I _D = 5 A, V _{GS} = 4.5 V	4.4	5.6	7	mΩ
Static Drain to Source On-State		I _D = 5 A, V _{GS} = 4.0 V	4.6	5.8	7.5	mΩ
Resistance	R _{DS} (on)	I _D = 5 A, V _{GS} = 3.1 V	5.2	6.5	9.1	mΩ
		I _D = 2.5 A, V _{GS} = 2.5 V	6	7.5	10.5	mΩ
Turn-ON Delay Time	t _d (on)			545		ns
Rise Time	tr	See Fig. 1 (Note 3)		525		ns
Turn-OFF Delay Time	t _d (off)			18.65		μS
Fall Time	tf			22.2		μS
Turn-ON Delay Time	t _d (on)			545		ns
Rise Time	tr			525		ns
Turn-OFF Delay Time	td(off)	See Fig. 2 (Note 3)		1,130		μS
Fall Time	tf			410		μS
Total Gate Charge	Qg	V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 14 A		13		nC
Gate to Source Charge	Qgs			3		nC
Gate to Drain "Miller" Charge	Qgd			2.4		nC
Forward Diode Voltage	VSD	I _S = 14 A, V _{GS} = 0 V		0.78	1.2	V

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Note 3 : The fall switching time is dependent on the input pulse width.

Fig.1 Switching Time Test Circuit 1

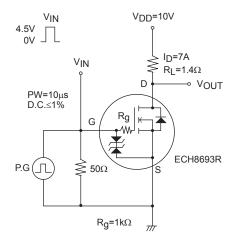
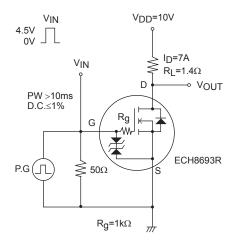
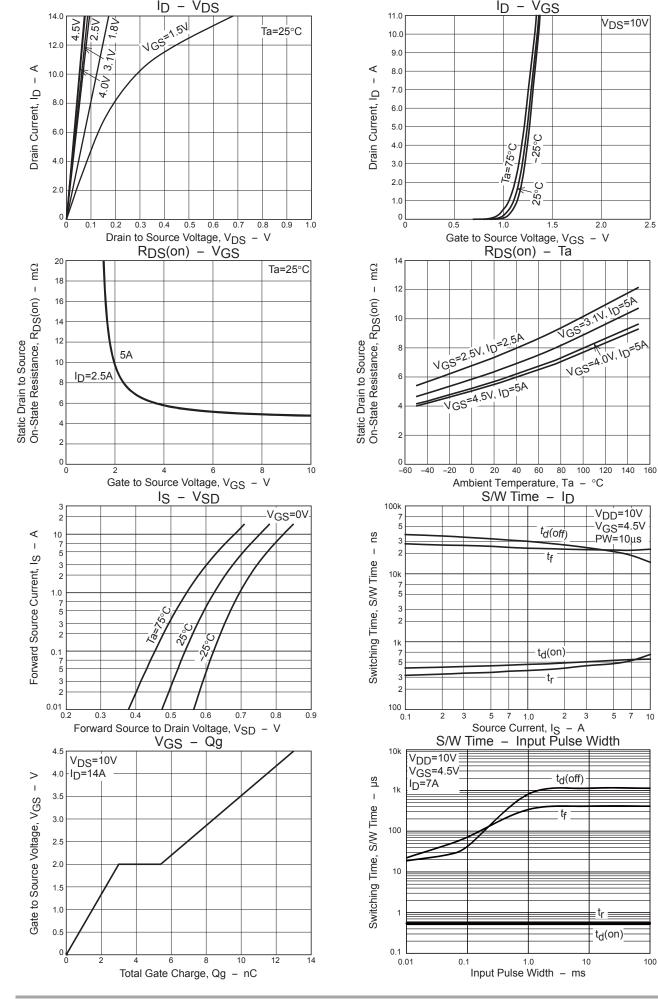


Fig.2 Switching Time Test Circuit 2



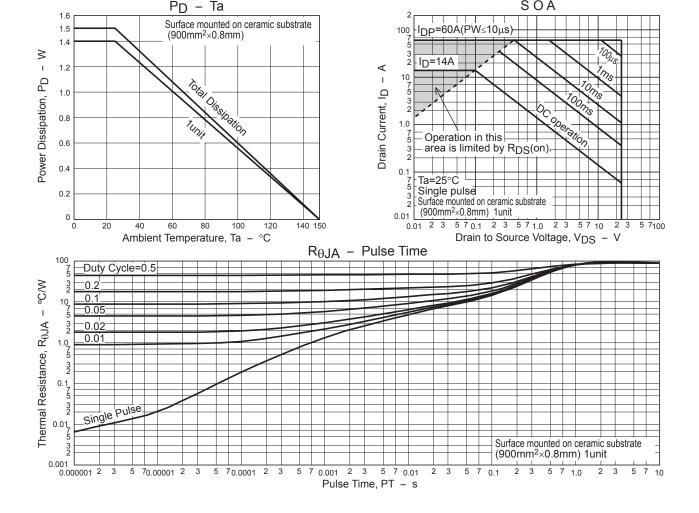


2.5

10

100

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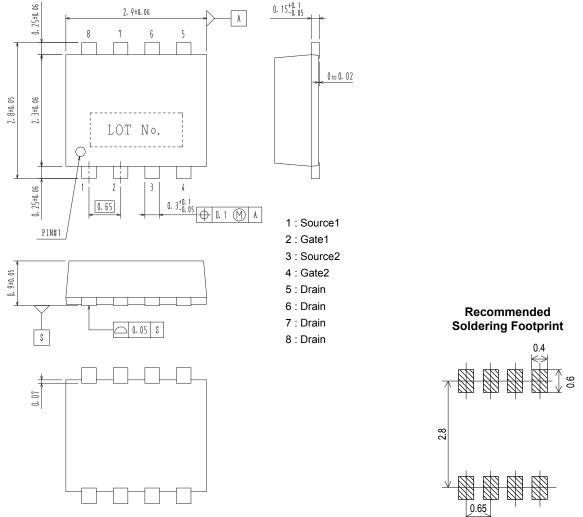


FACINAGE DIMIENSION

unit : mm

SOT-28FL / ECH8





ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
ECH8693R-TL-W	UQ	SOT-28FL / ECH8 (Pb-Free / Halogen Free)	3,000 / Tape & Reel

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the ECH8693R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects. Please contact sales for use except the designated application.

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