## **ESD Protection Diodes**

# Micro-packaged Diodes for ESD Protection

The ESD51x1 Series is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in smartphone, smart-watch, or many other portable / wearable applications where board space comes at a premium.

#### **Features**

- Low Capacitance (5 pF Max, I/O to GND)
- Small Body Outline Dimensions
  - 01005 Size: 0.435 x 0.23 mm
  - 0201 Size: 0.6 x 0.3 mm
- Protection for the Following IEC Standards: IEC 61000-4-2 (Level 4)
- Low ESD Clamping Voltage
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Operating Junction Temperature Range	$T_J$	-55 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C
Lead Solder Temperature – Maximum (10 Seconds)	TL	260	°C
IEC 61000-4-2 Contact (ESD) IEC 61000-4-2 Air (ESD)	ESD ESD	±15 ±15	kV kV

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

See Application Note AND8308/D for further description of survivability specs.



### ON Semiconductor®

www.onsemi.com

#### MARKING DIAGRAMS



ESD5101 (01005) DSN2 CASE 152AK





ESD5111 (0201) WLCSP2 CASE 567AV



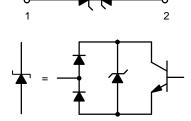


ESD5111P (0201) DSN2 CASE 152AX



L, E, P = Device Code

# PIN CONFIGURATION AND SCHEMATIC



#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Reverse Working Voltage	$V_{RWM}$	I/O Pin to GND			3.3	V
Breakdown Voltage	$V_{BR}$	I <sub>T</sub> = 1 mA, I/O Pin to GND	3.68	5.0	6.5	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 3.3 V, I/O Pin to GND			0.1	μΑ
ESD5101, ESD5111 Clamping Voltage TLP (Note 1)	V <sub>C</sub>	$I_{PP} = 8 \text{ A}$		5.5		٧
		IPP = 16 A Section   IEC 61000-4-2 Level 2 equivalent   (±8 kV Contact, ±15 kV Air)		6.5		
Junction Capacitance	СЈ	V <sub>R</sub> = 0 V, f = 1 MHz			5.5	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
ESD5101FCT5G	DSN2 (Pb-Free)	10,000 / Tape & Reel
ESD5111FCT5G	WLCSP2 (Pb-Free)	10,000 / Tape & Reel
ESD5111PFCT5G	DSN2 (Pb-Free)	10,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>1.</sup> ANSI/ESD STM5.5.1 – Electrostatic Discharge Sensitivity Testing using Transmission Line Pulse (TLP) Model. TLP conditions:  $Z_0 = 50 \ \Omega$ ,  $t_p = 100 \ ns$ ,  $t_r = 4 \ ns$ , averaging window;  $t_1 = 30 \ ns$  to  $t_2 = 60 \ ns$ .

#### **TYPICAL CHARACTERISTICS**

C (pF)

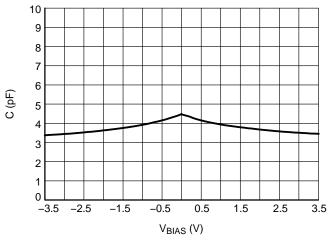


Figure 1. ESD5101 CV Characteristics

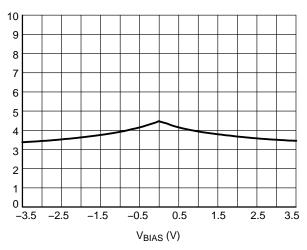


Figure 2. ESD5111 CV Characteristics

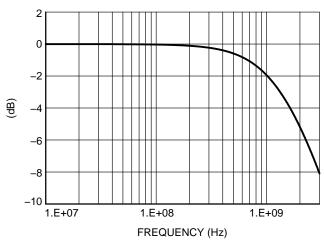


Figure 3. ESD5101 S21 Insertion Loss

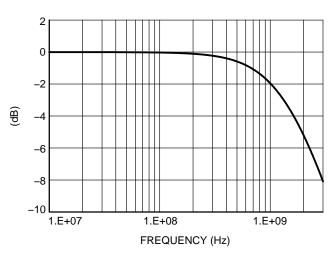


Figure 4. ESD5111 S21 Insertion Loss

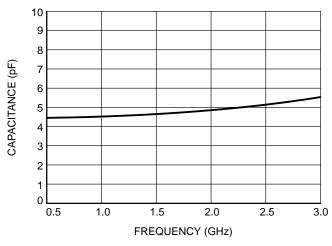


Figure 5. ESD5101 Capacitance over Frequency

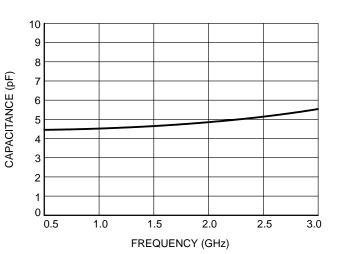


Figure 6. ESD5111 Capacitance over Frequency

#### **TYPICAL CHARACTERISTICS**

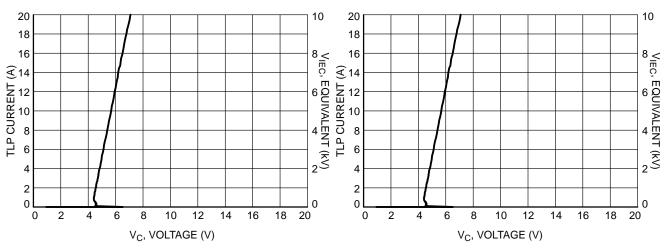


Figure 7. ESD5101 Positive TLP I-V Curve

Figure 8. ESD5111 Positive TLP I-V Curve

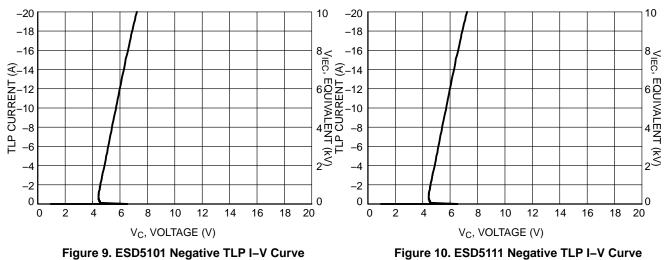


Figure 9. ESD5101 Negative TLP I-V Curve

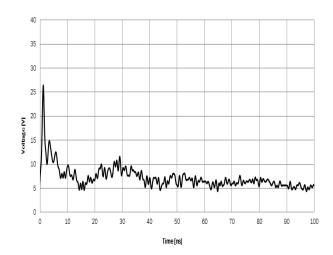


Figure 11. ESD5111 Positive 8 kV ESD Contact **Discharge** 

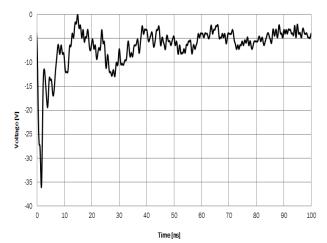


Figure 12. ESD5111 Negative 8 kV ESD **Contact Discharge** 

#### IEC 61000-4-2 Spec.

Level	Test Volt- age (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8

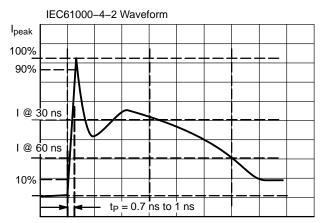


Figure 13. IEC61000-4-2 Spec

#### Transmission Line Pulse (TLP) Measurement

Transmission Line Pulse (TLP) provides current versus voltage (I–V) curves in which each data point is obtained from a 100 ns long rectangular pulse from a charged transmission line. A simplified schematic of a typical TLP system is shown in Figure 14. TLP I–V curves of ESD protection devices accurately demonstrate the product's ESD capability because the 10s of amps current levels and under 100 ns time scale match those of an ESD event. This is illustrated in Figure 15 where an 8 kV IEC 61000–4–2 current waveform is compared with TLP current pulses at 8 A and 16 A. A TLP I–V curve shows the voltage at which the device turns on as well as how well the device clamps voltage over a range of current levels.

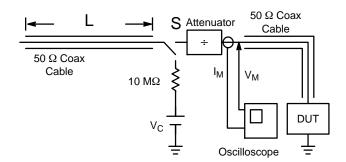


Figure 14. Simplified Schematic of a Typical TLP System

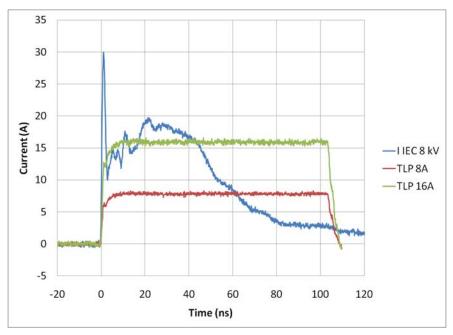
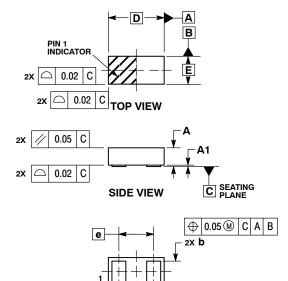


Figure 15. Comparison Between 8 kV IEC 61000-4-2 and 8 A and 16 A TLP Waveforms



#### DSN2, 0.435x0.23, 0.27P, (01005) CASE 152AK **ISSUE A**

**DATE 17 FEB 2015** 



**BOTTOM VIEW** 

2X

В

⊕ 0.05 M C

#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.

	MILLIMETERS			
DIM	MIN MAX			
Α	0.165 0.195			
A1	0.030			
b	0.177 0.193			
D	0.435 BSC			
Е	0.230 BSC			
е	0.270 BSC			
L	0.112 0.128			

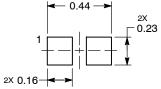
#### **GENERIC MARKING DIAGRAM\***



#### X = Specific Device Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

#### RECOMMENDED **SOLDER FOOTPRINT\***



**DIMENSIONS: MILLIMETERS** 

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

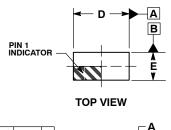
DOCUMENT NUMBER:	98AON82198E	Electronic versions are uncontrolled except when accessed directly from the Document Repositor Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	DSN2, 0.435X0.23, 0.27P (0	DSN2, 0.435X0.23, 0.27P (01005)	

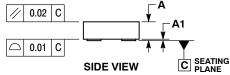
ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

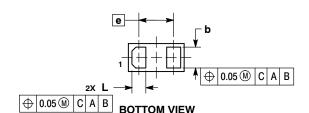


#### X4DFN2, 0.60x0.30, 0.36P CASE 152AX ISSUE G

**DATE 12 APR 2019** 







- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.

	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	0.175	0.200	0.225		
A1	0	0.018 REF			
b	0.205	0.215	0.225		
D	0.575	0.600	0.625		
Е	0.275	0.300	0.325		
е	0.36 BSC				
L	0.145	0.155	0.165		

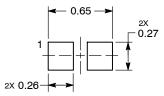
#### **GENERIC MARKING DIAGRAM\***



#### X = Specific Device Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present. Some products may not follow the Generic Marking.

#### **RECOMMENDED SOLDER FOOTPRINT\***



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

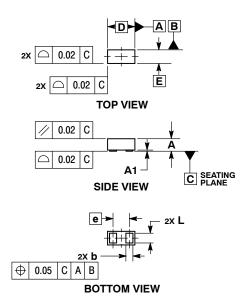
DOCUMENT NUMBER:	98AON06808G	Electronic versions are uncontrolled except when accessed directly from the Document Repositor Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	X4DFN2, 0.60x0.30, 0.36P		PAGE 1 OF 1

ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.



WLCSP2, 0.6x0.3 CASE 567AV ISSUE C

**DATE 22 SEP 2017** 



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.

		MILLIMETERS				
	MIC	MIN	MIN NOM MAX			
	Α	0.250	0.275	0.300		
	<b>A</b> 1	0.000	0.025	0.050		
	b	0.140	0.155	0.170		
	D	0.570	0.600	0.630		
	Е	0.270	0.300	0.330		
	е	0.36 BSC				
Г	1	0 190	0.215	0.240		

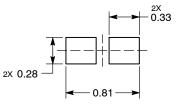
#### **GENERIC MARKING DIAGRAM\***



= Specific Device Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present. Some products may not follow the Generic Marking.

#### **RECOMMENDED SOLDER FOOTPRINT\***



**DIMENSIONS: MILLIMETERS** 

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DOCUMENT NUMBER:	98AON49805E	Electronic versions are uncontrolled except when accessed directly from the Document Reposito Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	WLCSP2, 0.6X0.3		PAGE 1 OF 1

ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer pu

#### **PUBLICATION ORDERING INFORMATION**

LITERATURE FULFILLMENT:
Email Requests to: orderlit@onsemi.com

onsemi Website: www.onsemi.com

TECHNICAL SUPPORT North American Technical Support: Voice Mail: 1 800-282-9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative

## **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for ESD Suppressors / TVS Diodes category:

Click to view products by ON Semiconductor manufacturer:

Other Similar products are found below:

60KS200C D12V0H1U2WS-7 D18V0L1B2LP-7B 82356050220 D5V0M5U6V-7 NTE4902 P4KE27CA P6KE11CA P6KE39CA-TP
P6KE8.2A SA110CA SA60CA SA64CA SMBJ12CATR SMBJ8.0A SMLJ30CA-TP ESD101-B1-02ELS E6327 ESD112-B1-02EL E6327
ESD119B1W01005E6327XTSA1 ESD5V0J4-TP ESD5V0L1B02VH6327XTSA1 ESD7451N2T5G 19180-510 CPDT-5V0USP-HF
3.0SMCJ33CA-F 3.0SMCJ36A-F HSPC16701B02TP D3V3Q1B2DLP3-7 D55V0M1B2WS-7 DESD5V0U1BL-7B DRTR5V0U4SL-7
SCM1293A-04SO ESD200-B1-CSP0201 E6327 ESD203-B1-02EL E6327 SM12-7 SMF8.0A-TP SMLJ45CA-TP CEN955 W/DATA
82350120560 82356240030 VESD12A1A-HD1-GS08 CPDUR5V0R-HF CPDUR24V-HF CPDQC5V0U-HF CPDQC5V0USP-HF
CPDQC5V0-HF D1213A-01LP4-7B D1213A-02WL-7 ESDLIN1524BJ-HQ 5KP100A