# **MOSFET** – Power, N-Channel, SUPERFET III, Easy Drive

## 650 V, 6 A, 600 m $\Omega$

#### Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET Easy drive series helps manage EMI issues and allows for easier design implementation.

#### Features

- 700 V @ T<sub>J</sub> = 150°C
- Typ.  $R_{DS(on)} = 493 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 11 \text{ nC}$ )
- Low Effective Output Capacitance (Typ. Coss(eff.) = 127 pF)
- 100% Avalanche Tested
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### Applications

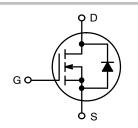
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter



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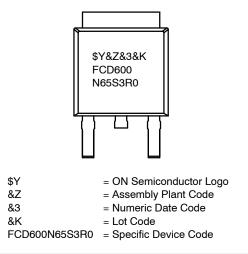
| V <sub>DSS</sub> | R <sub>DS(ON)</sub> MAX | I <sub>D</sub> MAX |
|------------------|-------------------------|--------------------|
| 650 V            | 600 m $\Omega$ @ 10 V   | 6 A                |



**N-Channel MOSFET** 



#### MARKING DIAGRAM



#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

| Symbol                            | Paramete                                | Value                               | Unit        |      |
|-----------------------------------|---|-------------------------------------|-------------|------|
| V <sub>DSS</sub>                  | Drain to Source Voltage                 | 650                                 | V           |      |
| V <sub>GSS</sub>                  | Gate to Source Voltage                  | DC                                  | ±30         | V    |
|                                   |   | AC (f > 1 Hz)                       | ±30         | V    |
| I <sub>D</sub>                    | Drain Current                           | Continuous (T <sub>C</sub> = 25°C)  | 6           | А    |
|                                   |   | Continuous (T <sub>C</sub> = 100°C) | 3.8         |      |
| I <sub>DM</sub>                   | Drain Current                           | Pulsed (Note 1)                     | 15          | А    |
| E <sub>AS</sub>                   | Single Pulsed Avalanche Energy (Note 2) |                                     | 24          | mJ   |
| I <sub>AS</sub>                   | Avalanche Current (Note 2)              |                                     | 1.6         | А    |
| E <sub>AR</sub>                   | Repetitive Avalanche Energy (Note 1)    |                                     | 0.54        | mJ   |
| dv/dt                             | MOSFET dv/dt                            |                                     | 100         | V/ns |
|                                   | Peak Diode Recovery dv/dt (Note 3)      |                                     | 20          |      |
| PD                                | Power Dissipation                       | (T <sub>C</sub> = 25°C)             | 54          | W    |
|                                   |   | Derate Above 25°C                   | 0.43        | W/°C |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temperature Range |                                     | -55 to +150 | °C   |
| ΤL                                | Maximum Lead Temperature for Soldering, | 300                                 | °C          |      |

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, Unless otherwise specified)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature. 2.  $I_{AS} = 1.6 \text{ A}$ ,  $R_G = 25 \Omega$ , starting  $T_J = 25^{\circ}\text{C}$ . 3.  $I_{SD} \le 3 \text{ A}$ , di/dt  $\le 200 \text{ A}/\mu\text{s}$ ,  $V_{DD} \le 400 \text{ V}$ , starting  $T_J = 25^{\circ}\text{C}$ .

#### **THERMAL CHARACTERISTICS**

| Symbol                | Parameter  | Value | Unit |
|-----------------------|--|-------|------|
| $R_{	extsf{	heta}JC}$ | Thermal Resistance, Junction to Case, Max.             | 2.3   | °C/W |
| $R_{	hetaJA}$         | Thermal Resistance, Junction to Ambient, Max. (Note 4) | 52    |      |

4. Device on 1 in<sup>2</sup> pad 2 oz copper pad on 1.5 x 1.5 in. board of FR-4 material.

#### PACKAGE MARKING AND ORDERING INFORMATION

| Part Number   | Top Marking   | Package   | Reel Size | Tape Width | Shipping (Qty / Packing) $^{\dagger}$ |
|---------------|---------------|---|-----------|------------|---------------------------------------|
| FCD600N65S3R0 | FCD600N65S3R0 | D-PAK<br>(DPAK3 (TO-252 3LD))<br>(Pb-Free / Halogen Free) | 330 mm    | 16 mm      | 2500 / Tape & Reel                    |

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

| Symbol   | Parameter  | Test Conditions   | Min | Тур  | Max  | Unit |
|--|--|---|-----|------|------|------|
| OFF CHARACT  | ERISTICS   | -   |     |      |      |      |
| BV <sub>DSS</sub>  | Drain to Source Breakdown Voltage                        | $V_{GS}$ = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 25°C                      | 650 | -    | -    | V    |
|  |  | $V_{GS}$ = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 150°C                     | 700 | -    | -    | V    |
| $\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$ | Breakdown Voltage Temperature<br>Coefficient             | $I_D = 1$ mA, Referenced to $25^{\circ}C$   | -   | 0.66 | -    | V/°C |
| I <sub>DSS</sub>   | Zero Gate Voltage Drain Current                          | V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V                                    | -   | -    | 1    | μΑ   |
|  |  | $V_{DS}$ = 520 V, $T_{C}$ = 125°C   | -   | 0.3  | -    |      |
| I <sub>GSS</sub>   | Gate to Body Leakage Current                             | $V_{GS}$ = ±30 V, $V_{DS}$ = 0 V  | -   | -    | ±100 | nA   |
| ON CHARACTE  | ERISTICS   | •   |     |      |      |      |
| V <sub>GS(th)</sub>  | Gate Threshold Voltage                                   | $V_{GS} = V_{DS}, I_{D} = 0.12 \text{ mA}$  | 2.5 | -    | 4.5  | V    |
| R <sub>DS(on)</sub>  | Static Drain to Source On Resistance                     | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A                                      | -   | 493  | 600  | mΩ   |
| 9 <sub>FS</sub>  | Forward Transconductance                                 | V <sub>DS</sub> = 20 V, I <sub>D</sub> = 3 A                                      | -   | 3.6  | -    | S    |
| OYNAMIC CHA  | RACTERISTICS   | •   |     |      |      |      |
| C <sub>iss</sub>   | Input Capacitance  | $V_{DS}$ = 400 V, $V_{GS}$ = 0 V, f = 1 MHz                                       | -   | 465  | -    | pF   |
| Coss   | Output Capacitance                                       |   | -   | 10   | -    | pF   |
| C <sub>oss(eff.)</sub>                                       | Effective Output Capacitance                             | $V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V   | -   | 127  | -    | pF   |
| C <sub>oss(er.)</sub>  | Energy Related Output Capacitance                        | $V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V   | -   | 17   | -    | pF   |
| Q <sub>g(tot)</sub>  | Total Gate Charge at 10 V                                | V <sub>DS</sub> = 400 V, I <sub>D</sub> = 3 A, V <sub>GS</sub> = 10 V<br>(Note 5) | -   | 11   | -    | nC   |
| Q <sub>gs</sub>  | Gate to Source Gate Charge                               |   | -   | 3    | -    | nC   |
| Q <sub>gd</sub>  | Gate to Drain "Miller" Charge                            |   | -   | 4.9  | -    | nC   |
| ESR  | Equivalent Series Resistance                             | f = 1 MHz   | -   | 0.9  | -    | Ω    |
| WITCHING CH  | IARACTERISTICS   | •   | -   |      |      |      |
| t <sub>d(on)</sub>   | Turn-On Delay Time                                       | $V_{DD} = 400 \text{ V}, I_D = 3 \text{ A},$                                      | -   | 11   | -    | ns   |
| t <sub>r</sub>   | Turn-On Rise Time  | $V_{GS} = 10 \text{ V}, \text{ R}_{g} = 4.7 \Omega$<br>(Note 5)                   | -   | 9    | -    | ns   |
| t <sub>d(off)</sub>  | Turn-Off Delay Time                                      |   | -   | 29   | -    | ns   |
| t <sub>f</sub>   | Turn-Off Fall Time                                       |   | -   | 14   | -    | ns   |
| SOURCE-DRAI  | N DIODE CHARACTERISTICS                                  | •   |     |      |      |      |
| ۱ <sub>S</sub>   | Maximum Continuous Source to Drain Diode Forward Current |   | -   | -    | 6    | Α    |
| I <sub>SM</sub>  | Maximum Pulsed Source to Drain Diode Forward Current     |   | -   | -    | 15   | А    |
| V <sub>SD</sub>  | Source to Drain Diode Forward<br>Voltage                 | $V_{GS}$ = 0 V, I <sub>SD</sub> = 3 A   | -   | -    | 1.2  | V    |
| t <sub>rr</sub>  | Reverse Recovery Time                                    | $V_{GS} = 0 V, I_{SD} = 3 A,$   | -   | 198  | -    | ns   |
| Q <sub>rr</sub>  | Reverse Recovery Charge                                  | dI <sub>F</sub> /dt = 100 A/µs  | _   | 1.6  | -    | μC   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 5. Essentially independent of operating temperature typical characteristics.

#### **TYPICAL PERFORMANCE CHARACTERISTICS**

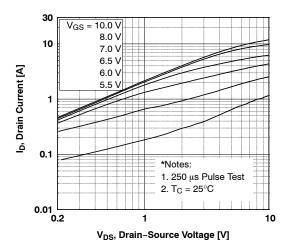


Figure 1. On-Region Characteristics

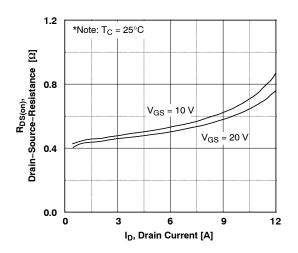


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

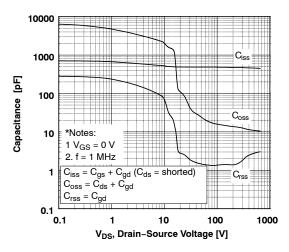
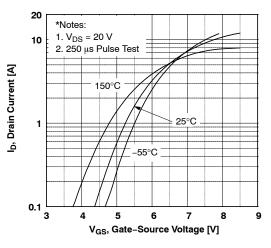


Figure 5. Capacitance Characteristics



**Figure 2. Transfer Characteristics** 

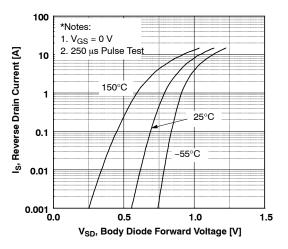


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

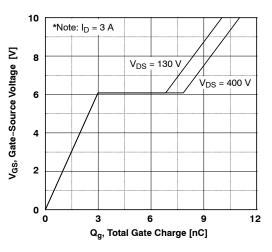
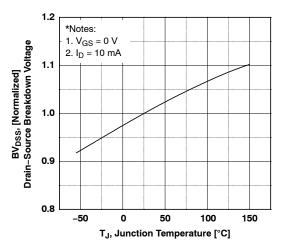


Figure 6. Gate Charge Characteristics

#### TYPICAL PERFORMANCE CHARACTERISTICS (Continued)





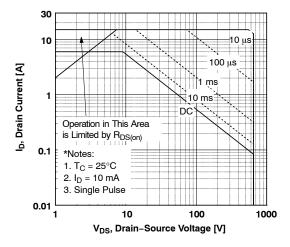


Figure 9. Maximum Safe Operation Area

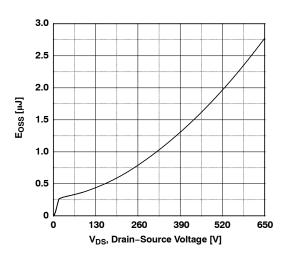


Figure 11. E<sub>OSS</sub> vs. Drain to Source Voltage

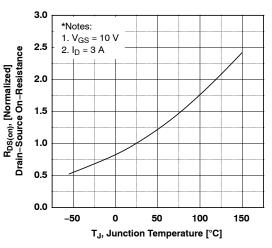


Figure 8. On-Resistance Variant vs. Temperature

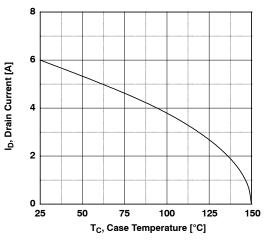


Figure 10. Maximum Drain Current vs. Case Temperature

### TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

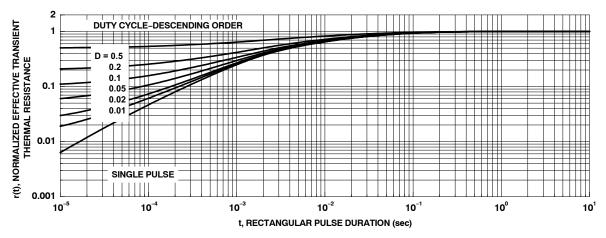
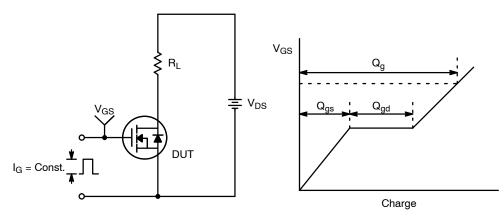


Figure 12. Transient Thermal Response Curve





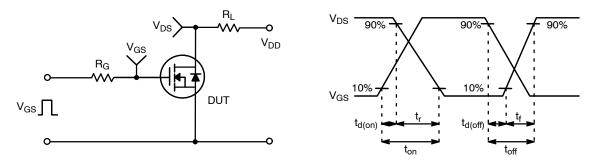


Figure 14. Resistive Switching Test Circuit & Waveforms

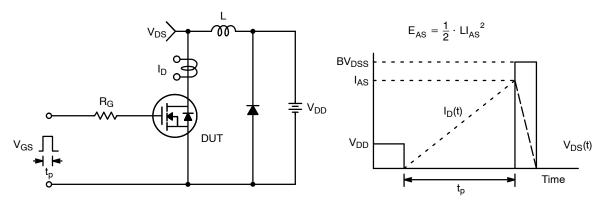


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

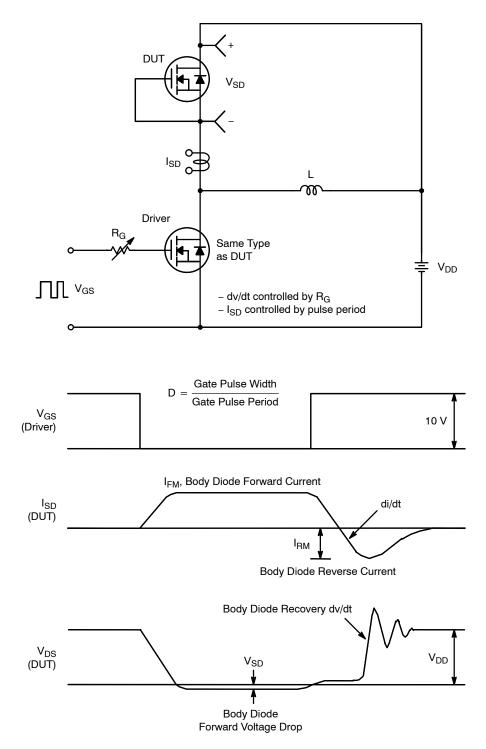
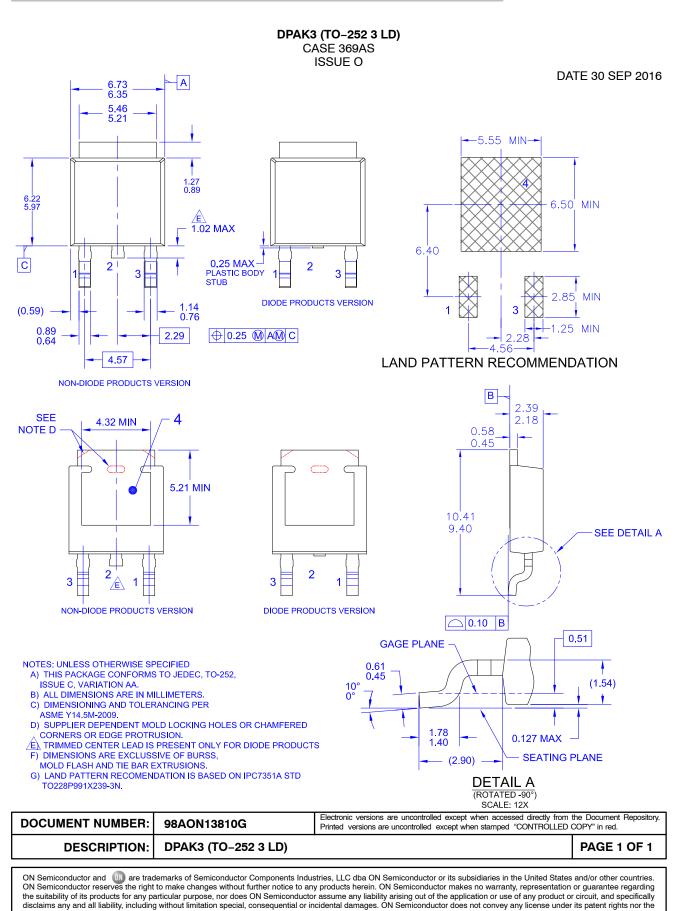


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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