# MOSFET – N-Channel, SUPERFET<sup>®</sup> II, Easy-Drive 600 V, 52 A, 70 m $\Omega$

# FCH070N60E

# **Description**

SUPERFET II MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SUPERFET II MOSFET easy-drive series offers slightly slower rise and fall times compared to the SUPERFET II MOSFET series. Noted by the "E" part number suffix, this family helps manage EMI issues and allows for easier design implementation. For faster switching in applications where switching losses must be at an absolute minimum, please consider the SUPERFET II MOSFET series.

# **Features**

- Typ.  $R_{DS(on)} = 58 \text{ m}\Omega$
- $650 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 128 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 457 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

# **Applications**

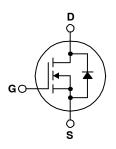
- Telecom / Sever Power Supplies
- Industrial Power Supplies



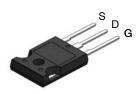
# ON Semiconductor®

## www.onsemi.com

| V <sub>DS</sub> | R <sub>DS(ON)</sub> MAX | I <sub>D</sub> MAX |
|-----------------|-------------------------|--------------------|
| 600 V           | 70 mΩ @ 10 V            | 52 A               |



**N-CHANNEL MOSFET** 



**TO-247-3LD CASE 340CK** 

## **MARKING DIAGRAM**



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Numeric Date Code

&K = Lot Code

FCH070N60E = Specific Device Code

## **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

# **ABSOLUTE MAXIMUM RATINGS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

| Symbol                            | Parameter  |                                       | FCH070N60E   | Unit |
|-----------------------------------|--|---------------------------------------|--------------|------|
| $V_{DSS}$                         | Drain to Source Voltage  |                                       | 600          | V    |
| $V_{GSS}$                         | Gate to Source Voltage   | - DC                                  | ±20          | V    |
|                                   |  | - AC (f > 1 Hz)                       | ±30          |      |
| I <sub>D</sub>                    | Drain Current:   | – Continuous (T <sub>C</sub> = 25°C)  | 52           | Α    |
|                                   |  | - Continuous (T <sub>C</sub> = 100°C) | 33           | 1    |
| I <sub>DM</sub>                   | Drain Current:   | - Pulsed (Note 1)                     | 156          | Α    |
| E <sub>AS</sub>                   | Single Pulsed Avalanche Energy (Note 2)                              |                                       | 1128         | mJ   |
| I <sub>AR</sub>                   | Avalanche Current (Note 1)   |                                       | 9.5          | Α    |
| E <sub>AR</sub>                   | Repetitive Avalanche Energy (Note 1)                                 |                                       | 4.8          | mJ   |
| dv/dt                             | MOSFET dv/dt   |                                       | 100          | V/ns |
|                                   | Peak Diode Recovery dv/dt (Note 3)                                   | 20                                    |              |      |
| $P_{D}$                           | Power Dissipation  | (T <sub>C</sub> = 25°C)               | 481          | W    |
|                                   |  | - Derate Above 25°C                   | 3.85         | W/°C |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temperature Range                              |                                       | -55 to + 150 | °C   |
| TL                                | Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds |                                       | 300          | °C   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse–width limited by maximum junction temperature. 2.  $I_{AS} = 9.5 \text{ A}$ ,  $R_{G} = 25 \Omega$ , Starting  $T_{J} = 25 ^{\circ}\text{C}$ . 3.  $I_{SD} \le 26 \text{ A}$ ,  $di/dt \le 200 \text{ A/µs}$ ,  $V_{DD} \le 380 \text{ V}$ , Starting  $T_{J} = 25 ^{\circ}\text{C}$ .

# PACKAGE MARKING AND ORDERING INFORMATION

| Part Number | Top Marking | Package | Packing Method | Reel Size | Tape Width | Quantity |
|-------------|-------------|---------|----------------|-----------|------------|----------|
| FCH070N60E  | FCH070N60E  | TO-247  | Tube           | N/A       | N/A        | 30 Units |

# THERMAL CHARACTERISTICS

| Symbol          | Parameter                                     | FCH070N60E | Unit |
|-----------------|---|------------|------|
| $R_{	heta JC}$  | Thermal Resistance, Junction to Case, Max.    | 0.26       | °C/W |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient, Max. | 40         |      |

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

| Symbol                           | Parameter  | Test Condition  | Min. | Тур. | Max. | Unit |
|----------------------------------|--|---|------|------|------|------|
| OFF CHARA                        | ACTERISTICS  |   |      |      |      |      |
| BV <sub>DSS</sub> [              | Drain to Source Breakdown Voltage                        | $I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}, T_J = 25^{\circ}\text{C}$   | 600  | -    | _    | V    |
|                                  |  | I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150°C   | 650  | -    | _    |      |
| $\Delta BV_{DSS} / \Delta T_{J}$ | Breakdown Voltage Temperature<br>Coefficient             | I <sub>D</sub> = 10 mA, Referenced to 25°C                              | -    | 0.7  | _    | V/°C |
| I <sub>DSS</sub>                 | Zero Gate Voltage Drain Current                          | V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V                          | _    | -    | 1    | μΑ   |
|                                  |  | V <sub>DS</sub> = 480 V, V <sub>GS</sub> = 0 V, T <sub>C</sub> = 125 °C | -    | 3.4  | _    |      |
| I <sub>GSS</sub>                 | Gate to Body Leakage Current                             | V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V                          | _    | -    | ±100 | nA   |
| ON CHARA                         | CTERISTICS   |   |      |      |      |      |
| V <sub>GS(th)</sub>              | Gate Threshold Voltage                                   | $V_{GS} = V_{DS}, I_D = 250 \mu A$                                      | 2.5  | -    | 3.5  | V    |
| R <sub>DS(on)</sub>              | Static Drain to Source On Resistance                     | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 26 A                           | -    | 58   | 70   | mΩ   |
| 9FS                              | Forward Transconductance                                 | V <sub>DS</sub> = 20 V, I <sub>D</sub> = 26 A                           | _    | 44   | _    | S    |
| DYNAMIC C                        | HARACTERISTICS   |   |      |      |      |      |
| C <sub>iss</sub>                 | Input Capacitance  | V <sub>DS</sub> = 380 V, V <sub>GS</sub> = 0 V, f = 1 MHz               | _    | 3705 | 4925 | pF   |
| C <sub>oss</sub>                 | Output Capacitance                                       | 7   | _    | 116  | 155  | pF   |
| C <sub>rss</sub>                 | Reverse Transfer Capacitance                             | 7   | -    | 12.3 | 20   | pF   |
| C <sub>oss(eff.)</sub>           | Effective Output Capacitance                             | V <sub>DS</sub> = 0 V to 480 V, V <sub>GS</sub> = 0 V                   | _    | 457  | _    | pF   |
| Q <sub>g(tot)</sub>              | Total Gate Charge at 10 V                                | V <sub>DS</sub> = 380 V, I <sub>D</sub> = 26 A, V <sub>GS</sub> = 10 V  | _    | 128  | 166  | nC   |
| Q <sub>gs</sub>                  | Gate to Source Gate Charge                               | (Note 4)  | _    | 18   | _    | nC   |
| Q <sub>gd</sub>                  | Gate to Drain "Miller" Charge                            | 7   | _    | 54   | _    | nC   |
| ESR                              | Equivalent Series Resistance                             | f = 1 MHz   | -    | 0.6  | _    | Ω    |
| SWITCHING                        | CHARACTERISTICS  |   |      |      |      |      |
| t <sub>d(on)</sub>               | Turn-On Delay Time                                       | V <sub>DD</sub> = 380 V, I <sub>D</sub> = 26 A,                         | _    | 29   | 68   | ns   |
| t <sub>r</sub>                   | Turn-On Rise Time  | $V_{GS} = 10 \text{ V}, R_g = 4.7 \Omega$ (Note 4)                      | _    | 28   | 66   | ns   |
| t <sub>d(off)</sub>              | Turn-Off Delay Time                                      |   | _    | 122  | 254  | ns   |
| t <sub>f</sub>                   | Turn-Off Fall Time                                       | 7   | _    | 28   | 66   | ns   |
| DRAIN-SOU                        | RCE DIODE CHARACTERISTICS                                |   |      |      |      |      |
| I <sub>S</sub>                   | Maximum Continuous Source to Drain Diode Forward Current |   |      | _    | 52   | Α    |
| I <sub>SM</sub>                  | Maximum Pulsed Drain to Source Diode F                   | Maximum Pulsed Drain to Source Diode Forward Current                    |      | -    | 156  | Α    |
| V <sub>SD</sub>                  | Drain to Source Diode Forward Voltage                    | V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 26 A                           | -    | -    | 1.2  | V    |
| t <sub>rr</sub>                  | Reverse Recovery Time                                    | V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 26 A,                          | -    | 463  | -    | ns   |
| Q <sub>rr</sub>                  | Reverse Recovery Charge                                  | $dI_{F}/dt = 100 A/\mu s$   | _    | 10.4 | _    | μC   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature.

## TYPICAL CHARACTERISTICS

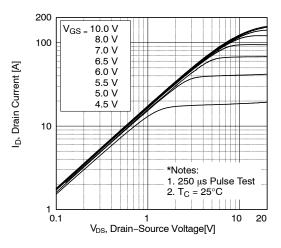


Figure 1. On-Region Characteristics

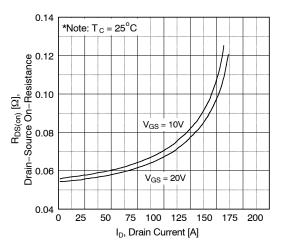


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

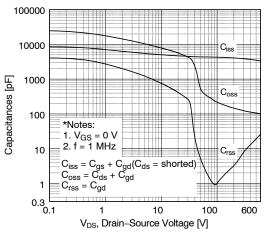


Figure 5. Capacitance Characteristics

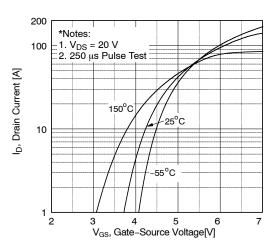


Figure 2. Transfer Characteristics

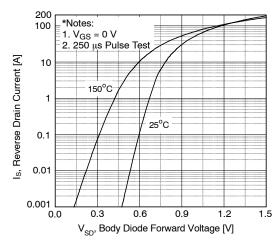


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

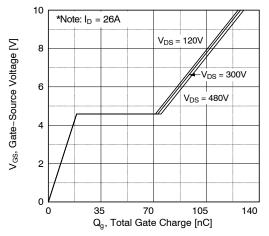


Figure 6. Gate Charge Characteristics

# **TYPICAL CHARACTERISTICS**

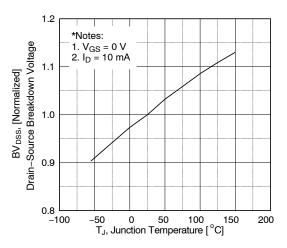


Figure 7. Breakdown Voltage Variation vs. Temperature

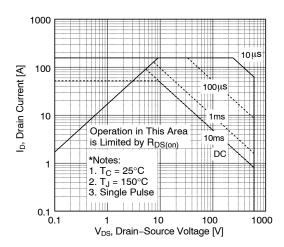


Figure 9. Maximum Safe Operating Area

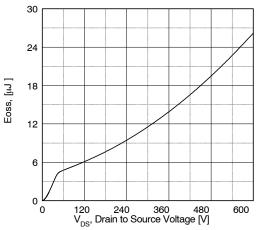


Figure 11. Eoss vs. Drain to Source Voltage

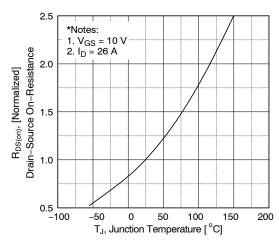


Figure 8. On–Resistance Variation vs. Temperature

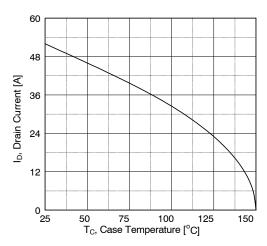


Figure 10. Maximum Drain Current vs. Case Temperature

# TYPICAL CHARACTERISTICS

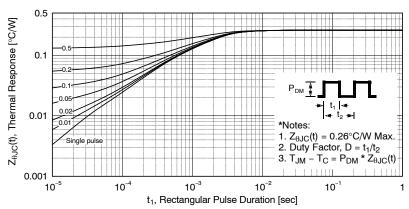


Figure 12. Transient Thermal Response Curve

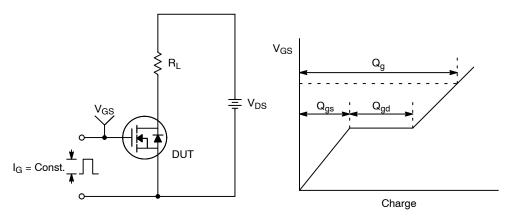


Figure 13. Gate Charge Test Circuit & Waveform

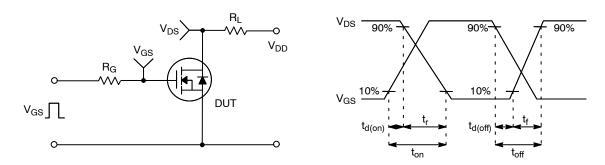


Figure 14. Resistive Switching Test Circuit & Waveforms

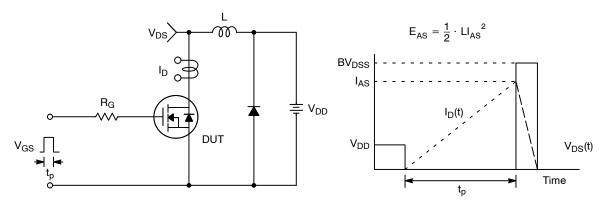


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

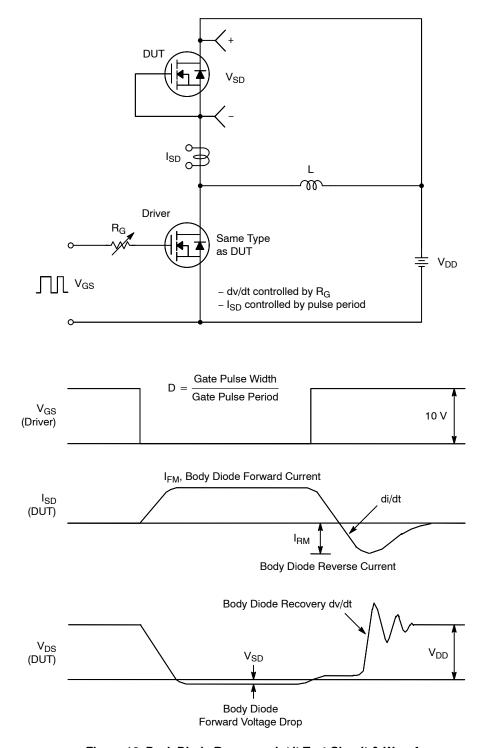


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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# TO-247-3LD SHORT LEAD

CASE 340CK ISSUE A





- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

# GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code

A = Assembly Location

Y = Year

WW = Work Week

ZZ = Assembly Lot Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



| DIM        | MILLIMETERS |       |       |  |  |
|------------|-------------|-------|-------|--|--|
| DIIVI      | MIN         | NOM   | MAX   |  |  |
| Α          | 4.58        | 4.70  | 4.82  |  |  |
| <b>A</b> 1 | 2.20        | 2.40  | 2.60  |  |  |
| A2         | 1.40        | 1.50  | 1.60  |  |  |
| b          | 1.17        | 1.26  | 1.35  |  |  |
| b2         | 1.53        | 1.65  | 1.77  |  |  |
| b4         | 2.42        | 2.54  | 2.66  |  |  |
| С          | 0.51        | 0.61  | 0.71  |  |  |
| D          | 20.32       | 20.57 | 20.82 |  |  |
| D1         | 13.08       | ~     | ~     |  |  |
| D2         | 0.51        | 0.93  | 1.35  |  |  |
| E          | 15.37       | 15.62 | 15.87 |  |  |
| E1         | 12.81       | ?     | ~     |  |  |
| E2         | 4.96        | 5.08  | 5.20  |  |  |
| е          | ~           | 5.56  | ~     |  |  |
| L          | 15.75       | 16.00 | 16.25 |  |  |
| L1         | 3.69        | 3.81  | 3.93  |  |  |
| ØΡ         | 3.51        | 3.58  | 3.65  |  |  |
| ØP1        | 6.60        | 6.80  | 7.00  |  |  |
| Q          | 5.34        | 5.46  | 5.58  |  |  |
| S          | 5.34        | 5.46  | 5.58  |  |  |

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