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FCP600N60Z / FCPF600N60Z N-Channel SuperFET[®] II MOSFET 600 V, 7.4 A, 600 m Ω

Features

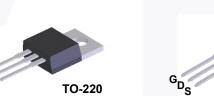
- 650 V @ T_{.I} = 150°C
- Typ. R_{DS(on)} = 510 mΩ
- Ultra Low Gate Charge (Typ. Q_q = 20 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 74 pF)
- 100% Avalanche Tested
- ESD Improved Capacity
- RoHS Compliant

Applications

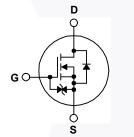
- LCD / LED / PDP TV and Monitor Lightning
- Solar Inverter
- AC-DC Power Supply

Description

SuperFET[®] II MOSFET is Fairchild Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET II MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.







Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

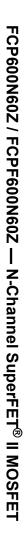
Symbol		FCP600N60Z	FCPF600N60Z	Unit			
V _{DSS}	Drain to Source Voltage	n to Source Voltage			600		
V _{GSS}	Cata ta Sauraa Valtaga	- DC	- DC		±20		
	Gate to Source Voltage	- AC	(f > 1 Hz)	±	30	V	
I _D	Droin Current	- Continuous (T _C = 25 ^o C)		7.4 7.4*		•	
	Drain Current	- Continuous (T _C = 100 ^o C)		4.7	4.7*	A	
I _{DM}	Drain Current	- Pulsed	(Note 1)	22.2	22.2*	А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)			1	mJ		
I _{AR}	Avalanche Current		(Note 1)	1.5		А	
E _{AR}	Repetitive Avalanche Energy		(Note 1)	0.89		mJ	
dv/dt	MOSFET dv/dt			1	V/ns		
	Peak Diode Recovery dv/dt			20			
P _D	Dewer Dissinction	(T _C = 25°C)		89	28	W	
	Power Dissipation	- Derate Above 25°C		0.71	0.22	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to	°C		
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			3	°C		
Drain current	limited by maximum junction tem	perature.					

Thermal Characteristics

Symbol	Parameter	FCP600N60Z	FCPF600N60Z	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	1.4	4.5	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	°C/W

December 2014

	nber	Top Mark	Packag	e Packing Method	Reel Size	Тар	e Width	Qua	ntity
FCP600N60Z FCP600N60Z TO-2		FCP600N60Z	TO-220) Tube	N/A		N/A	50 u	units
		TO-220	F Tube	N/A		N/A	50 units		
Electrica	l Char	acteristics T _c =	25°C unless	s otherwise noted.					
Symbol				Test Conditions		Min.	Тур.	Max.	Unit
Off Charac	teristic	s							
		•	-	V _{GS} = 0 V, I _D = 10 mA,	T. = 25°C	600	_	-	
BV _{DSS}	Drain to Source Breakdown Voltage		$V_{GS} = 0 V, I_D = 10 mA, T_J = 150^{\circ}C$		650	_		V	
∆BV _{DSS}	Breakdown Voltage Temperature Coefficient		$I_D = 10 \text{ mA}, \text{ Referenced to } 25^{\circ}\text{C}$		-	0.67	_	V/ºC	
$/\Delta T_J$					-	0.07	-	V/C	
BV _{DS}	Drain to Source Avalanche Breakdown Voltage		$V_{GS} = 0 V, I_{D} = 7.4 A$		-	700	-	V	
			V _{DS} = 600 V, V _{GS} = 0 V		-	-	1		
DSS	Zero Ga	Zero Gate Voltage Drain Current		$V_{DS} = 480 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$		-	1.32	-	μA
I _{GSS}	Gate to Body Leakage Current			$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$		-	-	±10	uA
On Charac	teristic	s							1
V _{GS(th)}	-	reshold Voltage		V _{GS} = V _{DS} , I _D = 250 μ/	4	2.5	-	3.5	V
R _{DS(on)}	Static Drain to Source On Resistance		stance	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3.7 \text{ A}$		-	0.51	0.6	Ω
9FS	Forward Transconductance		$V_{\rm DS} = 20 \text{ V}, \text{ I}_{\rm D} = 3.7 \text{ A}$		-	6.7	-	S	
	haracte	prietice						I	
C _{iss}	Input Capacitance Output Capacitance					840	1120	pF	
C _{oss}			V _{DS} = 25 V, V _{GS} = 0 V	,	-	630	840	pF	
C _{rss}	-	erse Transfer Capacitance		f = 1 MHz		-	30	45	pF
C _{oss}	Output Capacitance		V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz		-	16.5	-	pF	
C _{oss(eff.)}	Effective Output Capacitance			$V_{DS} = 0 V \text{ to } 480 V, V_{GS} = 0 V$		-	74	-	pF
Q _{g(tot)}		otal Gate Charge at 10V		$V_{DS} = 380 \text{ V}, \text{ I}_{D} = 3.7 \text{ A},$		-	20	26	nC
Q _{gs}	Gate to Source Gate Charge Gate to Drain "Miller" Charge Equivalent Series Resistance		$V_{GS} = 10 V$ (Note 4) f = 1 MHz		-	3.4	-	nC	
Q _{gd}					-	7.5	-	nC	
ESR					- /	2.89	-	Ω	
Switching	Charac	teristics							
t _{d(on)}		Delay Time				_	13	36	ns
t _r	Turn-On Rise Time Turn-Off Delay Time		V_{DD} = 380 V, I _D = 3.7 A, V_{GS} = 10 V, R _G = 4.7 Ω		_	7	24	ns	
t _{d(off)}					-	39	88	ns	
t _f		Turn-Off Fall Time		(Note 4)		-	9	28	ns
		de Characteristic			. ,				1
I _s		m Continuous Drain to		le Forward Current		-	-	7.4	A
I _{SM}		um Pulsed Drain to Source Diode F				-	-	22.2	A
V _{SD}	Drain to	Source Diode Forward Voltage		V _{GS} = 0 V, I _{SD} = 3.7 A		-	-	1.2	V
t _{rr}	Reverse	e Recovery Time		$V_{GS} = 0 V, I_{SD} = 3.7 A,$		-	200	-	ns
	Reverse	Recovery Charge		dl _F /dt = 100 A/μs		-	2.3	-	μC



25°C

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Notes:

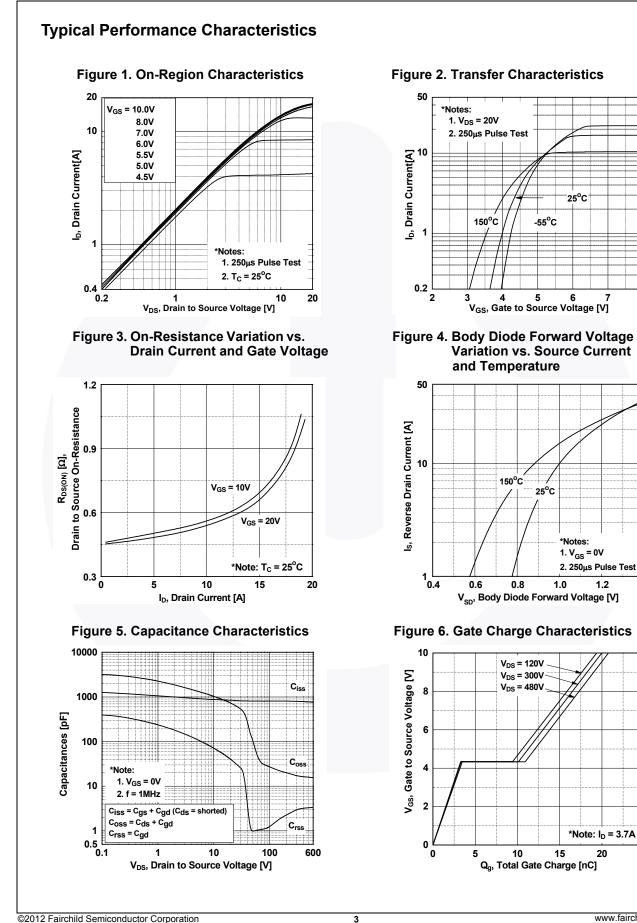
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*Note: I_D = 3.7A

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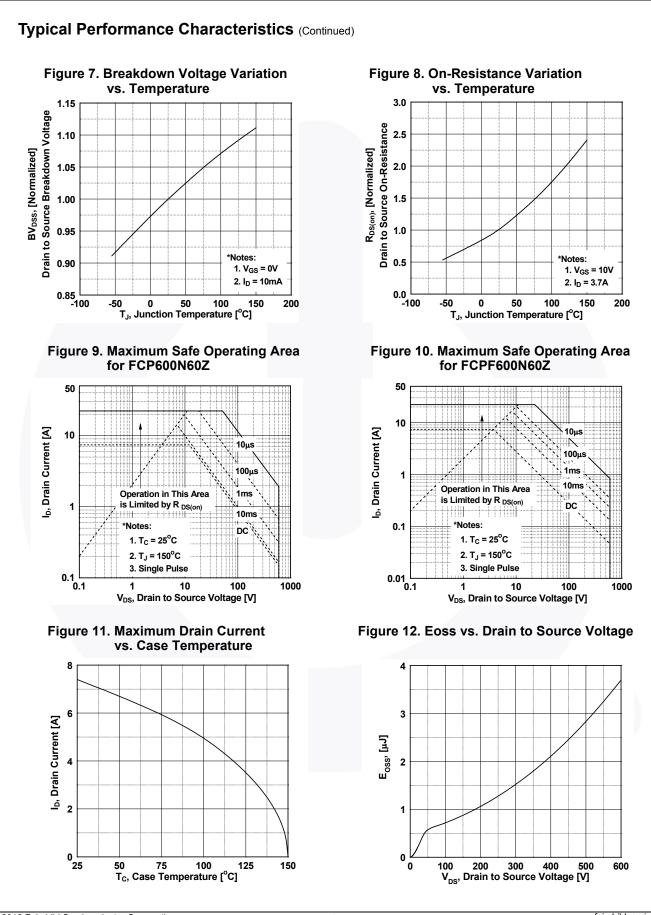
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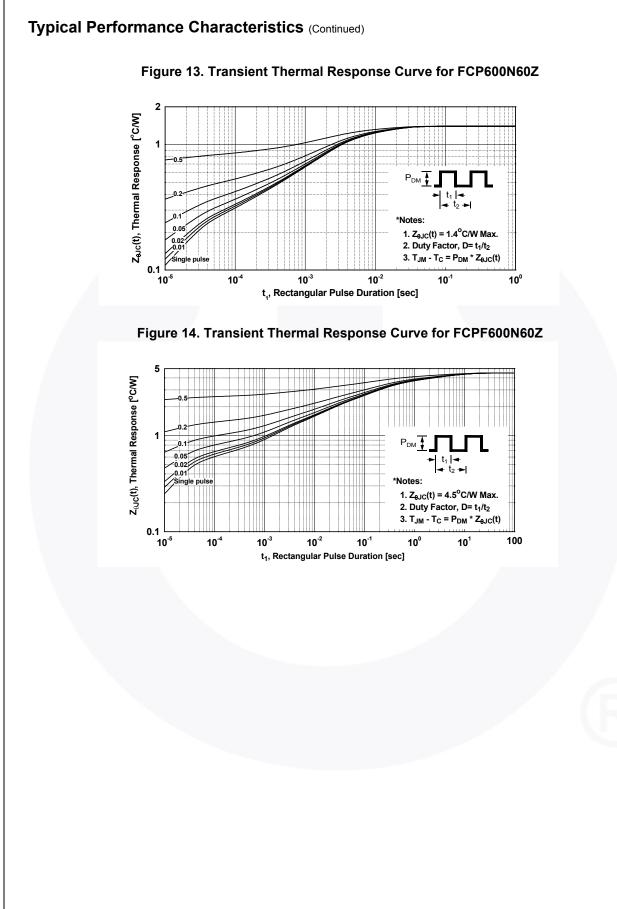


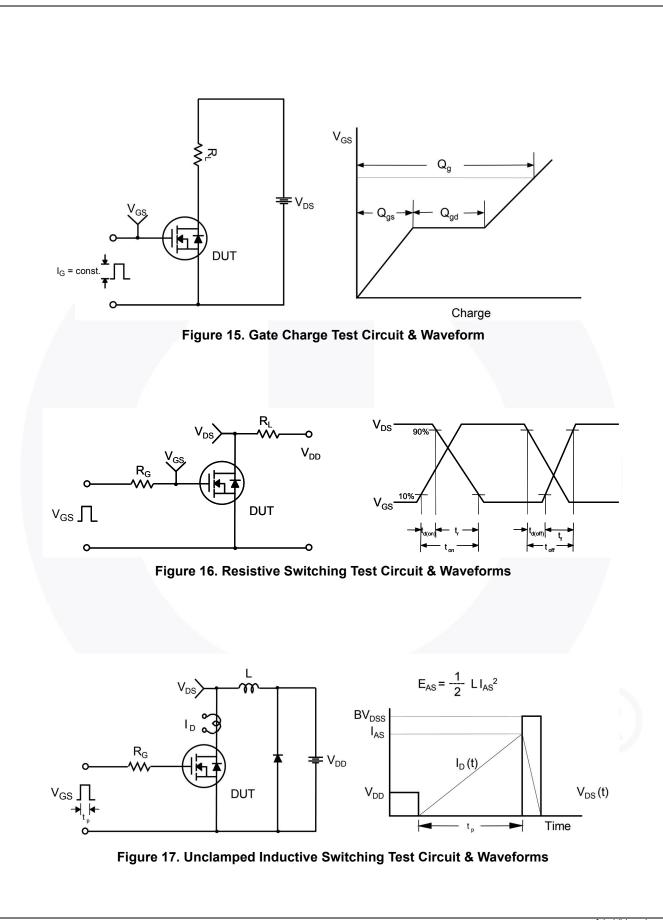
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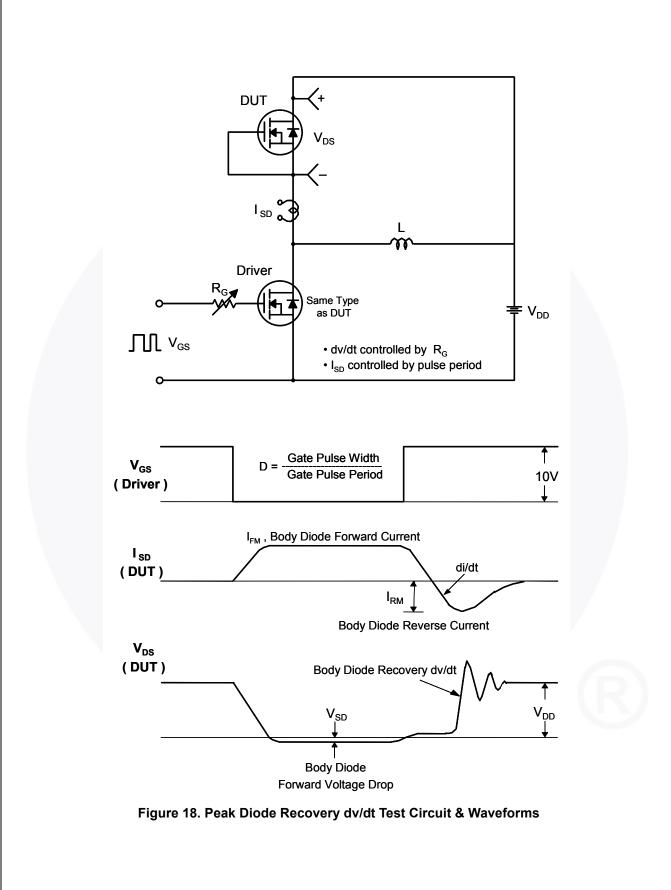


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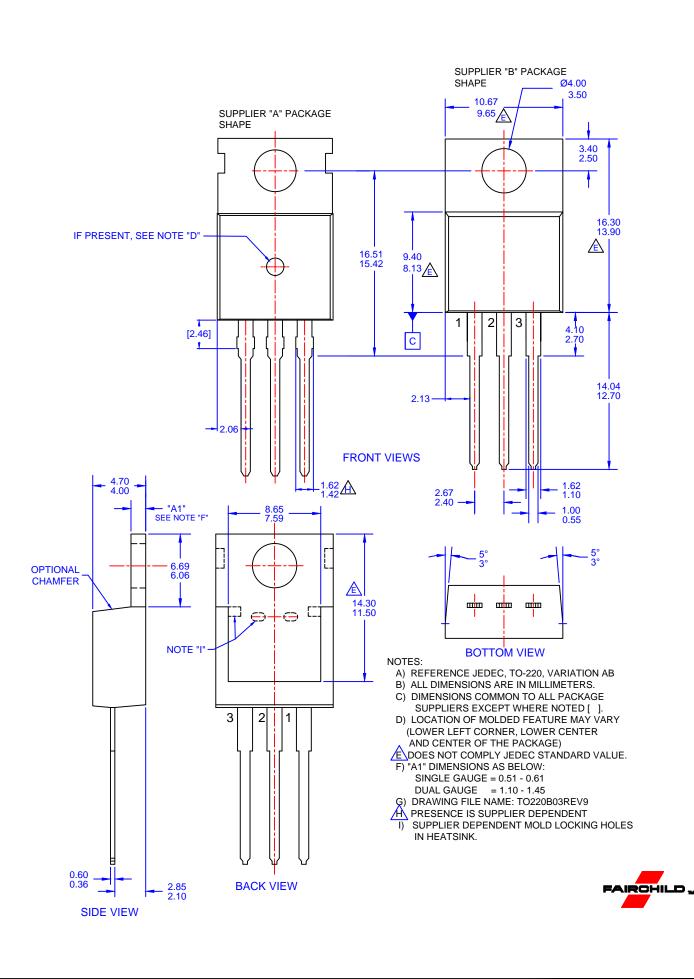


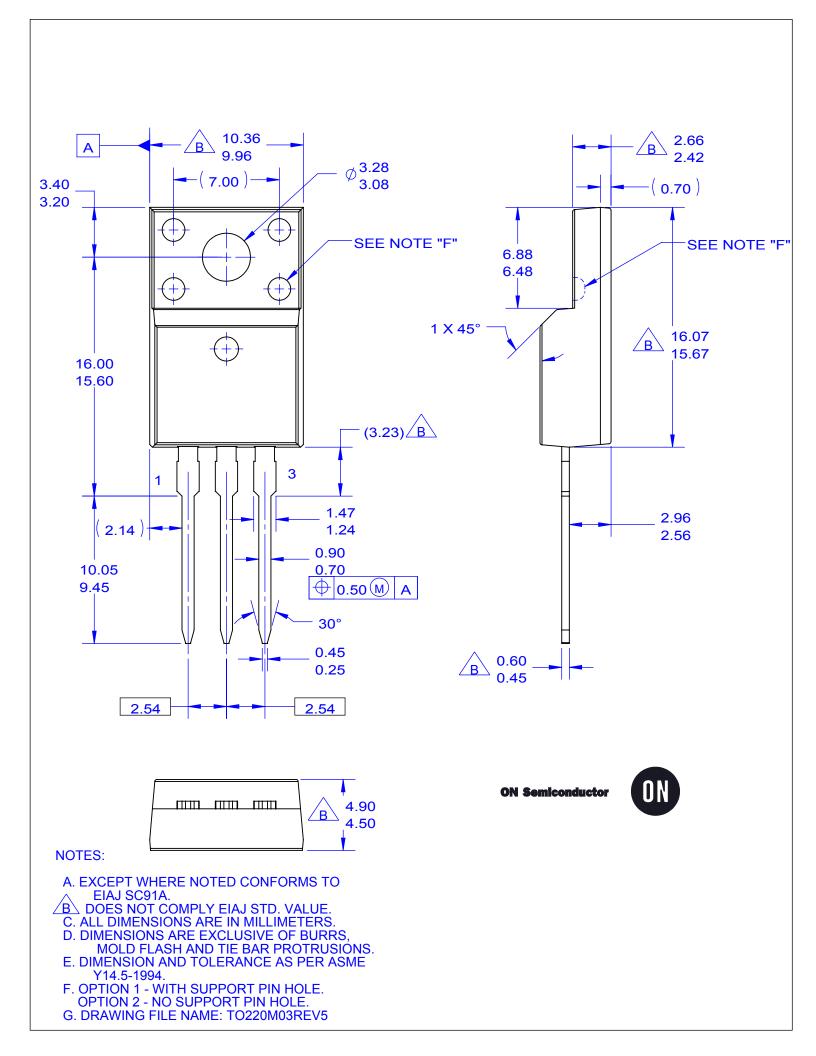


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FCP600N60Z / FCPF600N60Z — N-Channel SuperFET® II MOSFET





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