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November 2013

FCP11N60N / FCPF11N60NT N-Channel SupreMOS[®] MOSFET 600 V, 10.8 A, 299 m Ω

Features

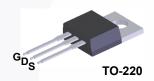
- $R_{DS(on)}$ = 255 $m\Omega$ (Typ.) @ V_{GS} = 10 V, I_D = 5.4 A
- Ultra Low Gate Charge (Typ. Q_q = 27.4 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 130 pF)
- 100% Avalanche Tested
- · RoHS Compliant

Application

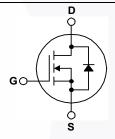
- LCD/LED/PDP TV
- · Lighting
- · Solar Inverter
- AC-DC Power Supply

Description

The SupreMOS® MOSFET is Fairchild Semiconductor's next generation of high voltage super-junction (SJ) technology employing a deep trench filling process that differentiates it from the conventional SJ MOSFETs. This advanced technology and precise process control provides lowest Rsp on-resistance, superior switching performance and ruggedness. SupreMOS MOSFET is suitable for high frequency switching power converter applications such as PFC, server/telecom power, FPD TV power, ATX power, and industrial power applications.







Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		FCP11N60N	FCPF11N60NT	Unit
V _{DSS}	Drain to Source Voltage			6	V	
V _{GSS}	Gate to Source Voltage	Gate to Source Voltage		±	:30	V
	Drain Current	- Continuous (T _C = 25°C))	10.8	10.8*	^
D	Drain Current	- Continuous (T _C = 100°C	C)	6.8	6.8*	Α
I _{DM}	Drain Current	- Pulsed	(Note 1)	32.4	32.4*	Α
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	201.7		mJ
AR	Avalanche Current		(Note 1)	3.7		Α
E _{AR}	Repetitive Avalanche Energy		(Note 1)	0.94		mJ
dv/dt	MOSFET dv/dt			100		V/ns
dv/dt	Peak Diode Recovery	dv/dt	(Note 3)) 20		V/ns
n	$(T_C = 25^{\circ}C)$			94.0	32.1	W
P_{D}	Power Dissipation - Derate Above 25°C			0.75	0.26	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to	o +150	οС	
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C	

^{*}Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FCP11N60N	FCPF11N60NT	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.33	3.9	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	*C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FCP11N60N	FCP11N60N	TO-220	Tube	N/A	N/A	50 units
FCPF11N60NT	FCPF11N60NT	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Chara	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}, T_C = 25^{\circ}\text{C}$	600	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C	-	0.73	-	V/°C
1	Zero Gate Voltage Drain Current	$V_{DS} = 480 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	10	μА
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 480 \text{ V}, V_{GS} = 0 \text{ V}, T_{C} = 125^{\circ}\text{C}$	1	-	100	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu\text{A}$	2.0	-	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 5.4 A	-	0.255	0.299	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 40 \text{ V}, I_{D} = 5.4 \text{ A}$	-	13.5	1	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 400 V V 0 V	-	1130	1505	pF
C _{oss}	Output Capacitance	V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz		45	60	pF
C _{rss}	Reverse Transfer Capacitance			3	5	pF
C _{oss}	Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	- \	25	-	pF
C _{oss(eff.)}	Effective Output Capacitance	$V_{DS} = 0 \text{ V to } 480 \text{ V}, V_{GS} = 0 \text{ V}$	-	130	-	pF
Q _{g(tot)}	Total Gate Charge at 10V	V _{DS} = 380 V, I _D = 5.4 A,	-	27.4	35.6	nC
Q_{gs}	Gate to Source Gate Charge	V _{GS} = 10 V	-	4.9	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	(Note 4)	-	8.8	-	nC
ESR	Equivalent Series Resistance (G-S)	f = 1 MHz	-	2.0	-	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time			-/	13.6	37.2	ns
t _r	Turn-On Rise Time	$V_{DD} = 380 \text{ V}, I_D = 5.4 \text{ A},$		-	9.1	28.2	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_G = 4.7 Ω		-	42.0	94.0	ns
t _f	Turn-Off Fall Time		(Note 4)	-	10.0	30.0	ns

Drain-Source Diode Characteristics

Is	Maximum Continuous Drain to Source Diode	Maximum Continuous Drain to Source Diode Forward Current		-	10.8	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	32.4	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 5.4 A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 5.4 A,	-	268	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 A/\mu s$	-	3.1	-	μС

Notes

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. I_{AS} = 3.7 A, R_G = 25 Ω , starting T_J = 25°C.
- 3. $I_{SD} \le 10.8$ A, di/dt ≤ 200 A/ μ s, V_{DD} = 380 V, starting T_J = 25°C.
- 4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

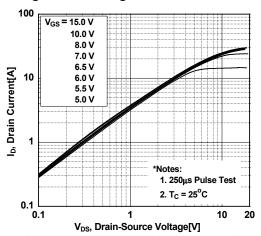


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

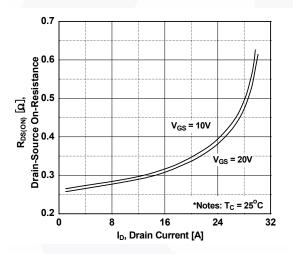


Figure 5. Capacitance Characteristics

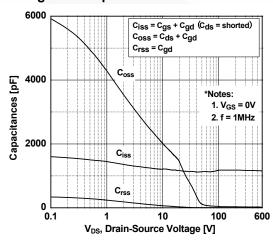


Figure 2. Transfer Characteristics

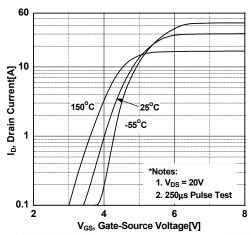


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

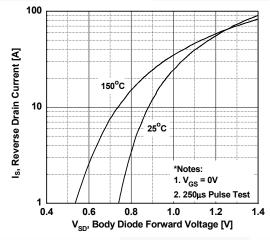
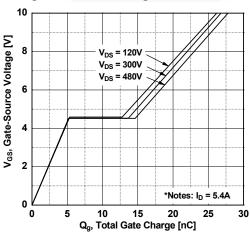
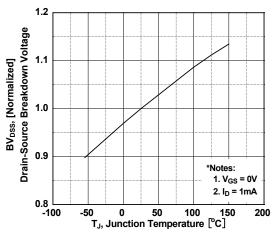


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature



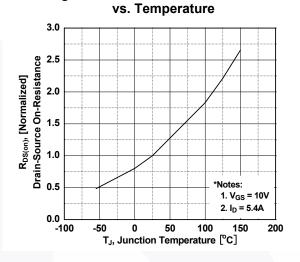


Figure 8. On-Resistance Variation

Figure 9. Maximum Safe Operating Area for FCP11N60N

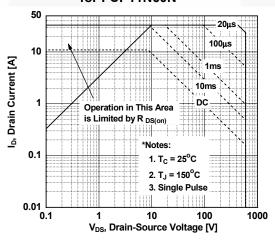


Figure 10. Maximum Safe Operating Area for FCPF11N60NT

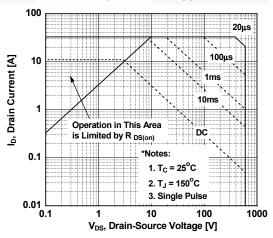
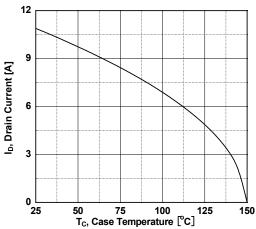


Figure 11. Maximum Drain Current vs. Case Temperature



Typical Performance Characteristics (Continued)

Figure 12. Transient Thermal Response Curve for FCP11N60N

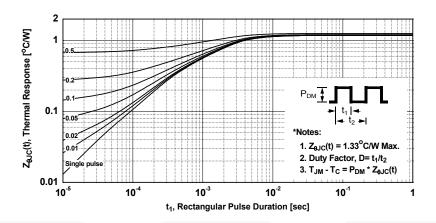
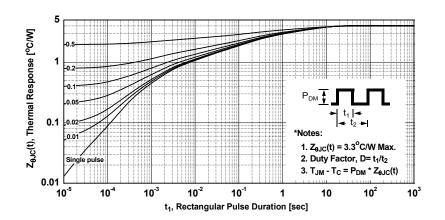


Figure 13. Transient Thermal Response Curve for FCPF11N60NT



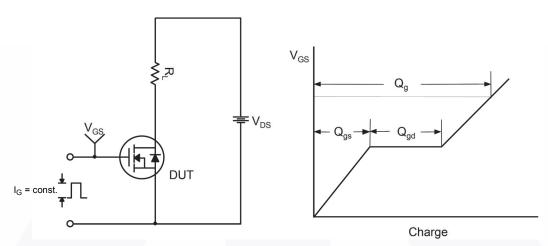


Figure 14. Gate Charge Test Circuit & Waveform

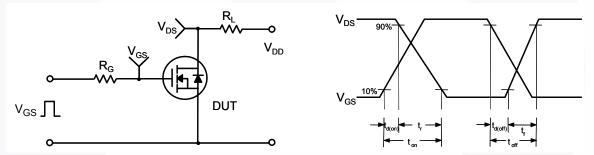


Figure 15. Resistive Switching Test Circuit & Waveforms

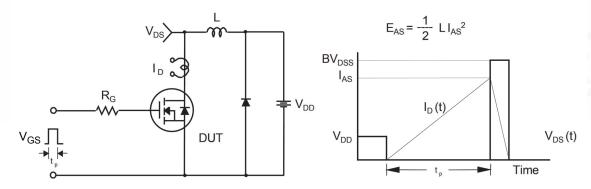


Figure 16. Unclamped Inductive Switching Test Circuit & Waveforms

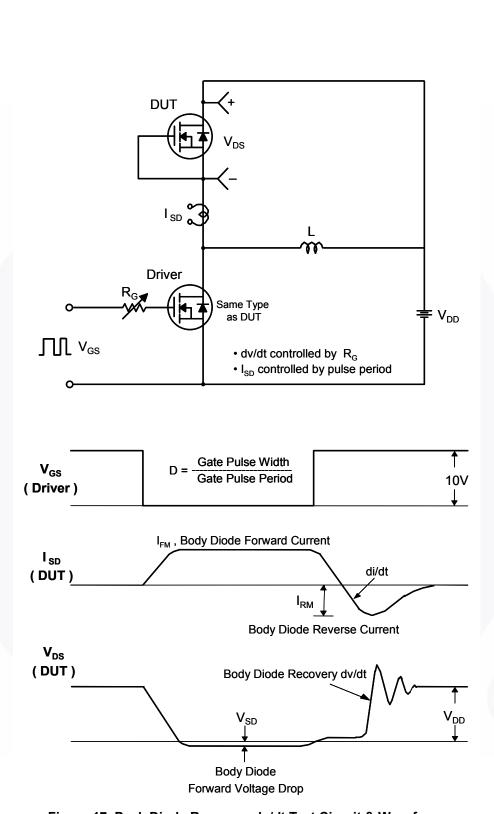


Figure 17. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

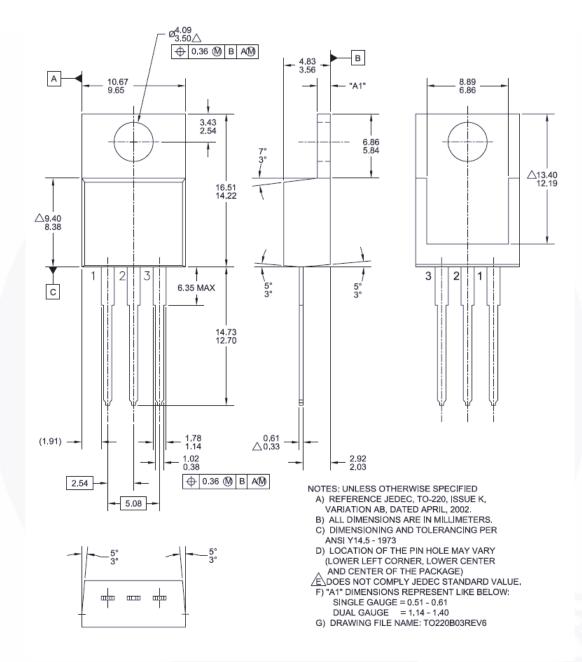


Figure 18. TO-220, Molded, 3-Lead, Jedec Variation AB

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Mechanical Dimensions

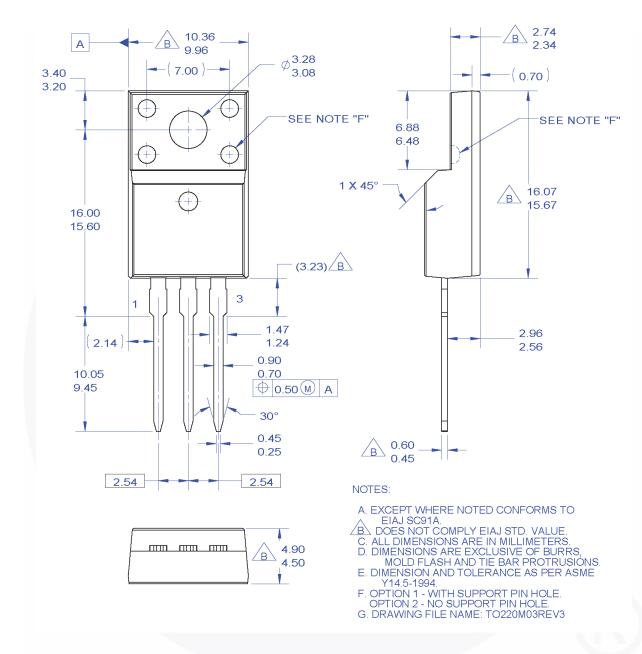


Figure 19. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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