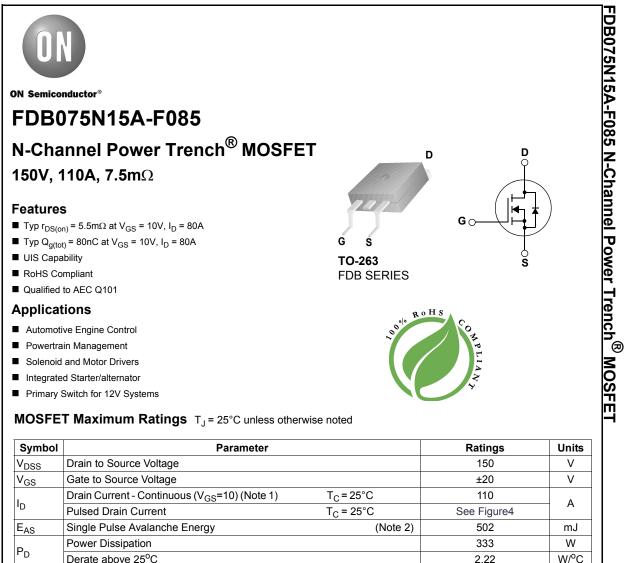
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Derate above 25°C 2.22 T_J, T_{STG} Operating and Storage Temperature -55 to + 175 $R_{\theta JC}$ 0.45 Thermal Resistance Junction to Case Maximum Thermal Resistance Junction to Ambient (Note 3) 43 $R_{\theta JA}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB075N15A	FDB075N15A-F085	D2-PAK(TO-263)	330mm	24mm	800 units

Notes:

1: Current is limited by bondwire configuration. 2: Starting $T_J = 25^{\circ}$ C, L = 0.24mH, I_{AS} = 64A, V_{DD} = 100V during inductor charging and V_{DD} = 0V during time in avalanche 3: R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design. The maximum rating presented here is based on mounting on a 1 in² pad of 2oz copper.

°C °C/W

°C/W

Symbol	Parameter	Test Conditions		Min	Тур	Мах	Units
Off Cha	racteristics						
B _{VDSS}	Drain to Source Breakdown Voltage	I _D = 250μA, V _{GS} = 0V		150	-	-	V
	Drain to Source Leakage Current	V _{DS} =150V,		-	-	1	μA
IDSS	Drain to Source Leakage Current	$V_{GS} = 0V$	$T_{\rm J} = 175^{\rm o}C({\rm Note}\;4)$	-	-	1	mA
GSS	Gate to Source Leakage Current	$V_{GS} = \pm 20V$		-	-	±100	nA
[•] DS(on) Dynam i	Drain to Source On Resistance	I _D = 80A, V _{GS} = 10V	$T_J = 175^{\circ}C(Note 4)$	-	5.5 14.2	7.5 20	mΩ mΩ
C _{iss}	Input Capacitance	V _{DS} = 75V, V _{GS} = 0V, f = 1MHz		-	5595	-	pF
C _{oss}	Output Capacitance			-	513	-	pF
C _{rss}	Reverse Transfer Capacitance			-	16	-	pF
₹ _g	Gate Resistance	f = 1MHz		-	2.4	-	Ω
Q _{g(ToT)}	Total Gate Charge at 10V	V _{GS} = 0 to 10	V V _{DD} = 75V	-	80	95	nC
	Threshold Gate Charge	V_{GS} = 0 to 2V	/ I _D = 80A	-	11	13	nC
ວ _{g(th)}							
Q _{g(th)} Q _{gs}	Gate to Source Gate Charge			-	26.5	-	nC

er Trench[®] MO:

Switching Characteristics

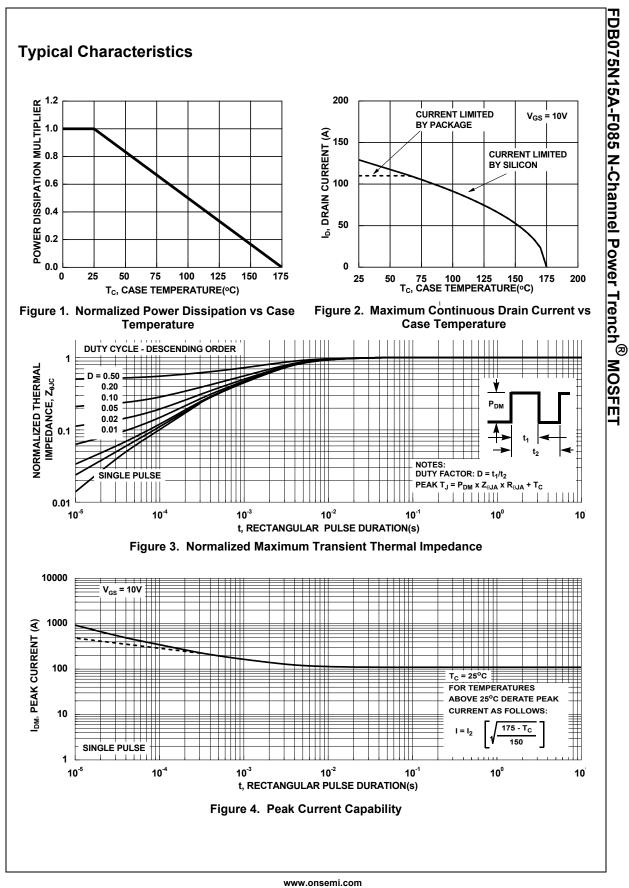
t _{on}	Turn-On Time		-	-	100	ns
t _{d(on)}	Turn-On Delay Time		-	33	-	ns
t _r	Rise Time	V _{DD} = 75V, I _D = 80A,	-	46	-	ns
t _{d(off)}	Turn-Off Delay Time	$V_{DD} = 75V, I_D = 80A,$ $V_{GS} = 10V, R_{GEN} = 6\Omega$	-	76	-	ns
t _f	Fall Time		-	25	-	ns
t _{off}	Turn-Off Time		-	-	138	ns

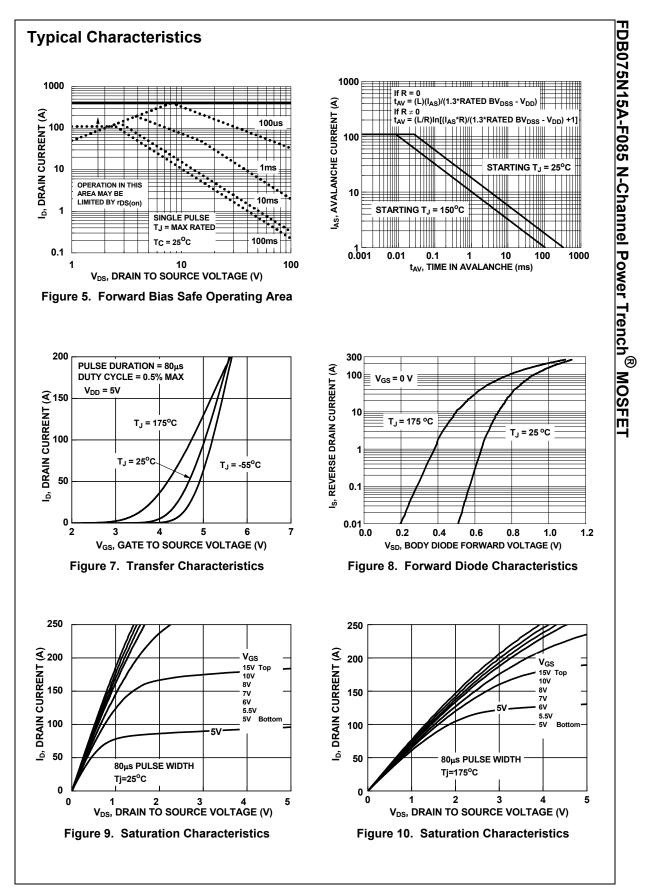
Drain-Source Diode Characteristics

V _{SD}	Source to Drain Diode Voltade	I _{SD} = 80A, V _{GS} = 0V	-	-	1.25	V
		I _{SD} = 40A, V _{GS} = 0V	-	-	1.2	V
T _{rr}	Reverse Recovery Time	I _F = 80A, dI _{SD} /dt = 100A/μs,	-	118	132	ns
Q _{rr}	Reverse Recovery Charge	V _{DD} =120V	-	341	494	nC

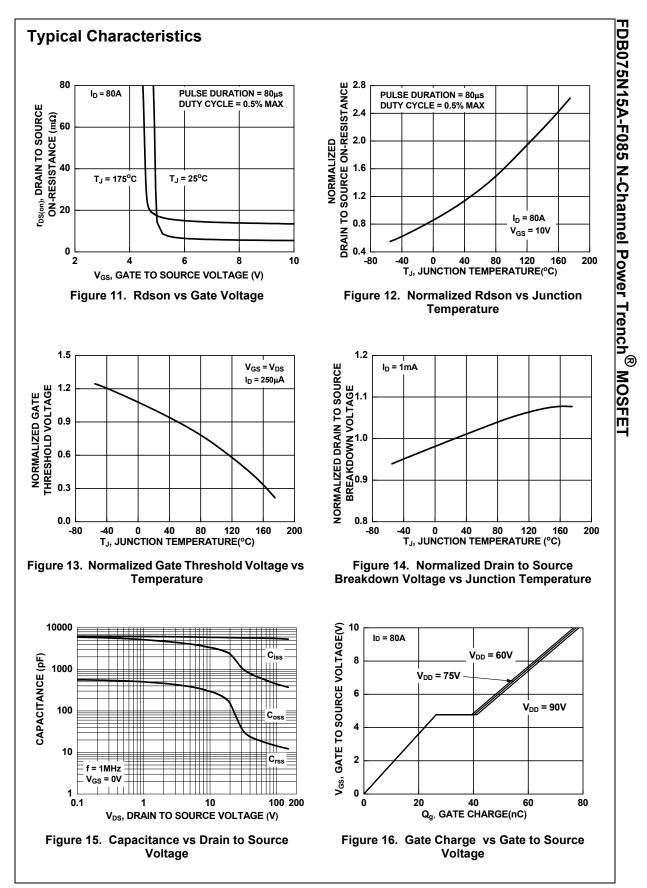
Notes:

4: The maximum value is specified by design at T_J = 175°C. Product is not tested to this condition in production.





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