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on semiconductor® FDB8445-F085

N-Channel PowerTrench[®] MOSFET

40V, 70A, 9mΩ

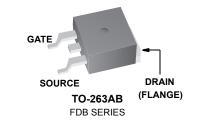
Features

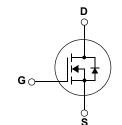
- Typ $r_{DS(on)}$ = 6.8m Ω at V_{GS} = 10V, I_D = 70A
- Typ Q_{g(10)} = 44nC at V_{GS} = 10V
- Low Miller Charge
- Low Q_{rr} Body Diode
- UIS Capability (Single Pulse/ Repetitive Pulse)
- Qualified to AEC Q101
- RoHS Compliant

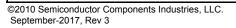
Applications

- Automotive Engine Control
- Powertrain Management
- Solenoid and Motor Drivers
- Electronic Transmission
- Distributed Power Architecture and VRMs
- Primary Switch for 12V Systems









Symbol	Parameter		Ratings	Units
V _{DSS}	Drain to Source Voltage		40	V
V _{GS}	Gate to Source Voltage		±20	V
	Drain Current Continuous (V _{GS} = 10V)	(Note 1)	70	А
D	Pulsed		Figure 4	
E _{AS}	Single Pulse Avalanche Energy	(Note 2)	102	mJ
	Power Dissipation		92	W
P _D	Derate above 25°C		0.6	W/ºC
T _J , T _{STG}	Operating and Storage Temperature		-55 to +175	°C

Thermal Characteristics

$R_{ ext{ heta}JC}$	Maximum Thermal Resistance, Junction to Case	1.63	°C/W
$R_{ hetaJA}$	Maximum Thermal Resistance, Junction to Ambient TO-263, lin ² copper pad area	43	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB8445	FDB8445-F085	TO-263AB	330mm	24mm	800 units

Electrical Characteristics T_J = 25°C unless otherwise noted

Symbol Parameter Test Conditions Min Typ Max U
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Off Characteristics

B _{VDSS}	Drain to Source Breakdown Voltage	I _D = 250μA, V _G	_S = 0V	40	-	-	V
1	Zero Gate Voltage Drain Current	V _{DS} = 32V		-	-	1	μA
DSS		$V_{GS} = 0V$	T _J =150°C	-	-	250	μA
I _{GSS}	Gate to Source Leakage Current	V_{GS} = $\pm 20V$		-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2	2.5	4	V
		I _D = 70A, V _{GS} = 10V	-	6.8	9	
r _{DS(on)}	Drain to Source On Resistance	I _D = 70A, V _{GS} = 10V, T _J = 175°C	-	13	17.2	mΩ

Dynamic Characteristics

C _{iss}	Input Capacitance		- 0)/	-	2860	3805	pF
C _{oss}	Output Capacitance	— V _{DS} = 25V, V _{GS} — f = 1MHz	= 0V,	-	295	395	pF
C _{rss}	Reverse Transfer Capacitance			-	180	270	pF
R _G	Gate Resistance	f = 1MHz		-	1.95	-	Ω
Q _{g(TOT)}	Total Gate Charge at 10V	V _{GS} = 0 to 10V		-	44	62	nC
Q _{g(TH)}	Threshold Gate Charge	V_{GS} = 0 to 2V	V _{DD} =20V,	-	2.9	4.1	nC
Q _{gs}	Gate to Source Gate Charge		I _D = 70A,	-	11	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau			-	8.2	-	nC
Q _{gd}	Gate to Drain "Miller" Charge			-	11	-	nC

naracteristics					
rn-On Time	V _{DD} = 20V, I _D = 70A	-	-	45	ns
rn-On Delay Time		-	10	-	ns
rn-On Rise Time		-	19	-	ns
rn-Off Delay Time	V _{GS} = 10V, R _{GS} = 5Ω	-	36	-	ns
rn-Off Fall Time		-	16	-	ns
rn-Off Time		-	-	81	ns
	rn-On Time rn-On Delay Time rn-On Rise Time rn-Off Delay Time rn-Off Fall Time rn-Off Time	$ \begin{array}{c} \text{rn-On Delay Time} \\ \text{rn-On Rise Time} \\ \text{rn-Off Delay Time} \\ \text{rn-Off Fall Time} \\ \end{array} \\ \begin{array}{c} V_{\text{DD}} = 20V, \ I_{\text{D}} = 70A \\ V_{\text{GS}} = 10V, \ R_{\text{GS}} = 5\Omega \\ \end{array} $	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	$ \begin{array}{c c} rn - On \ Delay \ Time & & - & 10 \\ rn - On \ Rise \ Time & & V_{DD} = 20V, \ I_D = 70A & - & 19 \\ rn - Off \ Delay \ Time & & V_{GS} = 10V, \ R_{GS} = 5\Omega & - & 36 \\ rn - Off \ Fall \ Time & & - & 16 \\ \end{array} $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

I_F = 70A, di/dt = 100A/μs

I_F = 70A, di/dt = 100A/μs

t_{rr} Q_{rr}

Notes: 1: Maximum wire current carrying capacity is 70A. 2: Starting $T_J = 25^{\circ}C$, L = 65uH, $I_{AS} = 56A$.

Reverse Recovery Time

Reverse Recovery Charge

59

77

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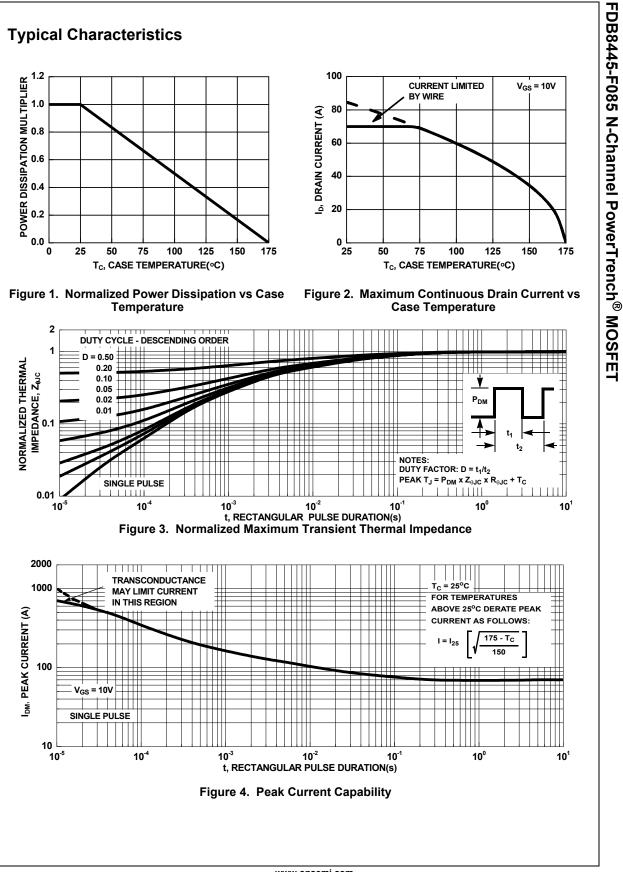
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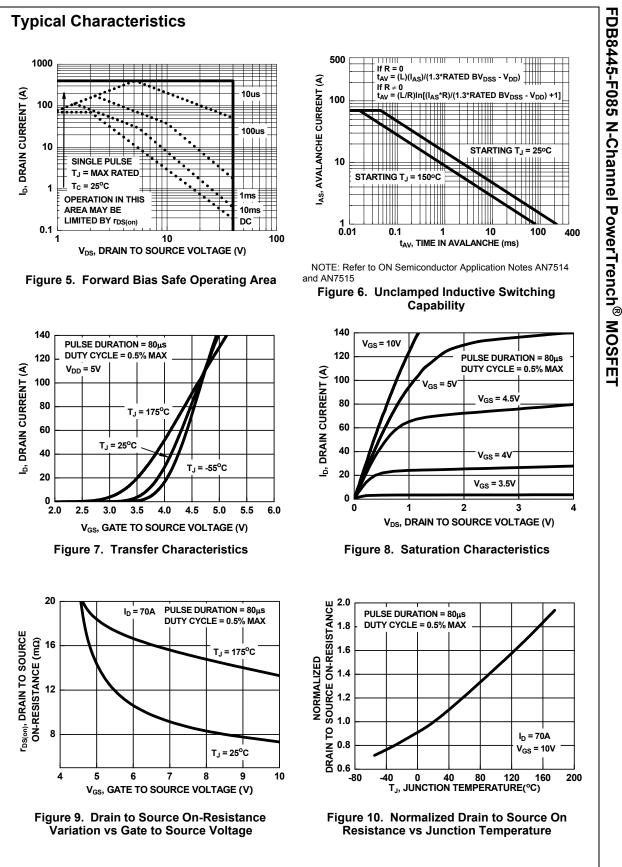
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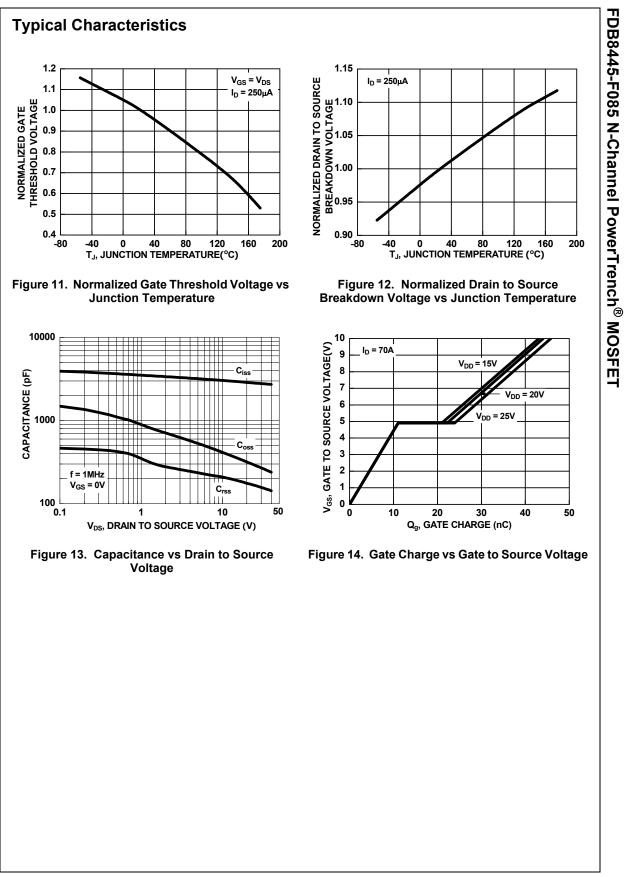
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