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ON Semiconductor®

FDC638APZ P-Channel 2.5V PowerTrench[®] Specified MOSFET

-20V, -4.5A, 43m Ω Features

- Max $r_{DS(on)}$ = 43m Ω at V_{GS} = -4.5V, I_D = -4.5A
- Max $r_{DS(on)}$ = 68m Ω at V_{GS} = -2.5V, I_D = -3.8A
- Low gate charge (8nC typical).
- High performance trench technology for extremely low r_{DS(on)}.
- SuperSOTTM –6 package:small footprint (72% smaller than
- standard SO–8) low profile (1mm thick). RoHS Compliant



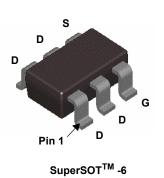
General Description

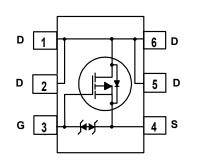
This P-Channel 2.5V specified MOSFET is produced using ON Semiconductor's advanced PowerTrench[®] process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance

These devices are well suited for battery power applications:load switching and power management,battery charging circuits,and DC/DC conversion.

Application

■ DC - DC Conversion





MOSFET Maximum Ratings TA= 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units		
V _{DS}	Drain to Source Voltage		-20	V	
V _{GS}	Gate to Source Voltage		±12	V	
ID	Drain Current -Continuous	(Note 1a)	-4.5	•	
	-Pulsed		-20	A	
P _D	Power Dissipation	(Note 1a)	1.6	- W	
	Power Dissipation	(Note 1b)	0.8		
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C	

Thermal Characteristics

$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	78	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	(Note 1b)	156	0/11

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
.638Z	FDC638APZ	7"	8mm	3000 units

Symbol	Parameter	Test Conditions	Min	Тур	Мах	Units	
Off Chara	cteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = -250μA, V _{GS} = 0V	-20			V	
ΔBV _{DSS} ΔTJ	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu A$, referenced to 25°C		-9.4		mV/°C	
DSS	Zero Gate Voltage Drain Current	$V_{DS} = -16V,$ $V_{GS} = 0V$ $T_J = 55^{\circ}C$			-1 -10	μA	
GSS	Gate to Source Leakage Current	$V_{GS} = 0V T_J = 55^{\circ}C V_{GS} = \pm 12V, V_{DS} = 0V$			±10	μA	
On Chara	cteristics						
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = -250 \mu A$	-0.4	-0.8	-1.5	V	
$\frac{\Delta V_{GS(th)}}{\Delta T_{.l}}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = -250\mu$ A, referenced to 25°C		2.9		mV/°C	
r _{DS(on)}		$V_{GS} = -4.5V, I_D = -4.5A$		37	43		
	Static Drain to Source On Resistance	$V_{GS} = -2.5V, I_D = -3.8A$		52	68	mΩ	
		$V_{GS} = -4.5V, I_D = -4.5A, T_J = 125^{\circ}C$		50	72	-	
D(on)	On-State Drain Current	$V_{GS} = -10V, V_{DS} = -4.5A$	-20			Α	
9FS	Forward Transconductance	$V_{DS} = -10V$, $I_{D} = -4.5A$		18		S	
Ciss Coss Crss	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{DS} = –10V, V _{GS} = 0V, f = 1MHz		750 155 130	1000 210 195	pF pF pF	
	g Characteristics (Note 2)			100	100	P	
	Turn-On Delay Time			6	12	ns	
d(on) r	Rise Time	V _{DD} = -5V, I _D = -4.5A		20	31	ns	
	Turn-Off Delay Time	V_{GS} = -4.5V, R_{GEN} = 6 Ω		48	77	ns	
d(off)	Fall Time	-		47	72	ns	
Q _{g(TOT)}	Total Gate Charge	$V_{GS} = 0V \text{ to } -4.5V$ $V_{DD} = -5V$		8	12	nC	
<u>∽g(101)</u> Ω _{gs}	Gate to Source Gate Charge	$V_{DD} = -3.0$ $I_{D} = -4.5A$		2	.=	nC	
∽ <u>gs</u> Q _{qd}	Gate to Drain "Miller" Charge			2		nC	
0	urce Diode Characteristics						
	Maximum Continuous Drain-Source Diod	de Forward Current			-1.3	А	
s V _{SD}	Source to Drain Diode Forward Voltage			-0.8	-1.2	V	
	Reverse Recovery Time	$V_{GS} = 0V, v_S = -1.3A$ (Note 2) $I_F = -4.5A, di/dt = 100A/\mu s$		24	36	ns	
						nC	
t _{rr} Q _{rr} Notes:	Reverse Recovery Charge	$I_F = -4.5A$, di/dt = 100A/µs where the case thermal reference is defined as the		13	20	n	

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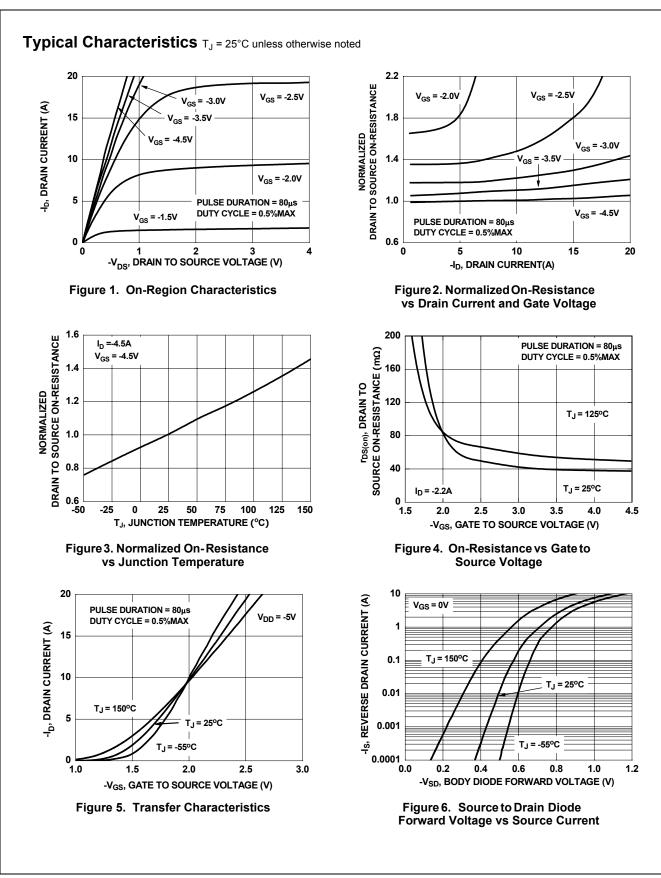
2: Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.

a 1 in² pad of 2 oz copper on FR-4 board.

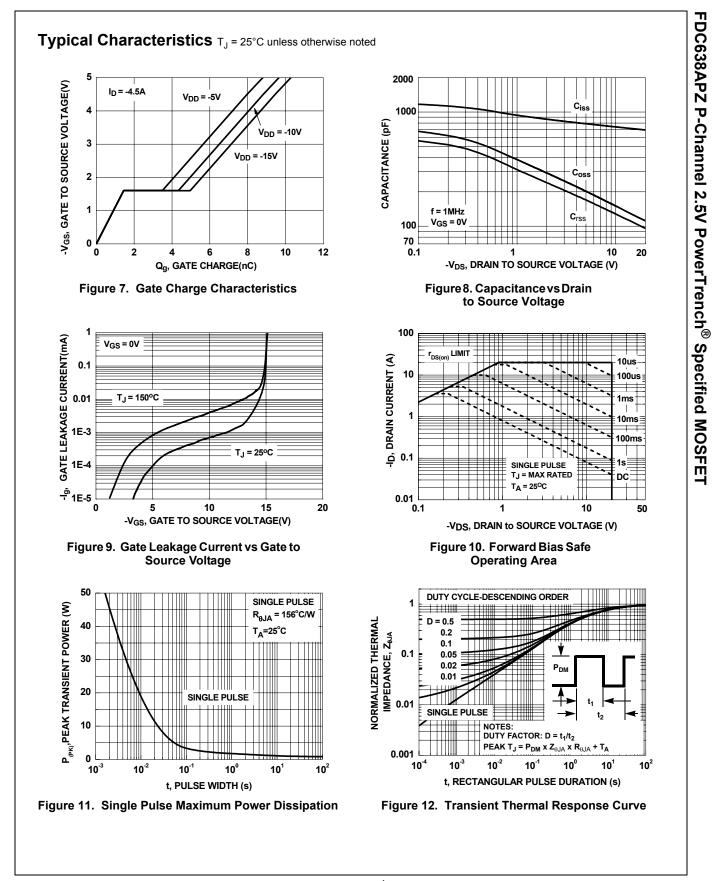


minimum pad of 2 oz copper.

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