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FDD5810-F085

N-Channel Logic Level Trench®MOSFET 60V, 36A, 27m Ω

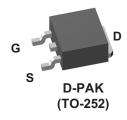
Features

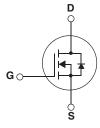
- $R_{DS(ON)} = 22m\Omega$ (Typ.), $V_{GS} = 5V$, $I_D = 29A$
- $Q_{q(5)} = 13nC$ (Typ.), $V_{GS} = 5V$
- Low Miller Charge
- Low Q_{rr} Body Diode
- UIS Capability (Single Pulse / Repetitive Pulse)
- Qualified to AEC Q101
- RoHS Compliant

Applications

- Motor / Body Load Control
- ABS Systems
- Powertrain Management
- Injection System
- DC-DC converters and Off-line UPS
- Distributed Power Architecture and VRMs
- Primary Switch for 12V and 24V systems







Absolute Maximum Ratings T_{C} = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{DSS}	Drain to Source Voltage	60	V
V _{GS}	Gate to Source Voltage	±20	V
	Drain Current Continuous (V _{GS} = 10V)	37	Α
1	Drain Current Continuous (V _{GS} = 5V)	33	Α
ID	Continuous ($T_A = 25^{\circ}$ C, $V_{GS} = 10$ V, with $R_{\theta JA} = 52^{\circ}$ C/W)	7.4	Α
	Pulsed	Figure 4	Α
E _{AS}	Single Pulse Avalanche Energy (Note 1)	45	mJ
В	Power Dissipation	72	W
P_{D}	Derate above 25°C	0.48	W/°C
T _J , T _{STG}	Operating and Storage Temperature	-55 to 175	°C

Thermal Characteristics

$R_{ heta JC}$	Maximum Thermal resistance Junction to Case TO-252	2.1	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient TO-252, 1in ² copper pad area	52	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD5810	FDD5810-F085	TO-252AA	330mm	16mm	2500 units

Electrical Characteristics $T_J = 25$ °C unless otherwise noted

Symbol	Parameter	Test Con	ditions	Min	Тур	Max	Units
Off Characteristics							
B _{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS}$	= 0V	60	-	-	V
1	Izero Gate Voltage Drain Current	V _{DS} = 48V		-	-	1	^
DSS		$V_{GS} = 0V$	$T_{\rm C} = 150^{\rm o}{\rm C}$	-	-	250	μΑ
I_{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20V$		-	-	±100	nA

On Characteristics

V _{GS(TH)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	1	1.6	2	V
R _{DS(ON)}	Drain to Source On Resistance	$I_D = 32A, V_{GS} = 10V$	-	18	22	
		$I_D = 29A, V_{GS} = 5V$	-	22	27	mΩ
		$I_D = 32A, V_{GS} = 10V,$ $T_{.1} = 175^{\circ}C$	-	43	53	11122

Dynamic Characteristics

C _{iss}	Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz	-	1420	1890	pF
C _{oss}	Output Capacitance		-	150	200	pF
C _{rss}	Reverse Transfer Capacitance		-	65	100	pF
R _G	Gate Resistance	f = 1MHz	-	3.5	-	Ω
Q_g	Total Gate Charge at 10V	V _{GS} = 0V to 10V	-	24	34	nC
Q_g	Total Gate Charge at 5V	$V_{GS} = 0V \text{ to } 5V$	-	13	18	nC
$Q_{g(th)}$	Threshold Gate Charge	$V_{GS} = 0V \text{ to } 1V$ $V_{DD} = 30V$ $I_{D} = 35A$	-	1.3	-	nC
Q_{gs}	Gate to Source Gate Charge	ID = 33A	-	4.0	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau		-	2.7	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	5.0	-	nC

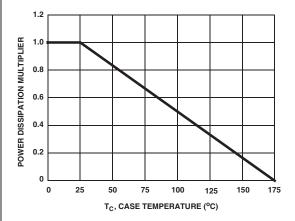
Switchi	Switching Characteristics								
t _{on}	Turn-On Time		-	-	130	ns			
t _{d(on)}	Turn-On Delay Time		-	12	-	ns			
t _r	Rise Time	$V_{DD} = 30V, I_{D} = 35A$	-	75	-	ns			
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 5V, R_{GS} = 11\Omega$	-	26	-	ns			
t _f	Fall Time		-	34	-	ns			
t_{off}	Turn-Off Time		-	-	90	ns			

Drain-Source Diode Characteristics

V _{SD}	ISource to Drain Dioge Voltage	I _{SD} = 32A	ı	-	1.25	V
		I _{SD} = 16A	-	-	1.0	V
t _{rr}	Reverse Recovery Time	I _F = 35A, di/dt = 100A/μs	-	-	39	ns
Q_{rr}	Reverse Recovery Charge	$I_F = 35A$, di/dt = 100A/ μ s	-	-	35	nC

Notes: 1: Starting $T_J = 25^{\circ}C$, $L = 110\mu H$, $I_{AS} = 28A$, $V_{DD} = 54V$, $V_{GS} = 10V$.

Typical Characteristics $T_J = 25$ °C unless otherwise noted



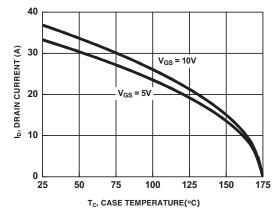


Figure 1. Normalized Power Dissipation vs Case Temperature

Figure 2. Maximum Continuous Drain Current vs Case Temperature

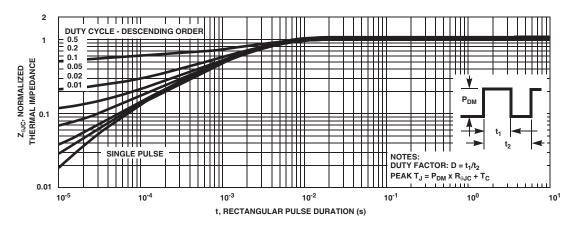


Figure 3. Normalized Maximum Transient Thermal Impedance

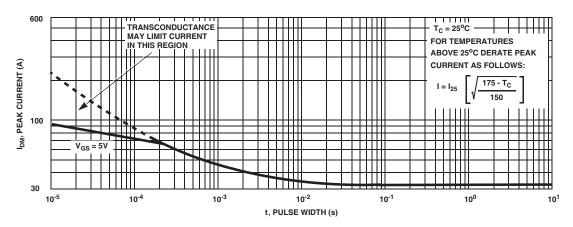


Figure 4. Peak Current Capability

Typical Characteristics $T_J = 25^{\circ}C$ unless otherwise noted

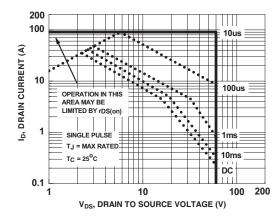
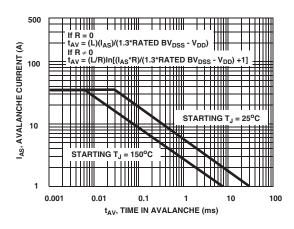


Figure 5. Forward Bias Safe Operating Area



NOTE: Refer to ON Semiconductor Application Notes AN7514 and AN7515

Figure 6. Unclamped Inductive Switching

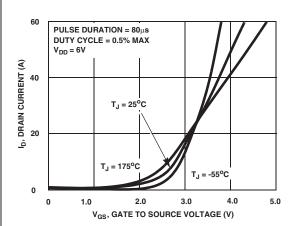


Figure 7. Transfer Characteristics

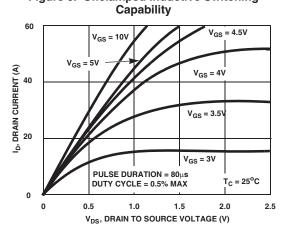


Figure 8. Saturation Characteristics

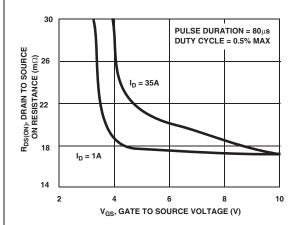


Figure 9. Drain to Source On Resistance vs Gate Voltage and Drain Current

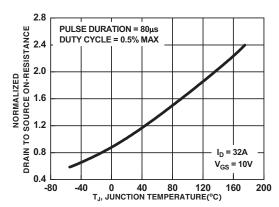


Figure 10. Normalized Drain to Source On Resistance vs Junction Temperature

Typical Characteristics $T_J = 25^{\circ}C$ unless otherwise noted

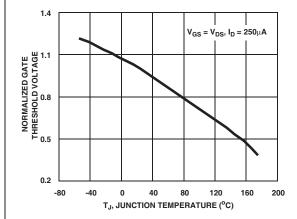


Figure 11. Normalized Gate Threshold Voltage vs Junction Temperature

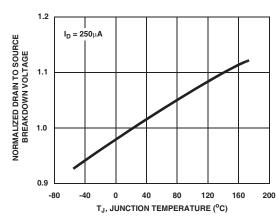


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

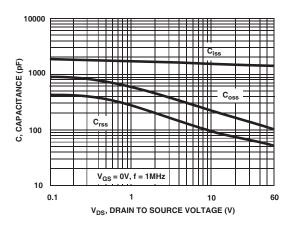


Figure 13. Capacitance vs Drain to Source Voltage

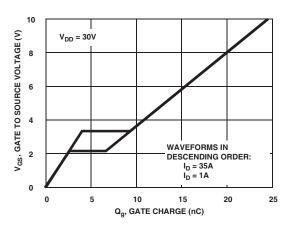


Figure 14. Gate Charge Waveforms for Constant Gate Current

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