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August 2014

FDFS6N754 Integrated N-Channel PowerTrench[®] MOSFET and Schottky Diode

Integrated N-Channel PowerTrench[®] MOSFET and Schottky Diode 30V, 4A, 56m Ω

Features

• Max $r_{DS(on)} = 56m\Omega$ at $V_{GS} = 0V$, $I_D = 4A$

Max $r_{DS(on)} = 75m\Omega$ at $V_{GS} = 4.5V$, $I_D = 3.5A$

■ V_F < 0.45V @ 2A

V_F < 0.28V @ 100mA

- Schottky and MOSFET incorporated into single power surface mount SO-8 package
- Electrically independent Schottky and MOSFET pinout for design flexibility
- Low Gate Charge (Qg = 4nC)
- Low Miller Charge



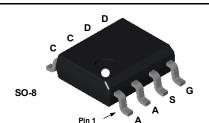
General Description

The FDFS6N754 combines the exceptional performance of Fairchild's PowerTrench MOSFET technology with a very low forward voltage drop Schottky barrier rectifier in an SO-8 package.

This device is designed specifically as a single package solution for DC to DC converters. It features a fast switching, low gate charge MOSFET with very low on-state resistance. The independently connected Schottky diode allows its use in a variety of DC/DC converter topologies.

Applications

■ DC/DC converters



A 1 + B C A 2 + 7 C S 3 + 6 D G 4 + 5 D

MOSFET Maximum Ratings T _A = 25	5°C unless otherwise noted
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Symbol	Parameter	Ratings	Units
V _{DS}	Drain to Source Voltage	30	V
V _{GS}	Gate to Source Voltage	±20	V
1	Drain Current -Continuous (Note	e 1a) 4	Α
D -Pulsed	20	A	
Б	Power Dissipation for Dual Operation	2	w
P _D	Power Dissipation for Single Operation (Note	1a) 1.6	vv
V _{RRM}	Schottky Repetitive Peak Reverse Voltage	30	V
lo	Schottky Average Forward Current (Note	e 1a) 2	А
T _J , T _{STG}	Operating and Storage Temperature	-55 to 150	°C

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	78	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	40	°C/W

Package Marking and Ordering Information

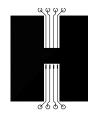
ſ	Device Marking	Device	Package	Reel Size	Tape Width	Quantity
	FDFS6N754	FDFS6N754	SO-8	330mm	12mm	2500 units

Symbol	Parameter	Test Conditions		Min	Тур	Max	Units
Off Chara	acteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	$I_{D} = 250 \mu A, V_{GS} = 0V$		30			V
ΔBV_{DSS} ΔT_J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$, referenced to $25^{\circ}C$			24.5		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24V$ $V_{GS} = 0V$	T _J = 125°C			1 20	μA
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20V,$	$V_{DS} = 0V$			±100	nA
On Chara	acteristics (Note 2)						
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		1	1.7	2.5	V
$\Delta V_{GS(th)}$	Gate to Source Threshold Voltage	$I_{\rm D} = 250 \mu A, r$		-			
ΔT_J	Temperature Coefficient	25°C			-4.2		mV/°C
		V _{GS} = 10V, I _E	₀ = 4A		42	56	
r	Drain to Source On Resistance	V _{GS} = 4.5V, I			53	75	mΩ
r _{DS(on)}		$V_{GS} = 10V, I_{E}$			61	81	1162
		T _J = 125°C			01	01	
9 _{FS}	Forward Transconductance	$V_{DS} = 5V, I_D = 4A$			10		S
Dvnamic	Characteristics						
C _{iss}	Input Capacitance				225	299	pF
C _{oss}	Output Capacitance	V _{DS} = 15V, V	_{GS} = 0V,		80	107	pF
C _{rss}	Reverse Transfer Capacitance	f = 1.0MHz			42	63	pF
R _G	Gate Resistance	f = 1.0MHz			5.1		Ω
Switching	g Characteristics (Note 2)						
t _{d(on)}	Turn-On Delay Time				6	12	ns
t _r	Rise Time	$V_{DD} = 15V, I_{D}$			8	16	ns
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10V, F	$GS = 6\Omega$		20	32	ns
t _f	Fall Time				2	10	ns
Q _{g(TOT)}	Total Gate Charge at 10V	V _{DS} = 15V, I _D = 4A			4	6	nC
Q _{g(5)}	Total Gate Charge at 5V				2	3	nC
Q _{gs}	Gate to Source Gate Charge				0.6		nC
Q _{gd}	Gate to Drain "Miller" Charge				1		nC
Drain-So	urce Diode Characteristics and	Maximum F	atings				
V _{SD}	Source to Drain Diode Forward Voltage		= 1.3A (Note 2)		0.8	1.2	V
t _{rr}	Reverse Recovery Time	$I_F = 4A, di/dt$	•		13	20	ns
Q _{rr}	Reverse Recovery Charge	$I_F = 4A$, di/dt	= 100A/μs		4	6	nC
Schottky	Diode Characteristics						
V _R	Reverse Breakdown Voltage	I _R = -1mA		-30			V
	Poverse Leokese	101	$T_J = 25^{\circ}C$		39	250	μA
I _R	Reverse Leakage	V _R = -10V	$T_{\rm J} = 125^{\rm o}{\rm C}$		18		mA
IR		L 100mA	$T_J = 25^{\circ}C$		225	280	m\/
I _R		I _F = 100mA	T _J = 125°C		140		
	Ecoward Violtago		$1_{\rm J} = 125$ C				m\/
I _R V _F	Forward Voltage	$I_F = 2A$	$T_{\rm J} = 125 {\rm C}$ $T_{\rm J} = 25^{\rm o}{\rm C}$		364	450	mV

FDFS6N754 Integrated N-Channel PowerTrench[®] MOSFET and Schottky Diode



1: $R_{0,LA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{0,LC}$ is guaranteed by design while $R_{0,CA}$ is determined by the user's board design.



a) 78°C/W when mounted on a 0.5in² pad of 2 oz copper



οφφρ b) 125°C/W when mounted on a 0.02 in² pad of 2 oz copper

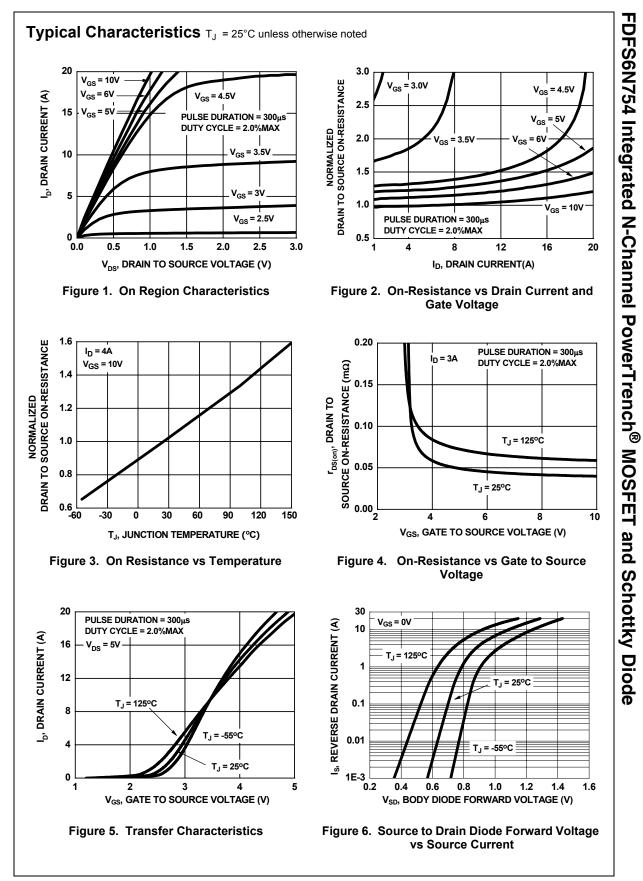
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c) 135°C/W when mounted on a minimun pad

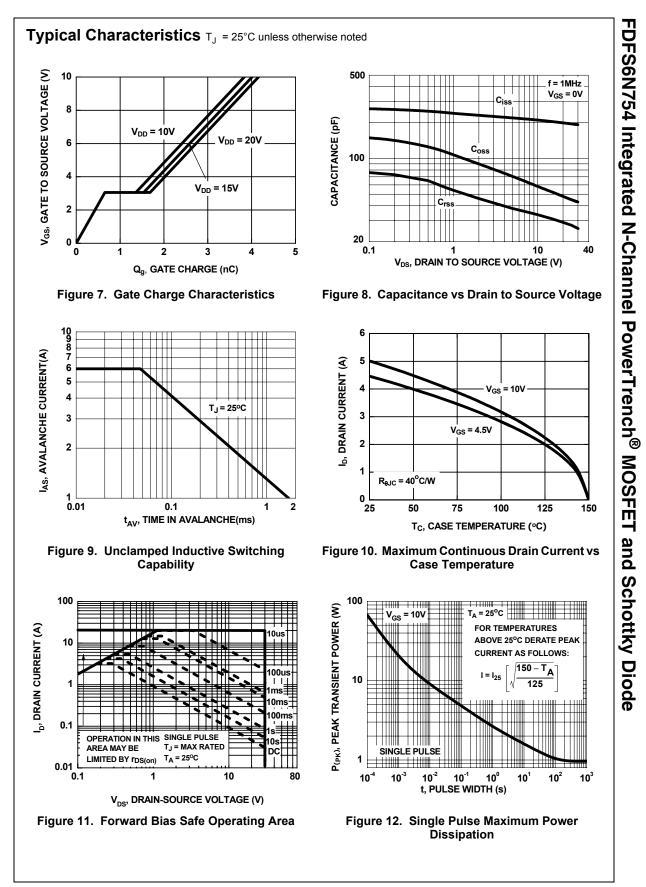
FDFS6N754 Integrated N-Channel PowerTrench<sup>®</sup> MOSFET and Schottky Diode

Scale 1 : 1 on letter size paper

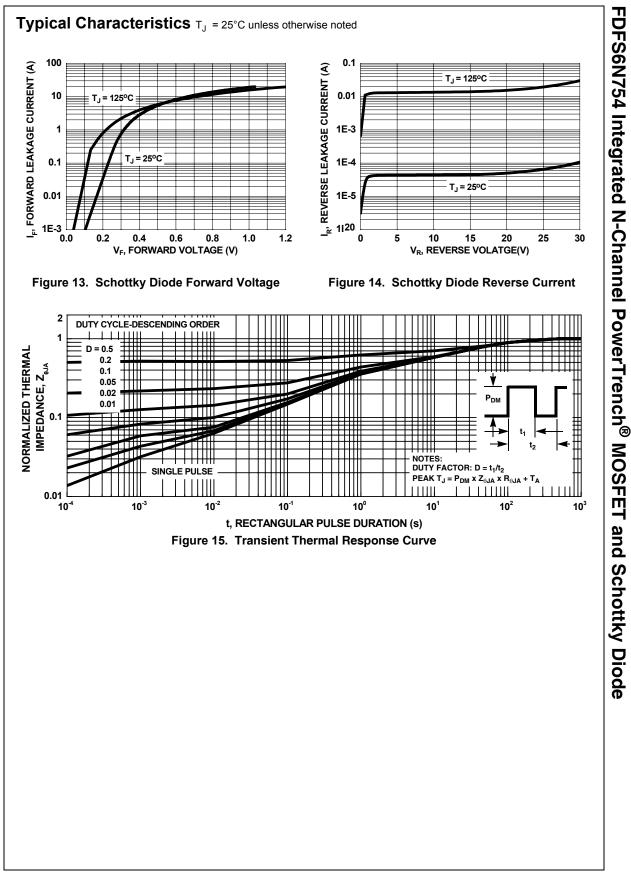
2: Pulse Test: Pulse Width < 300 µs, Duty Cycle < 2.0%.



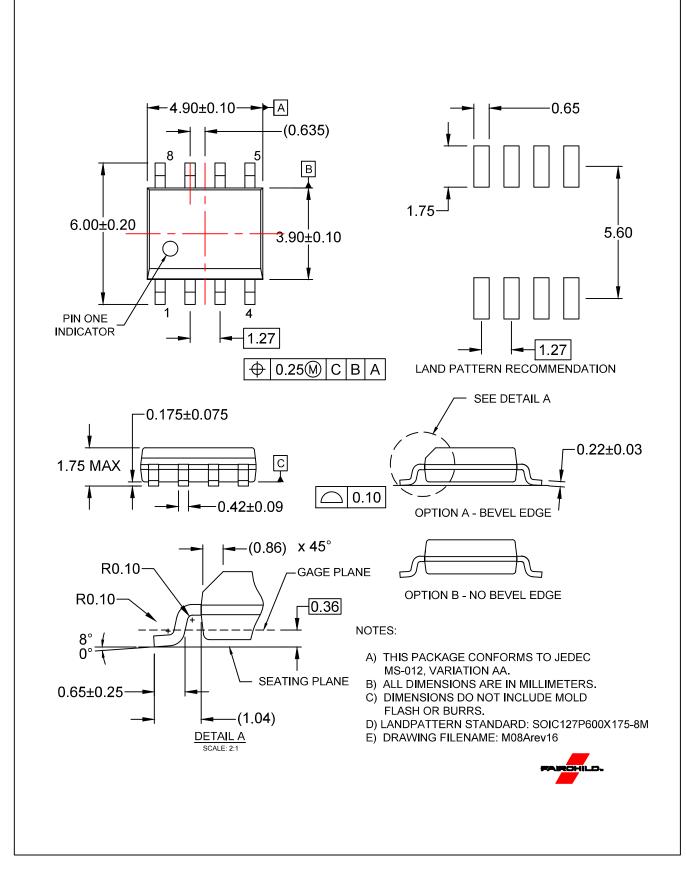
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