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ON Semiconductor®

FDMC7692S

N-Channel PowerTrench[®] SyncFETTM 30 V, 18 A, 9.3 m Ω

Features

- Max $r_{DS(on)}$ = 9.3 m Ω at V_{GS} = 10 V, I_D = 12.5 A
- Max $r_{DS(on)} = 13.6 \text{ m}\Omega$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 10.4 \text{ A}$
- High performance technology for extremely low r_{DS(on)}
- Termination is Lead-free and RoHS Compliant



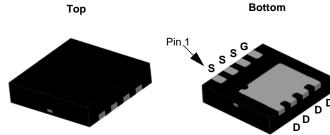
General Description

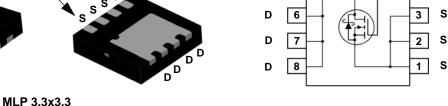
This FDMC7692S is produced using ON Semiconductor's advanced PowerTrench® process that has been especially tailored to minimize the on-state resistance. This device is well suited for Power Management and load switching applications common in Notebook Computers and Portable Battery packs.

Applications

- DC DC Buck Converters
- Notebook DC DC application

D





MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Parame		Ratings	Units	
V_{DS}	Drain to Source Voltage			30	V
V_{GS}	Gate to Source Voltage			±20	V
	Drain Current -Continuous	T _C = 25 °C		18	
I_D	-Continuous	T _A = 25 °C	(Note 1a)	12.5	Α
	-Pulsed			45	
E _{AS}	Sinlge Pulse Avalanche Energy		(Note 3)	21	mJ
Р	Power Dissipation	T _C = 25 °C		27	w
P_{D}	Power Dissipation	T _A = 25 °C	(Note 1a)	2.3	VV
T_J , T_{STG}	Operating and Storage Junction Tempera	ture Range		-55 to +150	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	4.7	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	53	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMC7692S	FDMC7692S	MLP 3.3X3.3	13 "	12 mm	3000 units

4 G

Electrical Characteristics $T_J = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	cteristics					
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		16		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			500	μΑ
I _{GSS}	Gate to Source Leakage Current	V _{GS} = 20 V, V _{DS} = 0 V			100	nA

On Characteristics (Note 2)

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$	1.2	2.0	3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		-5		mV/°C
r _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 12.5 A		7.8	9.3	mΩ
		$V_{GS} = 4.5 \text{ V}, I_D = 10.4 \text{ A}$		10.8	13.6	
		$V_{GS} = 10 \text{ V}, I_D = 12.5 \text{ A}$ $T_J = 125 ^{\circ}\text{C}$		9.6	13.0	
9 _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 12.5 A		62		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz	1040	1385	pF
C _{oss}	Output Capacitance		445	590	pF
C _{rss}	Reverse Transfer Capacitance		40	60	pF
R_q	Gate Resistance		1.1	2.9	Ω

Switching Characteristics

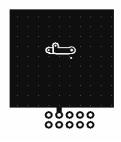
t _{d(on)}	Turn-On Delay Time		9	17	ns
t _r	Rise Time	V _{DD} = 15 V, I _D = 12.5 A,	3	10	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$	19	34	ns
t _f	Fall Time		3	10	ns
Q_g	Total Gate Charge	V _{GS} = 0 V to 10 V	16	23	nC
Qg	Total Gate Charge	$V_{GS} = 0 \text{ V to } 4.5 \text{ V}$ $V_{DD} = 15 \text{ V}$	8	10	nC
Q _{gs}	Gate to Source Gate Charge	I _D = 12.5 A	4		nC
Q_{gd}	Gate to Drain "Miller" Charge		2		nC

Drain-Source Diode Characteristics

V _{SD}	Source to Drain Dioge Forward voltage	V _{GS} = 0 V, I _S = 12.5 A (Note 2)	0.9	1.3	V
		$V_{GS} = 0 \text{ V}, I_S = 0.9 \text{ A}$ (Note 2)	0.5	0.7	
t _{rr}	Reverse Recovery Time	I _F = 12.5 A, di/dt = 300 A/μs	21	33	ns
Q _{rr}	Reverse Recovery Charge	T _F = 12.5 A, αι/αι = 300 A/μs	16	29	nC

Notes:

^{1.} R_{BJA} is determined with the device mounted on a 1in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{BJC} is guaranteed by design while R_{BCA} is determined by the user's board design.



a. 53 °C/W when mounted on a 1 in² pad of 2 oz copper.



b. 125 °C/W when mounted on a minimum pad of 2 oz copper.

- 2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.
- 3. E_{AS} of 21 mJ is based on starting T_J = 25 °C, L = 0.3 mH, I_{AS} = 12.0 A, V_{DD} = 27 V, V_{GS} = 10 V. 100% test at L = 3 mH, I_{AS} = 3.2 A .

Typical Characteristics T_J = 25 °C unless otherwise noted

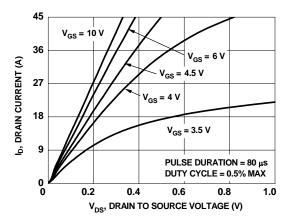


Figure 1. On-Region Characteristics

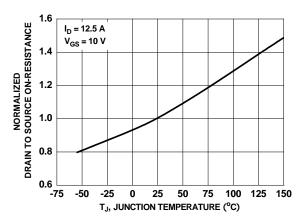


Figure 3. Normalized On-Resistance vs Junction Temperature

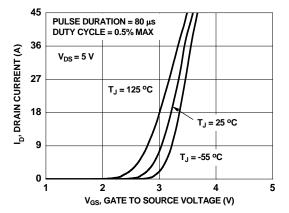


Figure 5. Transfer Characteristics

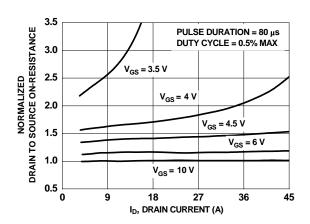


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

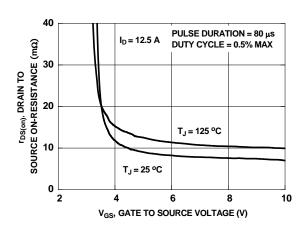


Figure 4. On-Resistance vs Gate to Source Voltage

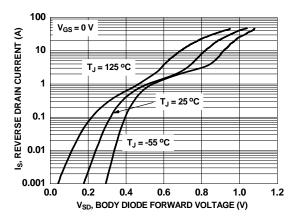


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics T_J = 25 °C unless otherwise noted

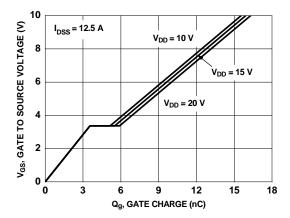


Figure 7. Gate Charge Characteristics

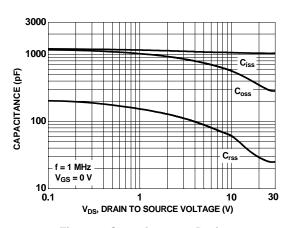


Figure 8. Capacitance vs Drain to Source Voltage

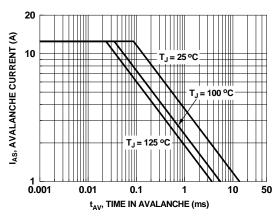


Figure 9. Unclamped Inductive Switching Capability

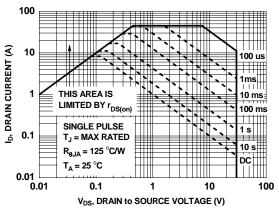


Figure 10. Forward Bias Safe Operating Area

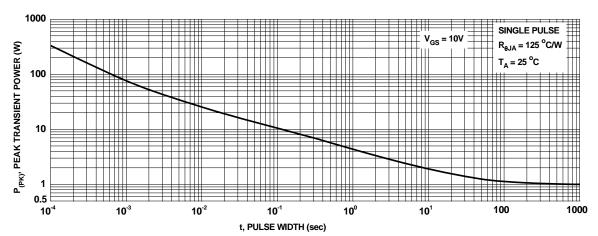


Figure 11. Single Pulse Maximum Power Dissipation



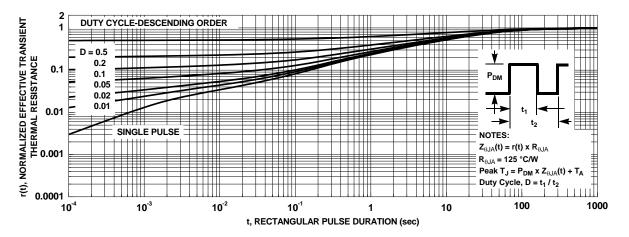


Figure 12. Junction-to-Ambient Transient Thermal Response Curve

Typical Characteristics (continued)

SyncFET Schottky body diode Characteristics

ON Semiconductor's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 13 shows the reverse recovery characteristic of the FDMC7692S.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

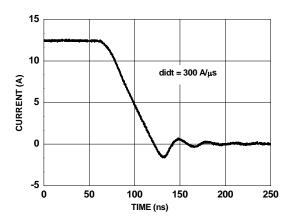


Figure 13. SyncFET body diode reverse recovery characteristic

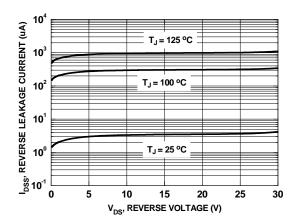


Figure 14. SyncFET body diode reverse leakage versus drain-source voltage

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