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SEMICONDUCTOR

October 2013

FDME510PZT P-Channel PowerTrench[®] MOSFET -20 V, -6 A, 37 m Ω

Features

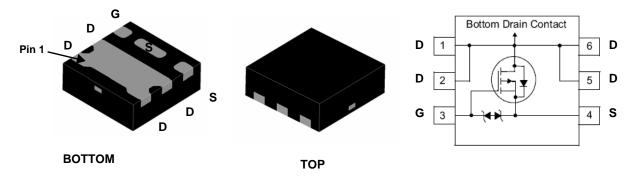
- Max $r_{DS(on)} = 37 \text{ m}\Omega$ at $V_{GS} = -4.5 \text{ V}$, $I_D = -5 \text{ A}$
- Max $r_{DS(on)} = 50 \text{ m}\Omega$ at $V_{GS} = -2.5 \text{ V}$, $I_D = -4 \text{ A}$
- Max $r_{DS(on)} = 65 \text{ m}\Omega$ at $V_{GS} = -1.8 \text{ V}$, $I_D = -3 \text{ A}$
- Max $r_{DS(on)}$ = 100 m Ω at V_{GS} = -1.5 V, I_D = -2 A
- Low profile: 0.55 mm maximum in the new package MicroFET 1.6x1.6 Thin
- Free from halogenated compounds and antimony oxides
- HBM ESD protection level > 2400V (Note3)
- RoHS Compliant



General Description

This device is designed specifically for battery charging or load switching in cellular handset and other ultraportable applications. It features a MOSFET with low on-state resistance.

The MicroFET 1.6x1.6 **Thin** package offers exceptional thermal performance for its physical size and is well suited to switching and linear mode applications.



MicroFET 1.6x1.6 Thin

MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Paramet		Ratings	Units		
V _{DS}	Drain to Source Voltage		-20	V		
V _{GS}	Gate to Source Voltage			±8	V	
I _D	Drain Current -Continuous	T _A = 25 °C	(Note 1a)	-6	•	
	-Pulsed			-15	Α	
P _D	Power Dissipation for Single Operation	T _A = 25 °C	(Note 1a)	2.1	14/	
	Power Dissipation for Single Operation	T _A = 25 °C	(Note 1b)	0.7	W	
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	60	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	(Note 1b)	175	0/10

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
7T	FDME510PZT	MicroFET 1.6x1.6 Thin	7 "	8 mm	5000 units

DME51
IOPZT
P-Cha
innel F
owerT
rench
® MOS
SFET

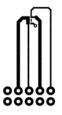
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	acteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = -250 μA, V _{GS} = 0 V	-20			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = -250 \ \mu\text{A}$, referenced to 25 °C		-13		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -16 V, V _{GS} = 0 V			-1	μA
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μA
On Chara	acteristics					
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = -250 \ \mu A$	-0.4	-0.5	-1.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I_D = -250 μ A, referenced to 25 °C		3		mV/°C
	Drain to Source On Resistance	$V_{GS} = -4.5 \text{ V}, I_D = -5 \text{ A}$		31	37	
		$V_{GS} = -2.5 \text{ V}, I_D = -4 \text{ A}$		38	50	mΩ
r _{DS(on)}		$V_{GS} = -1.8 \text{ V}, I_D = -3 \text{ A}$		48	65	
		$V_{GS} = -1.5 \text{ V}, \ I_D = -2 \text{ A}$		57	100	
		$V_{GS} = -4.5 \text{ V}, \ I_D = -5 \text{ A}, \ T_J = 125 \ ^{\circ}\text{C}$		40	60	
9 _{FS}	Forward Transconductance	$V_{DS} = -5 V, I_{D} = -5 A$		21		S
Dynamic	Characteristics					
C _{iss}	Input Capacitance			1120	1490	pF
C _{oss}	Output Capacitance	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz		155	210	pF
C _{rss}	Reverse Transfer Capacitance			140	210	pF
Switching	g Characteristics					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -10 \text{ V}, \text{ I}_{D} = -5 \text{ A}$		6.5	13	ns
t _r	Rise Time			10	16	ns
t _{d(off)}	Turn-Off Delay Time	$-$ V _{GS} = -4.5 V, R _{GEN} = 6 Ω		93	149	ns
t _f	Fall Time			54	86	ns
Q _g	Total Gate Charge			16	22	nC
Q _{gs}	Gate to Source Gate Charge	— V _{DD} = -10 V, I _D = -5 A — V _{GS} = -4.5 V		1.6		nC
Q _{gd}	Gate to Drain "Miller" Charge	* GS = *		4		nC

V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = -1.6 A$ (Note 2)		-0.6	-1.2	V
t _{rr}	Reverse Recovery Time	I _F = -5 A, di/dt = 100 A/μs		38	61	ns
Q _{rr}	Reverse Recovery Charge			16	29	nC

Notes: 1. R_{0JA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



a. 60 °C/W when mounted on a 1 in² pad of 2 oz copper.



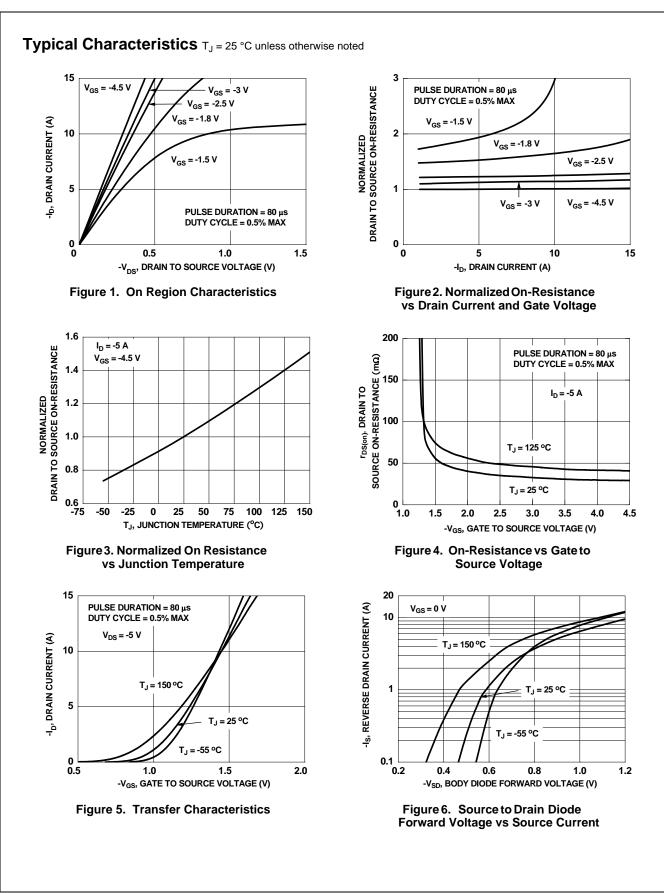
b. 175 °C/W when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.

3. The diode connected between the gate and source serves only as protection ESD. No gate overvoltage rating is implied.

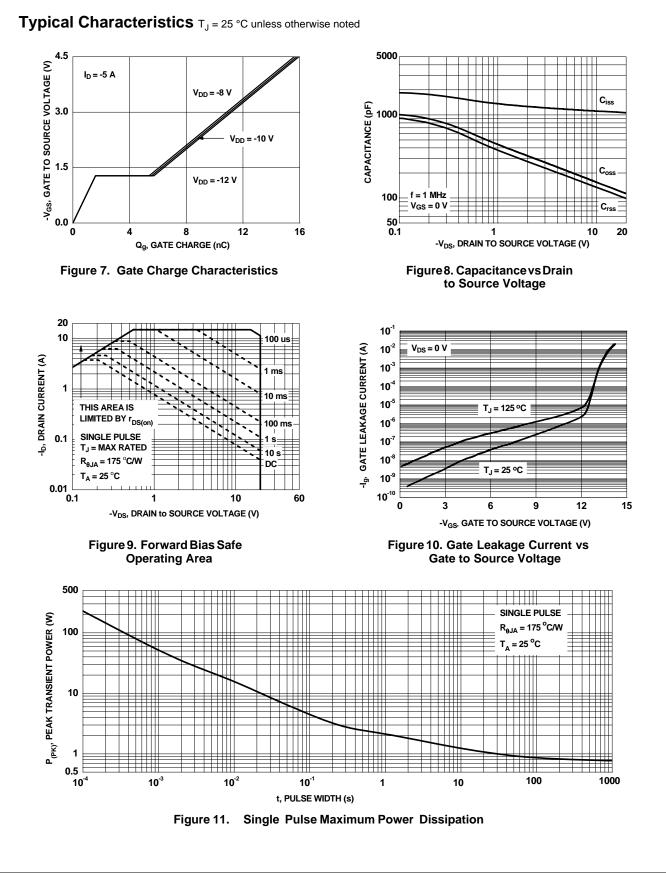
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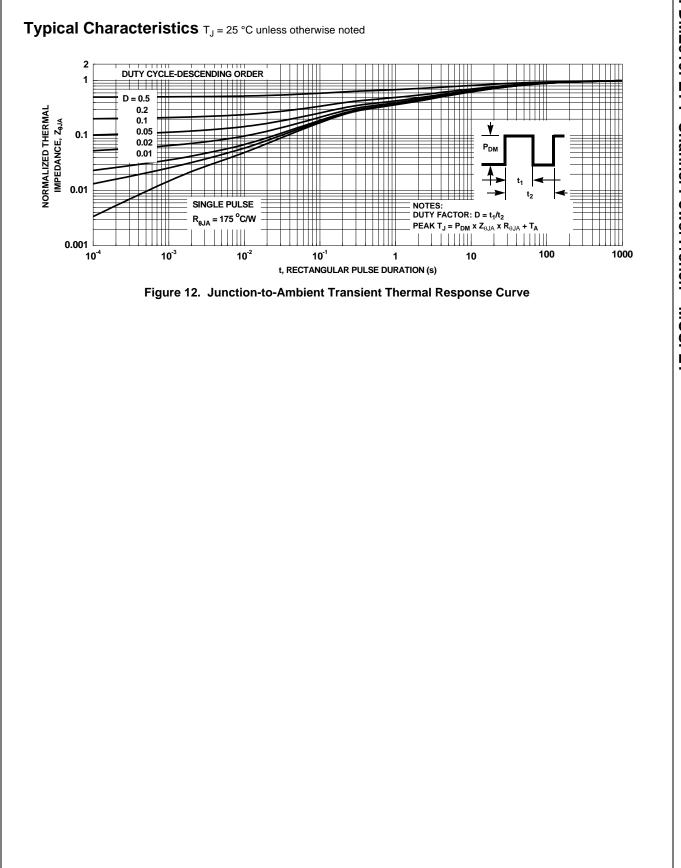


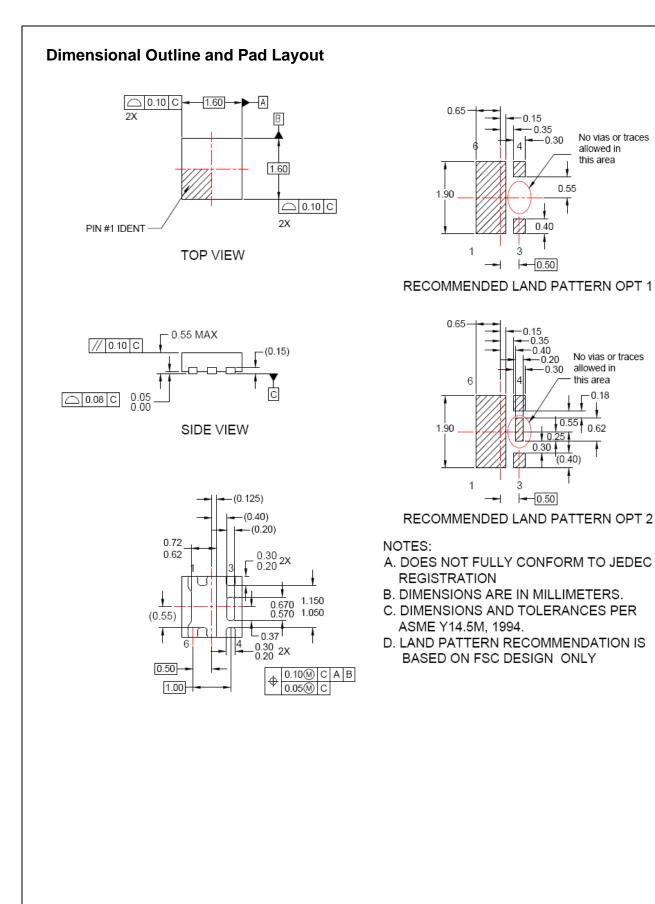
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