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N-Channel PowerTrench[®] MOSFET 30 V, 175 A, 1.8 $m\Omega$

Features

- Max $r_{DS(on)}$ = 1.8 m Ω at V_{GS} = 10 V, I_D = 30 A
- Max $r_{DS(on)} = 2.4 \text{ m}\Omega \text{ at } V_{GS} = 4.5 \text{ V}, I_D = 26 \text{ A}$
- Advanced Package and Silicon Combination for Low r_{DS(on)} and High Efficiency
- Next Generation Enhanced Body Diode Technology, Engineered for Soft Recovery
- MSL1 Robust Package Design
- 100% UIL Tested
- RoHS Compliant

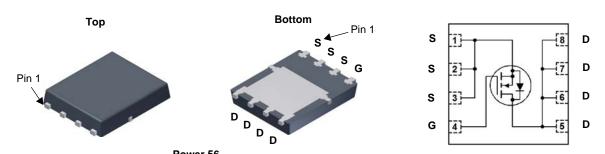


General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency and to minimize switch node ringing of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $r_{DS(on)}$, fast switching speed ang body diode reverse recovery performance.

Applications

- VRM Vcore Switching for Desktop and Server
- OringFET / Load Switching
- DC-DC Conversion
- Motor Bridge Switch



Power 56

MOSFET Maximum Ratings TA = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			30	V	
V _{GS}	Gate to Source Voltage		(Note 4)	±20	V	
ID	Drain Current -Continuous	T _C = 25 °C	(Note 6)	175		
	-Continuous	T _C = 100 °C	(Note 6)	110	_	
	-Continuous	T _A = 25 °C	(Note 1a)	30	Α	
	-Pulsed		(Note 5)	680		
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	126	mJ	
P _D	Power Dissipation	T _C = 25 °C		83		
	Power Dissipation	T _A = 25 °C	(Note 1a)	2.5		
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	1.5	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient (Note 1a	ı) 50	C/VV

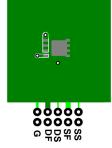
Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS8018	FDMS8018	Power 56	13 "	12 mm	3000 units

December 2015

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
Off Chara	octeristics						
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0 V	30			V	
ΔBV _{DSS} ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25 °C		14		mV/°C	
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24 V, V_{GS} = 0 V$			1	μA	
I _{GSS}	Gate to Source Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA	
	cteristics			- <u>!</u>			
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \ \mu A$	1.0	1.5	3.0	V	
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25 °C		-6		mV/°C	
	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 30 A		1.5	1.8	mΩ	
r _{DS(on)}		V _{GS} = 4.5 V, I _D = 26 A		1.9	2.4		
		V_{GS} = 10 V, I _D = 30 A, T _J = 125 °C		2.2	2.7		
9 _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 30 A		194		S	
Dynamic	Characteristics						
C _{iss}	Input Capacitance			3935	5235	pF	
C _{oss}	Output Capacitance	─ V _{DS} = 15 V, V _{GS} = 0 V, _ f = 1 MHz		1380	1835	pF	
C _{rss}	Reverse Transfer Capacitance			137	210	pF	
R _g	Gate Resistance			0.9		Ω	
Switching	g Characteristics						
t _{d(on)}	Turn-On Delay Time			15	27	ns	
t _r	Rise Time	V _{DD} = 15 V, I _D = 30 A,		7.3	15	ns	
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_{GEN} = 6 Ω		38	62	ns	
t _f	Fall Time			4.8	10	ns	
Qg	Total Gate Charge	V _{GS} = 0 V to 10 V		58	61	nC	
Qg	Total Gate Charge	$V_{GS} = 0 V \text{ to } 4.5 V V_{DD} = 15 V,$		28	39	nC	
Q _{gs}	Gate to Source Charge	I _D = 30 A		10.3		nC	
Q _{gd}	Gate to Drain "Miller" Charge			7.7		nC	
Drain-Sou	urce Diode Characteristics						
V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = 2.1 A$ (Note 2)		0.67	1.1	V	
		V _{GS} = 0 V, I _S = 30 A (Note 2)		0.77	1.2		
t _{rr}	Reverse Recovery Time	–I _F = 30 A, di/dt = 100 A/μs		43	69	ns	
Q _{rr}	Reverse Recovery Charge	$r_{\rm F} = 00 \text{A}, \text{and} = 100 \text{A} \mu \text{s}$		25	40	nC	
t _{rr}	Reverse Recovery Time	- I _F = 30 A, di/dt = 300 A/μs		34	55	ns	
Q _{rr}	Reverse Recovery Charge	$F = 3070, 0000 = 00070 \mu s$		46	72	nC	

Notes: 1. R_{0JA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



a) 50 °C/W when mounted on a 1 in² pad of 2 oz copper



b) 125 °C/W when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%. 3. Starting T_J = 25 °C; N-ch: L = 0.3 mH, I_{AS} = 29 A, V_{DD} = 27 V, V_{GS} = 10 V.

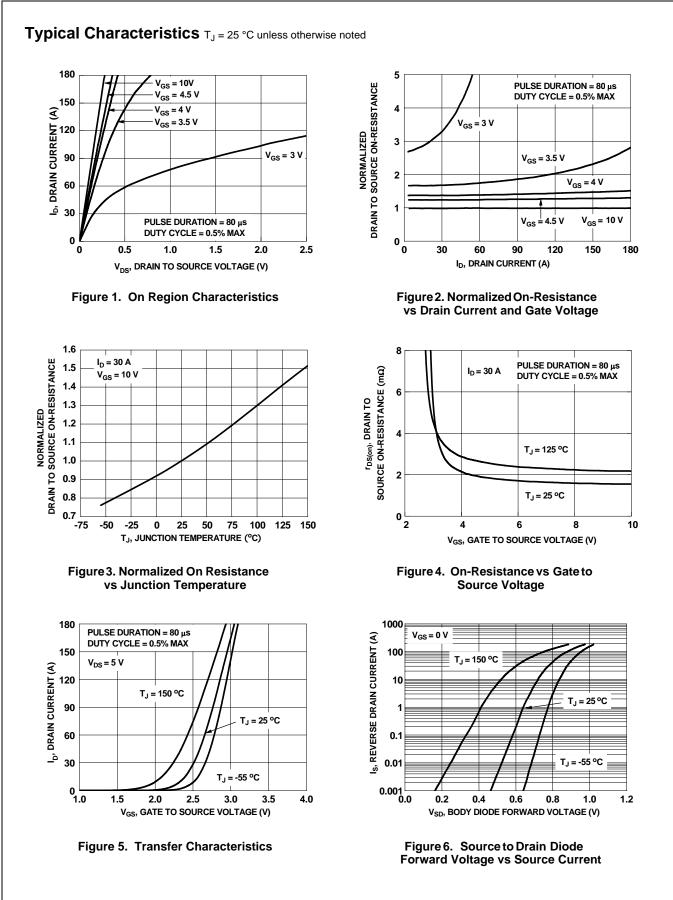
4. As an N-ch device, the negative Vgs rating is for low duty cycle pulse occurrence only. No continuous rating is implied.

5. Pulsed Id please refer to Fig 11 SOA graph for more details.

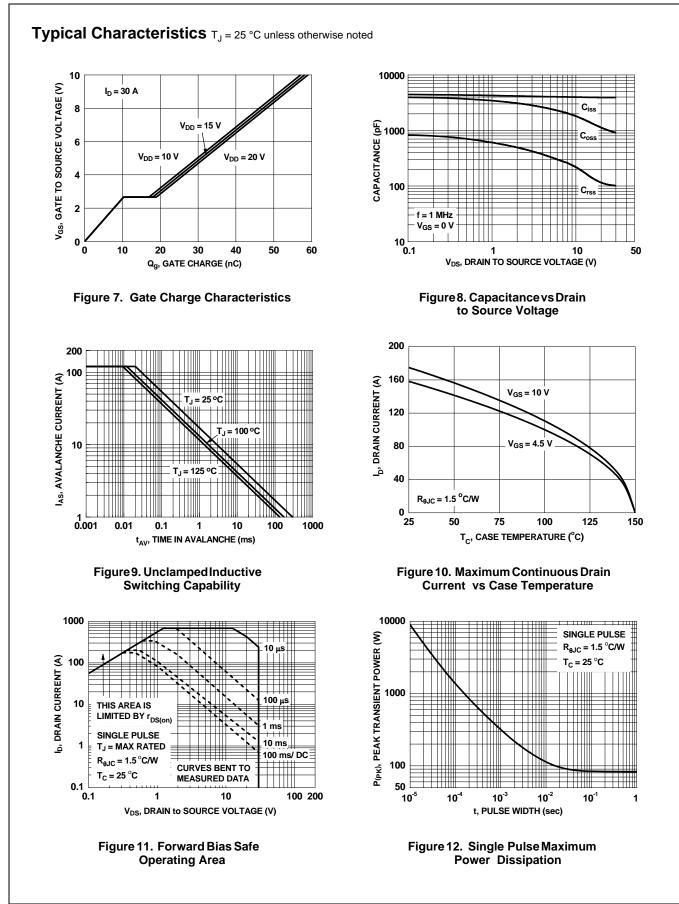
6. Computed continuous current limited to Max Junction Temperature only, actual continuous current will be limited by thermal & electro-mechanical application board design.

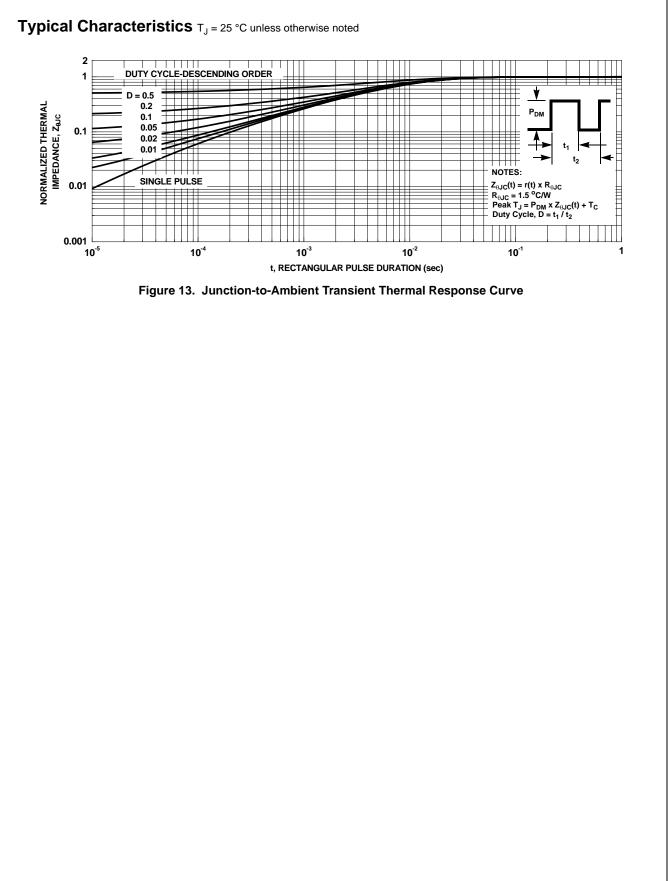
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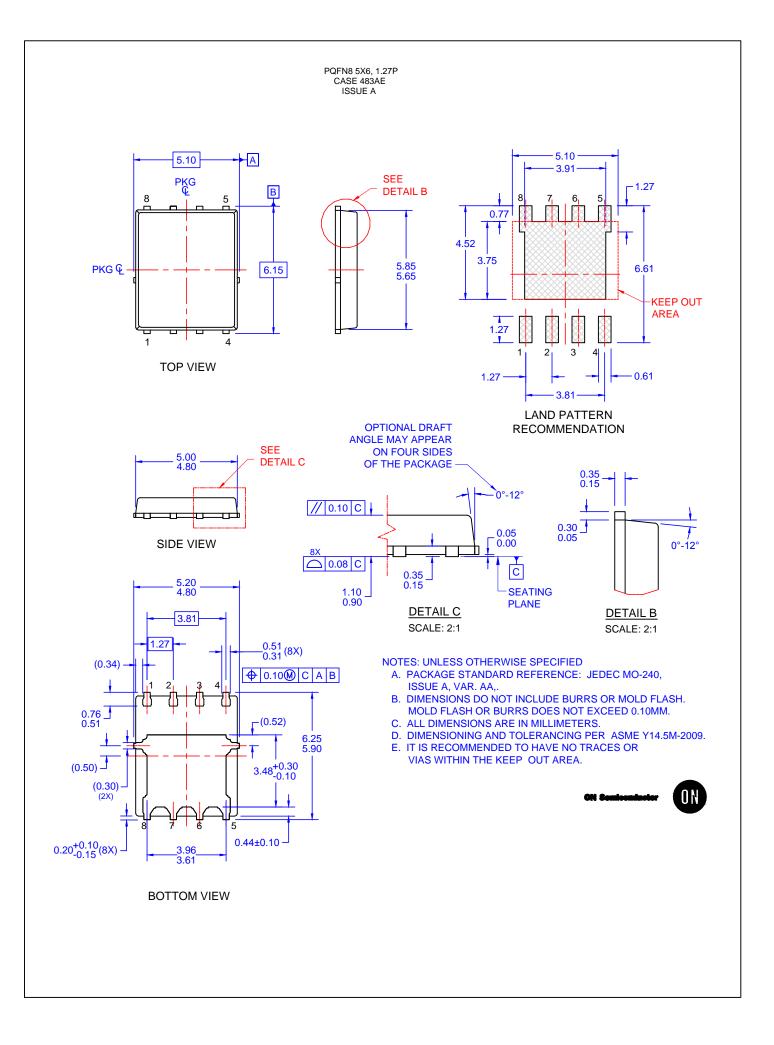
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