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N-Channel PowerTrench[®] MOSFET 80 V, 122 A, 3.9 m Ω

Features

- Max $r_{DS(on)}$ = 3.9 m Ω at V_{GS} = 10 V, I_D = 19 A
- Max r_{DS(on)} = 5.5 mΩ at V_{GS} = 8 V, I_D = 15.5 A
- Advanced Package and Silicon combination for low r_{DS(on)} and high efficiency
- Next generation enhanced body diode technology, engineered for soft recovery
- MSL1 robust package design
- 100% UIL tested
- RoHS Compliant

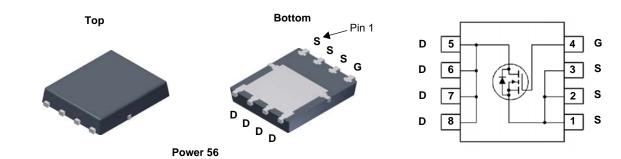


General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency and to minimize switch node ringing of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $r_{DS(on)}$, fast switching speed and body diode reverse recovery performance.

Applications

- OringFET / Load Switching
- DC-DC Conversion



MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			80	V	
V _{GS}	Gate to Source Voltage			±20	V	
	Drain Current -Continuous	T _C = 25 °C		122		
	-Continuous	T _C = 100 °C		78		
D	-Continuous	T _A = 25 °C	(Note 1a)	19	Α	
	-Pulsed		(Note 4)	556		
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	252	mJ	
P _D	Power Dissipation	T _C = 25 °C		104		
	Power Dissipation	T _A = 25 °C	(Note 1a)	2.5		
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case		1.2	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	(Note 1a)	50	C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS86300	FDMS86300	Power 56	13 "	12 mm	3000 units

FDMS86300
N-Channel
PowerTrench
[®] MOSFET

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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
Off Chara	cteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0 V	80			V	
ΔBV_{DSS} ΔT_J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25 °C		39		mV/°C	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 64 V, V _{GS} = 0 V			1	μA	
I _{GSS}	Gate to Source Leakage Current	V_{GS} = ±20 V, V_{DS} = 0 V			±100	nA	
On Chara	cteristics						
V _{GS(th)}	Gate to Source Threshold Voltage	V _{GS} = V _{DS} , I _D = 250 μA	2.5	3.4	4.5	V	
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25 °C		-11		mV/°C	
r _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 19 A		3.2	3.9		
		V _{GS} = 8 V, I _D = 15.5 A		3.8	5.5		
		V _{GS} = 10 V, I _D = 19 A, T _J = 125 °C		5.0	5.8		
9 FS	Forward Transconductance	V _{DS} = 10 V, I _D = 19 A		60		S	
Dynamic	Characteristics						
C _{iss}	Input Capacitance			5325	7082	pF	
C _{oss}	Output Capacitance	$-V_{DS} = 40 V, V_{GS} = 0 V,$		957	1272	pF	
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		26	63	pF	
R _g	Gate Resistance			1.2		Ω	
Switching	Characteristics						
t _{d(on)}	Turn-On Delay Time			31	50	ns	
t _r	Rise Time	V _{DD} = 40 V, I _D = 19 A,		26	43	ns	
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10 V, R _{GEN} = 6 Ω		36	58	ns	
t _f	Fall Time			9	18	ns	
Q _q	Total Gate Charge	V _{GS} = 0 V to 10 V		72	86	nC	
Q _g	Total Gate Charge	$V_{GS} = 0 V \text{ to } 8 V V_{DD} = 40 V,$		59	71	nC	
Q _{gs}	Gate to Source Charge	I _D = 19 A		28.2		nC	
Q _{gd}	Gate to Drain "Miller" Charge			14.9		nC	
Drain-Sou	rce Diode Characteristics						
		$V_{GS} = 0 V, I_S = 2.1 A$ (Note 2)		0.71	1.2		
V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = 19 A$ (Note 2)		0.81	1.3	V	
t _{rr}	Reverse Recovery Time	1 = 10 A = 100 A = 100 A = 100 A		57	90	ns	
Q _{rr}	Reverse Recovery Charge	- I _F = 19 A, di/dt = 100 A/μs		50	80	nC	
t _{rr}	Reverse Recovery Time	1 = 10.4 di/dt = 200.4/m		48	77	ns	
Q _{rr}	Reverse Recovery Charge	—I _F = 19 A, di/dt = 300 A/μs		103	165	nC	

Q_{rr}

Notes: 1. R_{0JA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



Reverse Recovery Charge

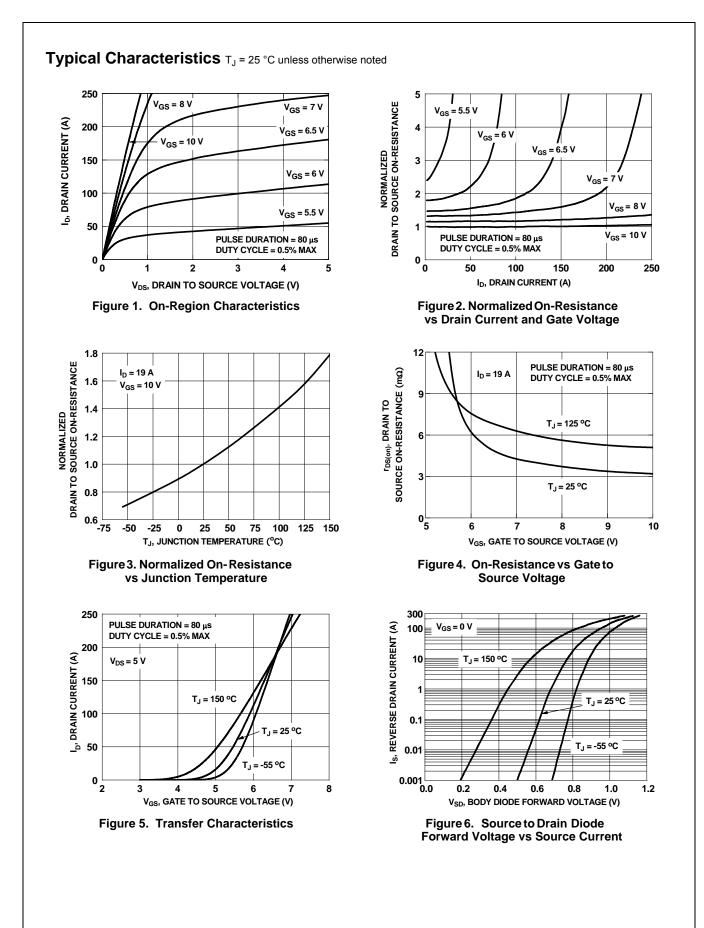
a) 50 °C/W when mounted on a 1 in² pad of 2 oz copper

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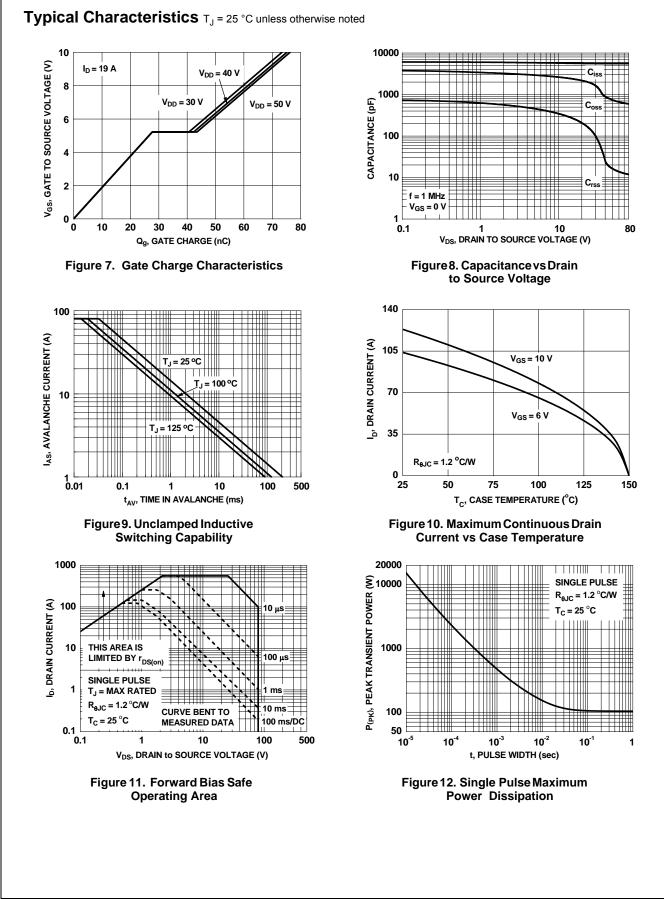
b) 125 °C/W when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.

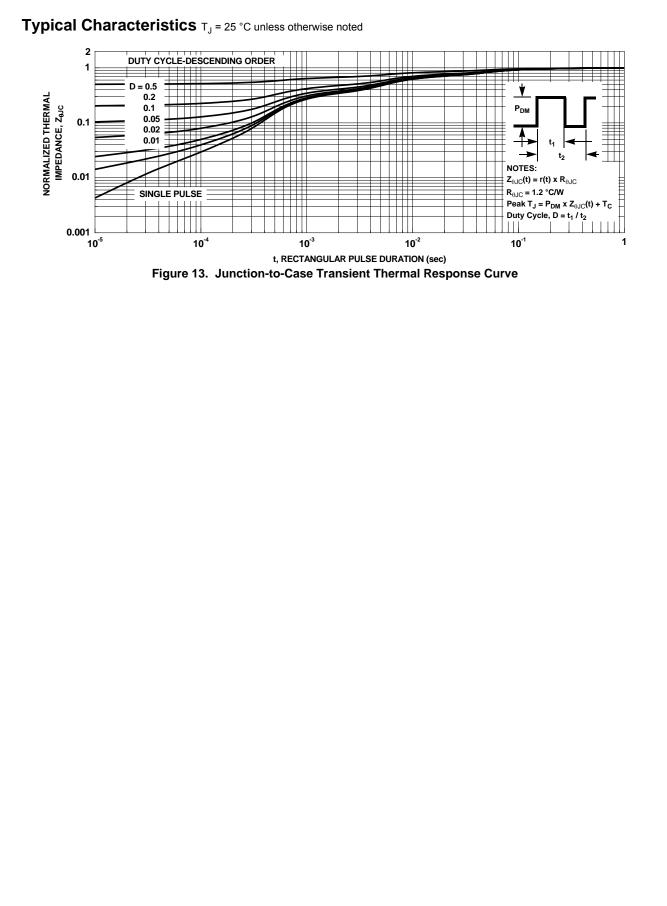
3. E_{AS} of 252 mJ is based on starting T_J = 25 °C, L = 0.3 mH, I_{AS} = 41 A, V_{DD} = 72 V, V_{GS} = 10 V. 4. Pulse Id limited by junction temperature, td ≤ 100 μ s, please refer to SOA curve for more details.







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FDMS86300 N-Channel PowerTrench[®] MOSFET



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