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ON Semiconductor®

# FDP6030BL/FDB6030BL

## N-Channel Logic Level PowerTrench® MOSFET

### **Features**

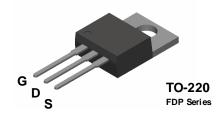
### **General Description**

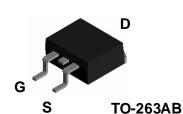
This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

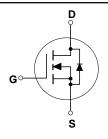
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable  $R_{\text{DS(on)}}$  specifications resulting in DC/DC power supply designs with higher overall efficiency.

- 40 A, 30 V.  $R_{DS(ON)} = 0.018~\Omega~@~V_{GS} = 10~V$   $R_{DS(ON)} = 0.024~\Omega~@~V_{GS} = 4.5~V.$
- Critical DC electrical parameters specified at elevated temperature.
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor.
- High performance trench technology for extremely low  $R_{\mbox{\tiny DS(ON)}}.$
- 175°C maximum junction temperature rating.

FDB Series







Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted

Symbol	Parameter	FDP6030BL	FDB6030BL	Units
V <sub>DSS</sub>	Drain-Source Voltage	30		V
$V_{GSS}$	Gate-Source Voltage	±20		V
I <sub>D</sub>	Maximum Drain Current - Continuous (Note 1)	40		Α
	- Pulsed	1	20	
$P_D$	Total Power Dissipation @ T <sub>C</sub> = 25°C	(	60	W
	Derate above 25°C	0	.36	W/°C
$T_J$ , $T_{STG}$	Operating and Storage Junction Temperature Range	-65 to +175		°C
<b>Therma</b>	I Characteristics			
$R_{\theta_{JC}}$	Thermal Resistance, Junction-to-Case	2.5		°C/W
$R_{\theta_{JA}}$	Thermal Resistance, Junction-to-Ambient	62.5		°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
FDB6030BL	FDB6030BL	13"	24mm	800
FDP6030BL	FDP6030BL	Tube	N/A	45

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
DRAIN-S	OURCE AVALANCHE RAT	INGS (Note 1)				
W <sub>DSS</sub>	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 15 \text{ V}, I_D = 40 \text{ A}$			150	mJ
I <sub>AR</sub>	Maximum Drain-Source Avalnche	Current			40	Α
Off Char	acteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	30			V
ΔBVnss ΔT.	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$		23		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V			1	μд
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Chara	acteristics (Note 1)					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1	1.6	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_{,J}}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$		-4.5		mV/°C
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A},$ $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125^{\circ}\text{C}$ $V_{GS} = 4.5 \text{ V}, I_D = 17 \text{ A}$		0.015 0.021 0.019	0.018 0.030 0.024	Ω
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS} = 10 \text{ V}, V_{DS} = 10 \text{ V}$	40			Α
<b>g</b> <sub>FS</sub>	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_{D} = 20 \text{ A}$		30		S
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$		1160		pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		250		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			100		pF
Switchin	q Characteristics (Note 1)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 15 \text{ V}, I_D = 1 \text{ A},$		9	17	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS}$ = 10 V, $R_{GEN}$ = 6 $\Omega$		11	20	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			23	37	ns
t <sub>f</sub>	Turn-Off Fall Time			8	16	ns
$Q_g$	Total Gate Charge	V <sub>DS</sub> = 15 V,		12	17	nC
$Q_{gs}$	Gate-Source Charge	$I_D = 20 \text{ A}, V_{GS} = 5 \text{ V}$		3.2		nC
$Q_{gd}$	Gate-Drain Charge			3.7		nC
Drain-So	ource Diode Characteristics	and Maximum Ratings				
I <sub>s</sub>	Maximum Continuous Drain-Source				40	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 20 \text{ A}$ (Note 1)		0.95	1.2	V

Note: 1. Pulse Test: Pulse Width  $\leq 300~\mu s$ , Duty Cycle  $\leq 2.0\%$ 

## **Typical Characteristics**

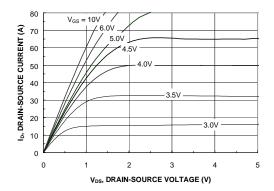


Figure 1. On-Region Characteristics.

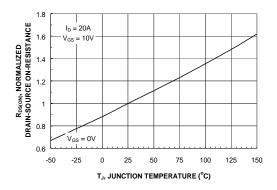


Figure 3. On-Resistance Variation with Temperature.

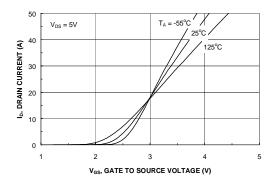


Figure 5. Transfer Characteristics.

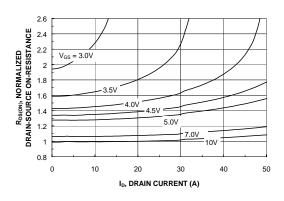


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

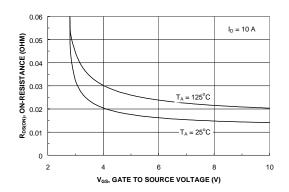


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

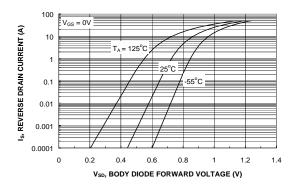
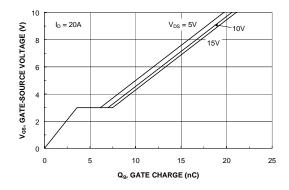


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## Typical Characteristics (continued)



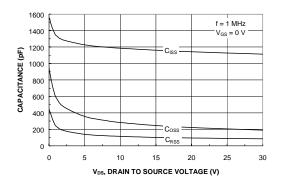
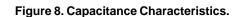
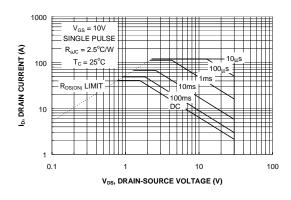


Figure 7. Gate-Charge Characteristics.





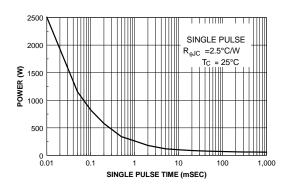


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

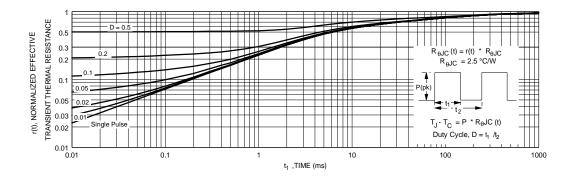


Figure 11. Transient Thermal Response Curve.

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