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## FDP7N50 N-Channel UniFET<sup>TM</sup> MOSFET 500 V, 7 A, 900 mΩ

### 500 V, I A, 900

### Features

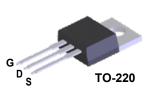
- +  $R_{DS(on)}$  = 900 m $\Omega$  (Max.) @ V<sub>GS</sub> = 10 V, I<sub>D</sub> = 3.5 A
- Low Gate Charge (Typ. 12.8 nC)
- Low C<sub>rss</sub> (Typ. 9 pF)
- 100% Avalanche Tested

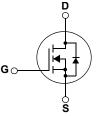
### Applications

- ALCD/LED TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supplypplications

### Description

UniFET<sup>TM</sup> MOSFET is Fairchild Semiconductor<sup>®</sup>, s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





### **Absolute Maximum Ratings**

Symbol	Parameter			FDP7N50	Unit	
V <sub>DSS</sub>	Drain-Source Voltage			500	V	
I <sub>D</sub>	Drain Current - Continuous ( $T_C = 25^{\circ}C$ - Continuous ( $T_C = 100^{\circ}$			7 4.2	A A	
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	28	A	
V <sub>GSS</sub>	Gate-Source voltage			±30	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy		(Note 2)	270	mJ	
I <sub>AR</sub>	Avalanche Current		(Note 1)	7	A	
E <sub>AR</sub>	Repetitive Avalanche Energy		(Note 1)	8.9	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.5	V/ns	
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C) - Derate above 25°C		89 0.71	W W/°C	
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +150	°C	
Τ <sub>L</sub>	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		e,	300	°C	

### **Thermal Characteristics**

Symbol	Parameter	FDP7N50	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.4	
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	

April 2013

DP7N50
N-Channel
UniFET <sup>TM</sup>
MOSFET

### Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP7N50	FDP7N50	TO-220			50

### Electrical Characteristics T<sub>c</sub> = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max	Unit
Off Charac	teristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA				V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C		0.5		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 500V, V_{GS} = 0V$ $V_{DS} = 400V, T_{C} = 125^{\circ}C$			1 10	μΑ μΑ
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30V, V <sub>DS</sub> = 0V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V			-100	nA
On Charac	teristics				1	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3.5A		0.76	0.9	Ω
9 <sub>FS</sub>	Forward Transconductance $V_{DS} = 40V, I_D = 3.5A$			2.5		S
Dynamic C	haracteristics				1	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V,		720	940	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0MHz		95	190	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			9	13.5	pF
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 250V, I <sub>D</sub> = 7A		6	20	ns
t <sub>r</sub>	Turn-On Rise Time	$R_{G} = 25\Omega$		55	120	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			25	60	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4)		35	80	ns
Qg	Total Gate Charge	V <sub>DS</sub> = 400V, I <sub>D</sub> = 7A		12.8	16.6	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10V		3.7		nC
Q <sub>gd</sub>	Gate-Drain Charge	(Note 4)		5.8		nC
Drain-Sou	rce Diode Characteristics and Maximur	n Ratings		1	1	
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current				7	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current				28	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 7A			1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 7A		275		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> /dt =100A/μs		1.7		μC

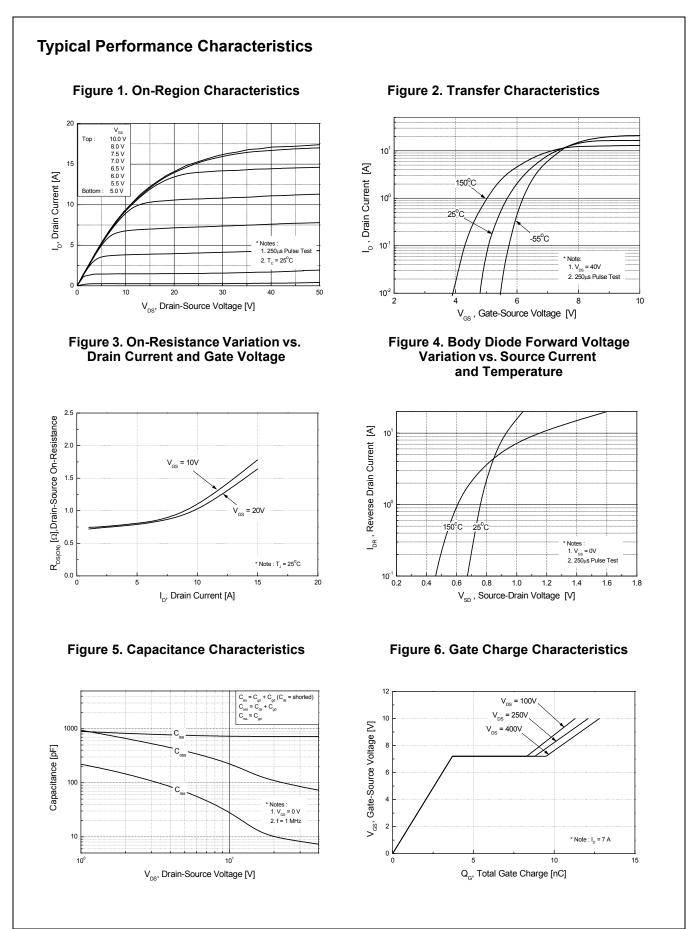
#### NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

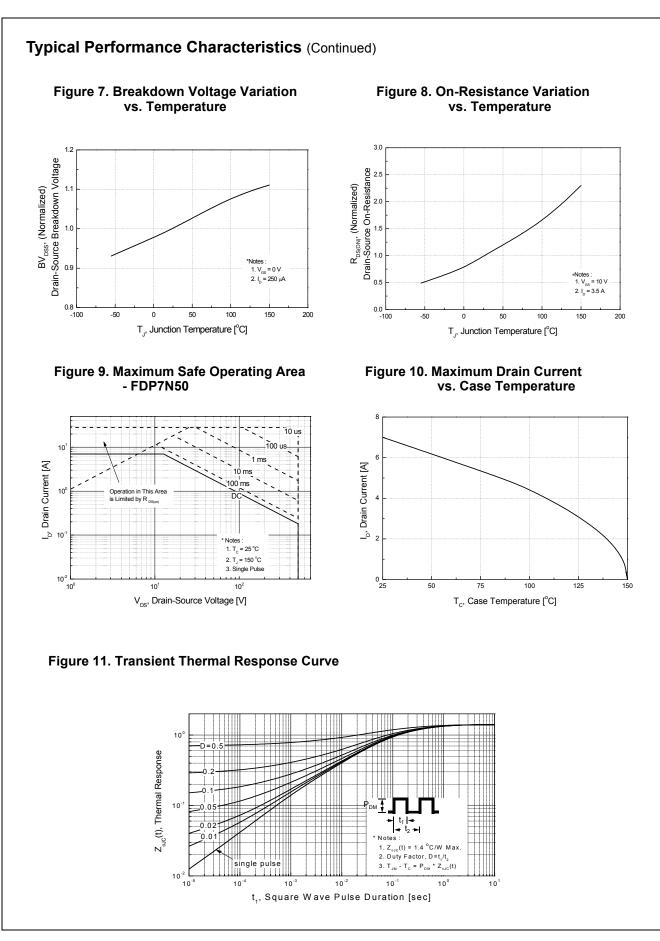
2. I\_{AS} = 7A, V\_{DD} = 50V, L=10mH, R\_G = 25 $\Omega$ , Starting T\_J = 25°C

3. I\_{SD} \leq 7A, di/dt  $\leq$  200A/µs, V\_{DD}  $\leq$  BV\_{DSS}, Starting T\_J = 25°C

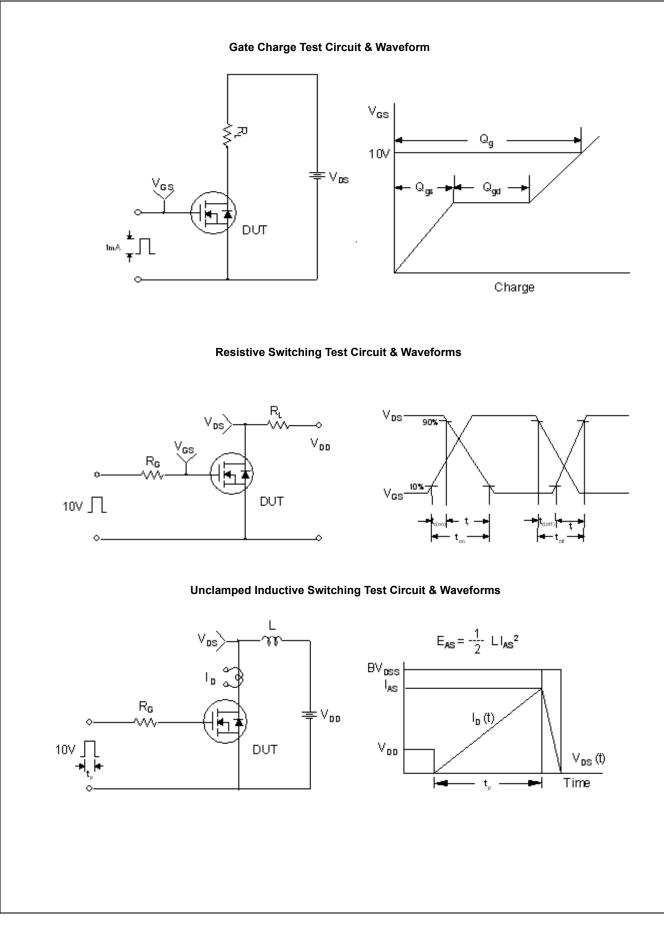
4. Essentially Independent of Operating Temperature Typical Characteristics



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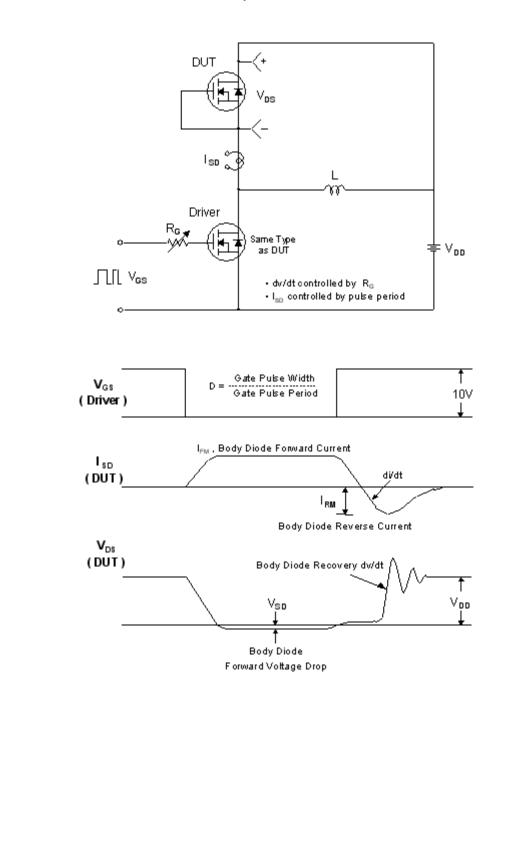


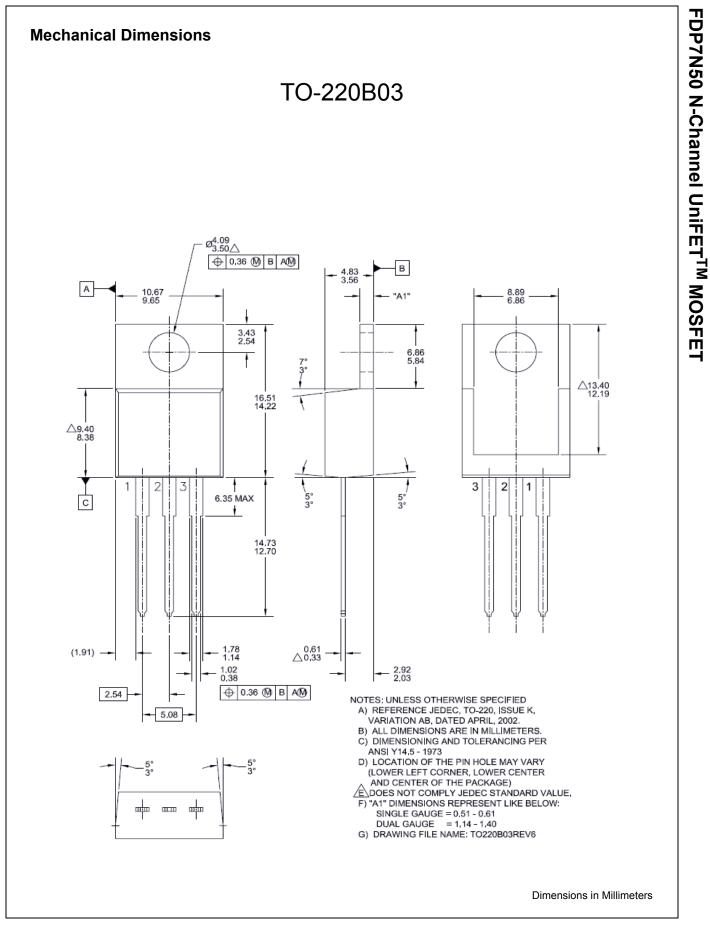
FDP7N50 N-Channel UniFET<sup>TM</sup> MOSFET



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### Peak Diode Recovery dv/dt Test Circuit & Waveforms







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XS™

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