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August 2014

FDP39N20 / FDPF39N20 N-Channel UniFETTM MOSFET 200 V, 39 A, 66 m Ω

Features

- $R_{DS(on)}$ = 66 m Ω (Max.) @ V_{GS} = 10 V, I_D = 19.5 A
- Low Gate Charge (Typ. 38 nC)
- Low C_{rss} (Typ. 57 pF)
- 100% Avalanche Tested

Applications

- PDP TV
- Lighting
- · Uninterruptible Power Supply
- AC-DC Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.



Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		FDP39N20	FDPF39N20 / FDPF39N20TLDTU	Unit
V_{DSS}	Drain-Source Voltage		2	V		
I _D	Drain Current	- Continuous (T _C = 25°C) - Continuous (T _C = 100°C)		39 23.4	39 * 23.4 *	A A
I _{DM}	Drain Current	- Pulsed	(Note 1)	156	156 *	Α
V _{GSS}	Gate-Source voltage			1	V	
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	3	mJ	
I _{AR}	Avalanche Current		(Note 1)		Α	
E _{AR}	Repetitive Avalanche Energy		(Note 1)	25.1		mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.5		V/ns
P _D	Power Dissipation	(T _C = 25°C) - Derate Above 25°C		251 2.0	37 0.29	W W/°C
T _{J,} T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C	
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300		

^{*} Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FDP39N20	FDPF39N20 / FDPF39N20TLDTU	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.5	3.4	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	62.5	C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDP39N20	FDP39N20	TO-220	Tube	N/A	N/A	50 units
FDPF39N20	FDPF39N20	TO-220F	Tube	N/A	N/A	50 units
FDPF39N20TLDTU	FDPF39N20T	TO-220F (L-formed)	Tube	N/A	N/A	50 units

$\textbf{Electrical Characteristics} \quad \textbf{T}_{C} = 25^{\circ} \text{C unless otherwise noted}.$

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics			•		
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA				V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.2		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200 V, V _{GS} = 0 V V _{DS} = 160 V, T _C = 125°C			1 10	μ Α μ Α
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Charac	cteristics			•	•	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 19.5 A		0.056	0.066	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 19.5 A		28.5		S
Dynamic (Characteristics			•	•	
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz		1640	2130	pF
C _{oss}	Output Capacitance			400	520	pF
C _{rss}	Reverse Transfer Capacitance		-	57	85	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 100 V, I _D = 39 A,		30	70	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{G} = 25 \Omega$		160	330	ns
t _{d(off)}	Turn-Off Delay Time			150	310	ns
t _f	Turn-Off Fall Time	(Note 4)		150	310	ns
Qg	Total Gate Charge	V _{DS} = 160 V, I _D = 39 A,		38	49	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V	-	11		nC
Q _{gd}	Gate-Drain Charge	(Note 4)	-	16.5		nC
Drain-Sou	rce Diode Characteristics and Maximur	n Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current				39	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				156	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 39 A	-		1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 39 A,		152		ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt =100 A/μs		1.1	\I	μС

Notes

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. L = 0.85 mH, I $_{AS}$ = 39 A, V $_{DD}$ = 50 V, R $_{G}$ = 25 Ω , starting T $_{J}$ = 25°C.
- 3. I_{SD} \leq 39 A, di/dt \leq 200 A/ μ s, V_{DD} \leq BV_{DSS}, starting T_J = 25°C.
- ${\bf 4.} \ {\bf Essentially \ independent \ of \ operating \ temperature \ typical \ characteristics.}$

Typical Performance Characteristics

Figure 1. On-Region Characteristics

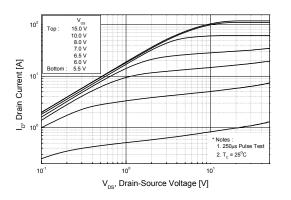


Figure 2. Transfer Characteristics

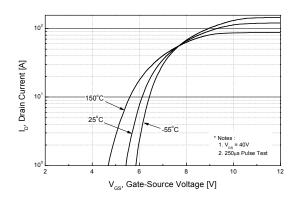
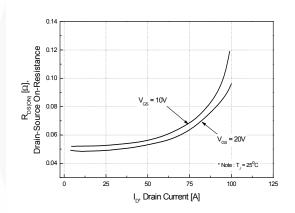


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue



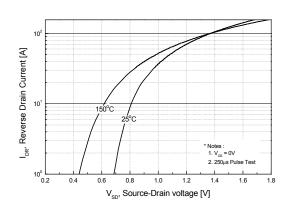


Figure 5. Capacitance Characteristics

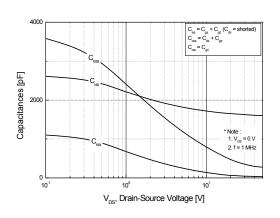
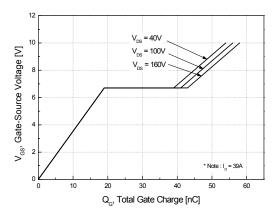


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

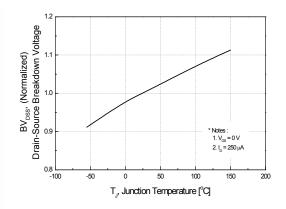


Figure 8. On-Resistance Variation vs. Temperature

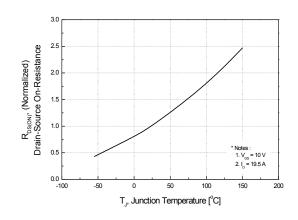


Figure 9-1. Maximum Safe Operating Area - FDP39N20

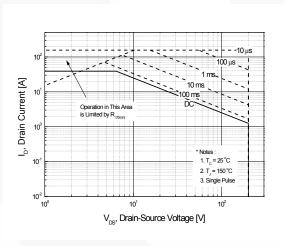


Figure 9-2. Maximum Safe Operating Area - FDPF39N20

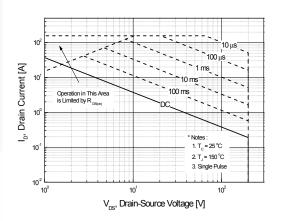
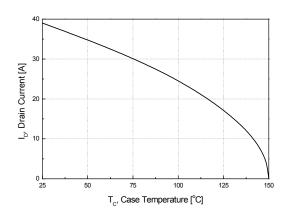


Figure 10. Maximum Drain Current vs. Case Temperature



Typical Performance Characteristics (Continued)

Figure 11-1. Transient Thermal Response Curve - FDP39N20

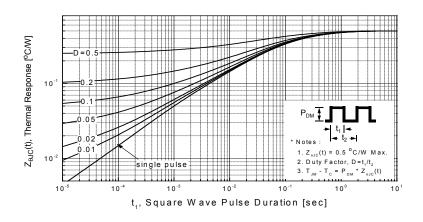
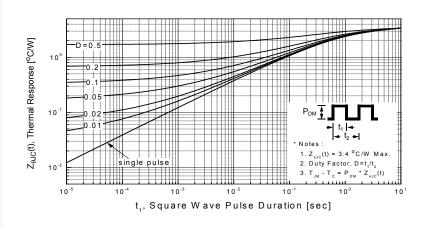


Figure 11-2. Transient Thermal Response Curve - FDPF39N20



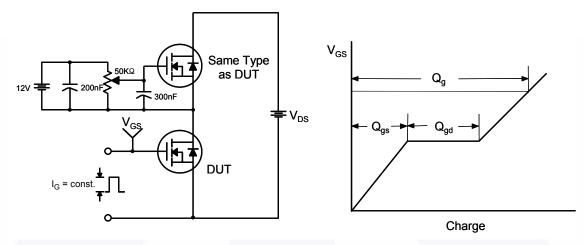


Figure 12. Gate Charge Test Circuit & Waveform

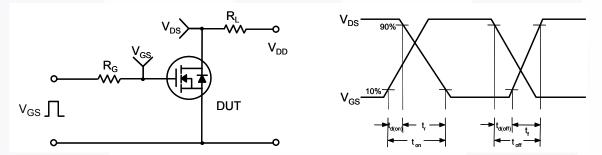


Figure 13. Resistive Switching Test Circuit & Waveforms

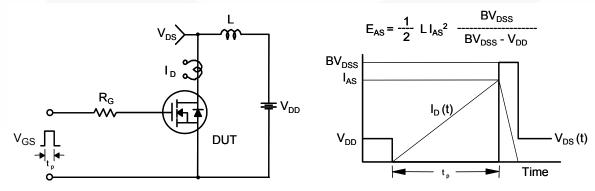


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

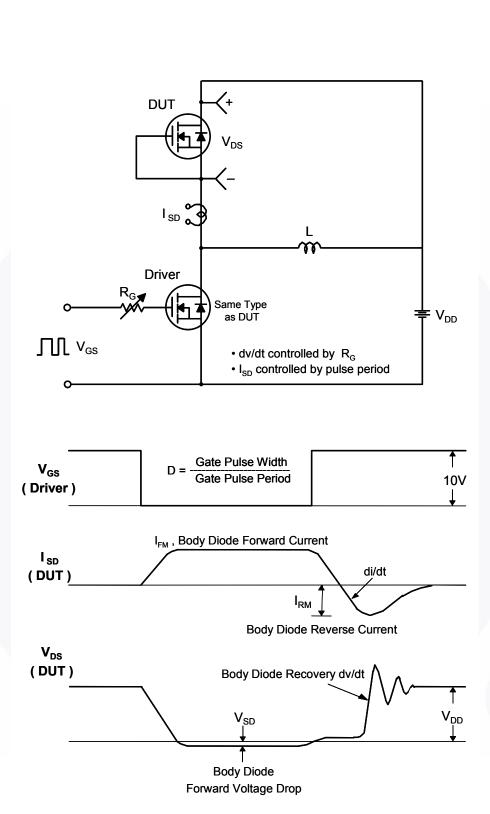
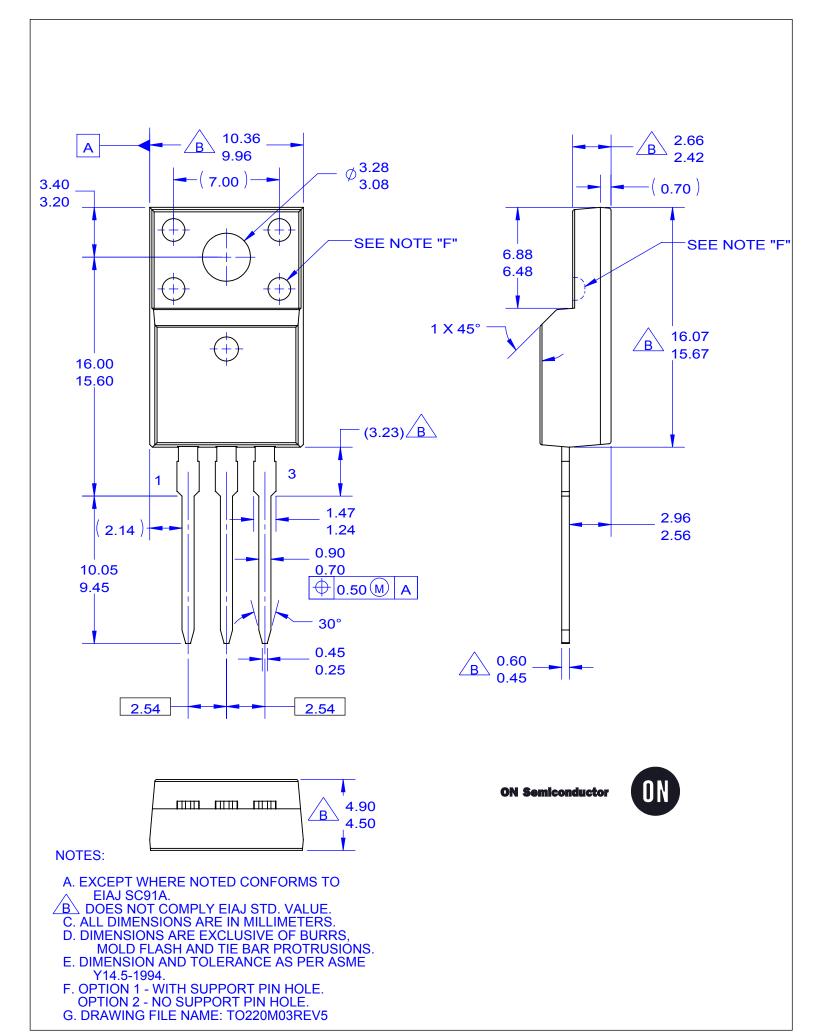


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms





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