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November 2013

FDPF6N60ZUT N-Channel UniFETTM II Ultra FRFETTM MOSFET 600 V, 4.5 A, 2 Ω

Features

- $R_{DS(on)}$ = 1.7 Ω (Typ.) @ V_{GS} = 10 V, I_D = 2.25 A
- Low Gate Charge (Typ. 14.5 nC)
- Low C_{rss} (Typ. 5 pF)
- 100% Avalanche Tested
- Improved dv/dt Capability
- RoHS Compliant

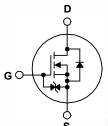
Applications

- LCD/LED TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

Description

UniFETTM II MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on advanced planar stripe and DMOS technology. This advanced MOSFET family has the smallest on-state resistance among the planar MOSFET, and also provides superior switching performance and higher avalanche energy strength. In addition, internal gate-source ESD diode allows UniFET II MOSFET to withstand over 2kV HBM surge stress. UniFET II Ultra FRFET[™] MOSFET has much superior body diode reverse recovery performance. Its t_{rr} is less than 50nsec and the reverse dv/dt immunity is 20V/nsec while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore UniFET II Ultra FRFET MOSFET can remove additional component and improve system reliability in certain applications that require performance improvement of the MOSFET's body diode. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		FDPF6N60ZUT	Unit			
V _{DSS}	Drain to Source Voltage			600	V	
V _{GSS}	Gate to Source Voltage			±30	V	
ID	Desire Current	- Continuous ($T_C = 25^{\circ}C$)		4.5*	Α	
	Drain Current	- Continuous (T _C = 100 ^o C)		2.7*		
I _{DM}	Drain Current	- Pulsed	- Pulsed (Note 1)		А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		150	mJ		
I _{AR}	Avalanche Current (Note		(Note 1)	4.5	А	
E _{AR}	Repetitive Avalanche Energy (Note		(Note 1)	10.5	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		(Note 3)	20	V/ns	
P _D	Devuer Dissignation	$(T_{\rm C} = 25^{\rm o}{\rm C})$		33.8	W	
	Power Dissipation	- Derate Above 25°C		0.27	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C	

Drain current limited by maximum junction temperature

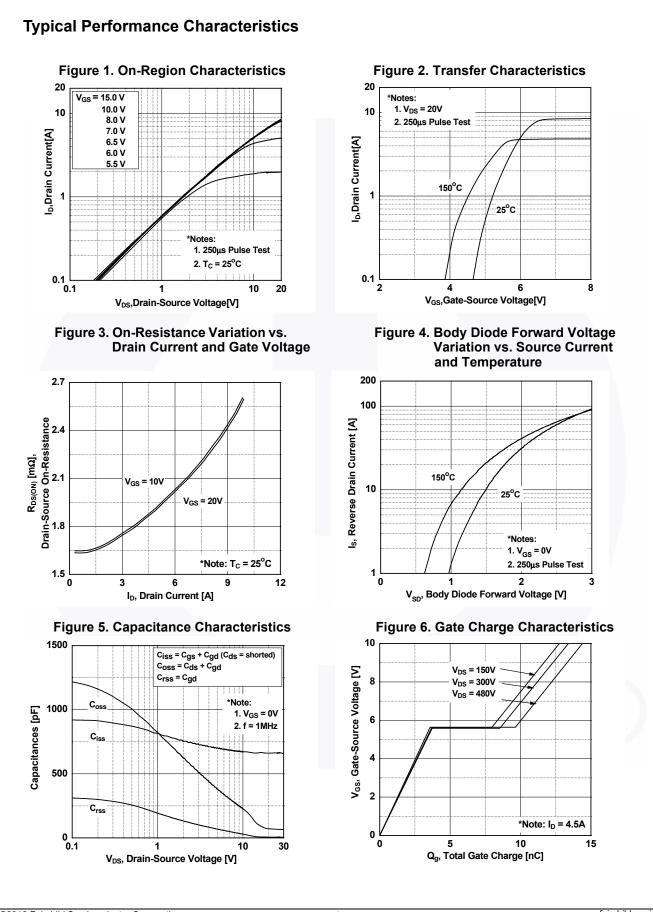
Thermal Characteristics

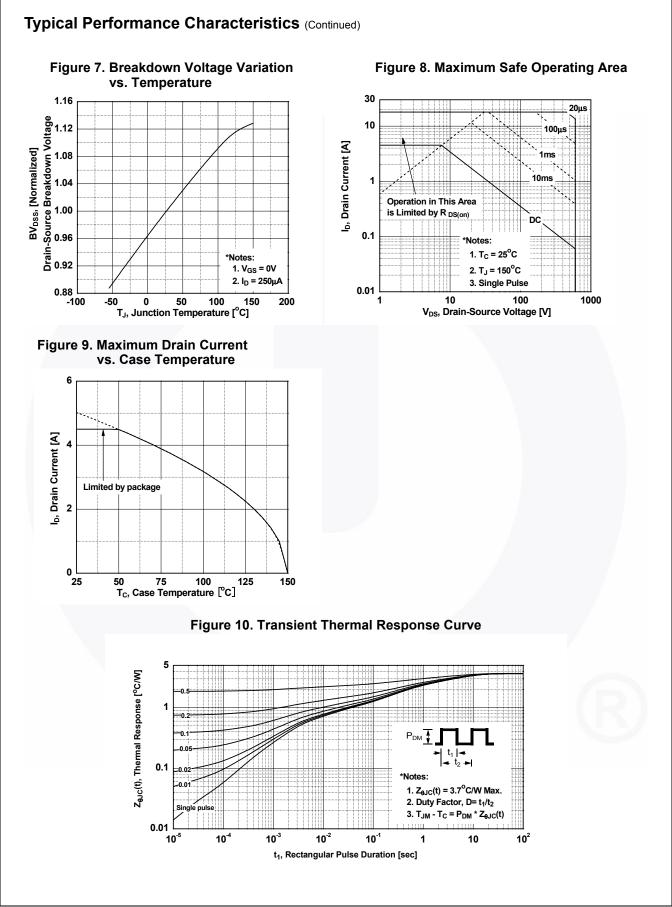
Symbol	Parameter	FDPF6N60ZUT	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.7	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	°C/W	

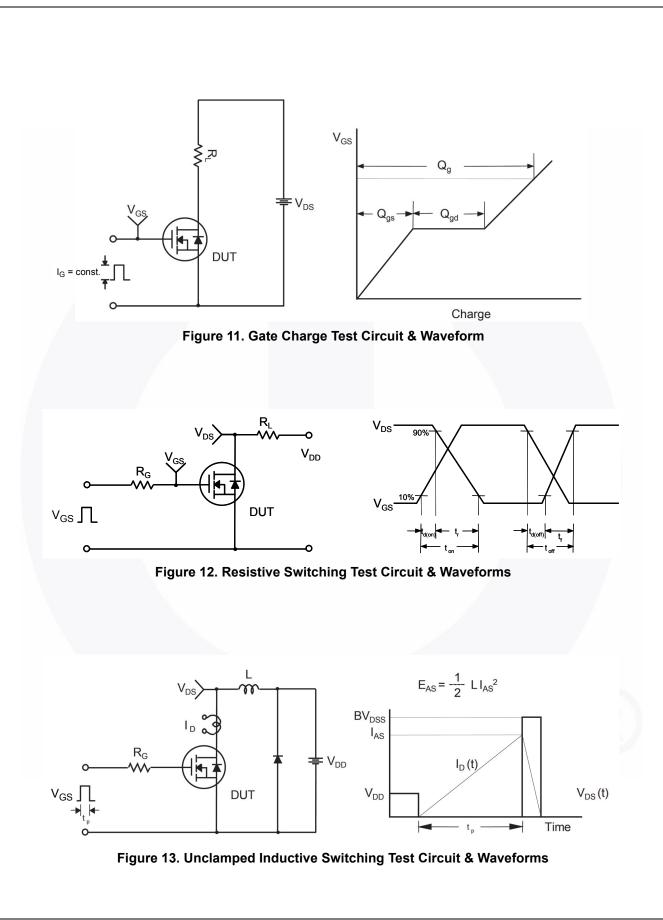
Part Nu	Part Number Top Mark		Package	Package Packing Method Reel Size		Ta	ape Width	Qu	antity
FDPF6N6	•		TO-220F			N/A		50 units	
Electrica	l Chara	icteristics T _C = 25°C	unless othe	rwise noted.					
Symbol		Parameter		Test Condition	S	Min.	Тур.	Max.	Unit
Off Charac	teristics				·				
BV _{DSS}		Source Breakdown Voltage	lo =	I _D = 250 μA, V _{GS} = 0 V, T _J = 25 ^o C		600	-	-	V
ΔBV _{DSS}		wn Voltage Temperature				000			
$/\Delta T_J$			$I_D = 250 \ \mu$ A, Referenced to 25° C		-	0.75	-	V/ºC	
	Zoro Cot	Zero Gate Voltage Drain Current		$V_{DS} = 600 V, V_{GS} = 0 V$ $V_{DS} = 480 V, T_{C} = 125^{\circ}C$		-	-	25	μΑ
DSS	Zero Gal					-		250	
I _{GSS}	Gate to E	Body Leakage Current	V _G	$_{\rm S}$ = ±30 V, V _{DS} = 0 V		-	-	±10	μA
On Charac	toristics								
V _{GS(th)}		eshold Voltage	Va	_S = V _{DS} , I _D = 250 μA		3.0		5.0	V
R _{DS(on)}		ain to Source On Resistance	-	$s = 10 \text{ V}, I_D = 2.25 \text{ A}$		-	1.7	2.0	Ω
9FS		Transconductance		$_{\rm S} = 40$ V, $I_{\rm D} = 2.25$ A		-	3.5	-	S
			,	3					-
Dynamic C	-								T
C _{iss}	Input Ca					-	650	865	pF
C _{oss}	-	apacitance		f = 1 MHz		-	75	100	pF
C _{rss}	Reverse	Transfer Capacitance				-	5	10	pF
Qg		e Charge at 10V	VD	_S = 480 V, I _D = 4.5 A,		-	14.5	20	nC
Q _{gs}	Gate to S	Source Gate Charge		V _{GS} = 10 V		-	4	-	nC
Q _{gd}	Gate to D	Drain "Miller" Charge			(Note 4)	-	6	-	nC
Switching	Charact	eristics							
t _{d(on)}		Delay Time				-	19	48	ns
t _r		Rise Time	VDI	_D = 300 V, I _D = 4.5 A,	_	-	25	60	ns
t _{d(off)}		Delay Time		$R_{G} = 25 \Omega, V_{GS} = 10 V$ (Note 4)		-	25	60	ns
t _f		Fall Time					45	100	ns
					(11010 4)		10	100	
	1	e Characteristics					I		
I _S	Maximum Continuous Drain to Source Did					-	-	4.5	A
SM		Pulsed Drain to Source Did				-	-	18	A
V _{SD}		Source Diode Forward Volta		V _{GS} = 0 V, I _{SD} = 4.5 A		-	-	1.6	V
t _{rr}		Recovery Time		$V_{GS} = 0 V, I_{SD} = 4.5 A,$		-	36	· -	ns
Q _{rr}	Reverse	Recovery Charge	ai _F /	/dt = 100 A/μs		-	37		nC

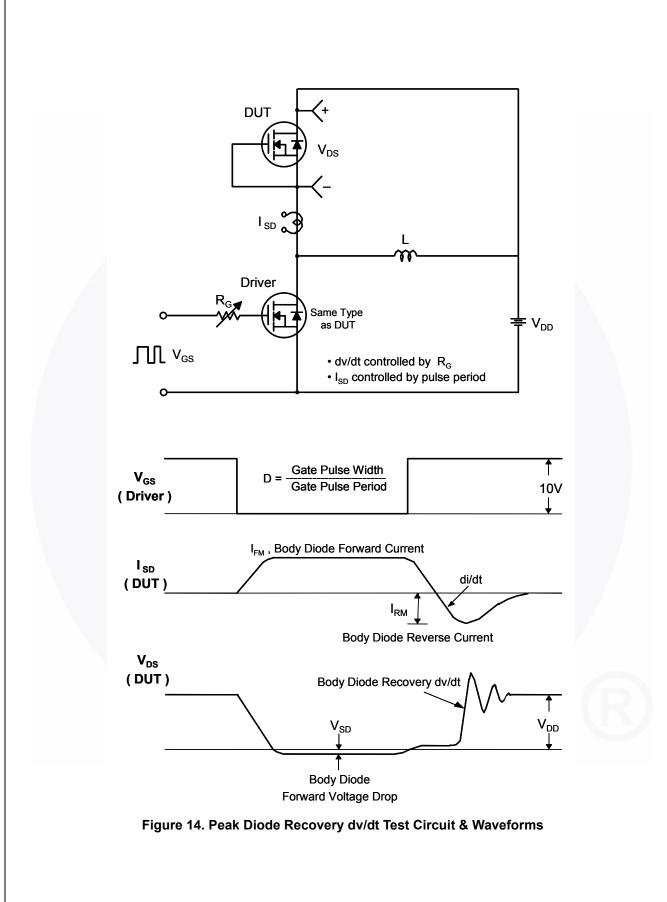
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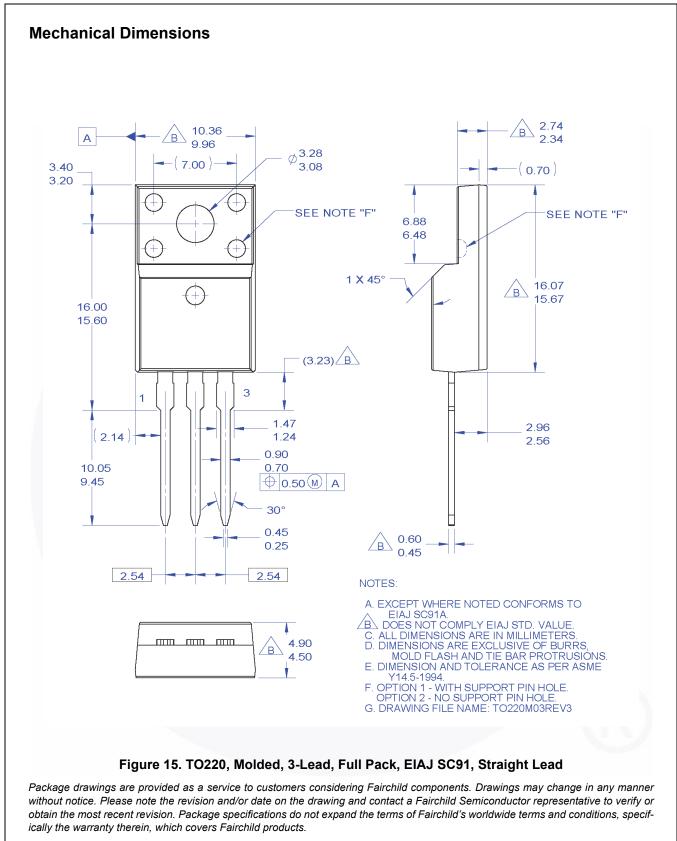
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DPF6N60ZUT --

N-Channel UniFETTM II Ultra FRFETTM MOSFET

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