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### FDS6682

# 30V N-Channel PowerTrench<sup>o</sup> MOSFET

### **General Description**

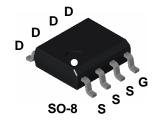
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for "low side" synchronous rectifier operation, providing an extremely low  $R_{\text{DS(ON)}}$  in a small package.

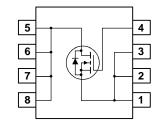
### **Applications**

• DC/DC converter

#### **Features**

- 14 A, 30 V.  $R_{DS(ON)} = 7.5 \text{ m}\Omega$  @  $V_{GS} = 10 \text{ V}$   $R_{DS(ON)} = 9.0 \text{ m}\Omega$  @  $V_{GS} = 4.5 \text{ V}$
- Low gate charge (22 nC typical)
- High performance trench technology for extremely low  $R_{\mbox{\scriptsize DS}(\mbox{\scriptsize ON})}$
- High power and current handling capability





### Absolute Maximum Ratings T<sub>A=25°C</sub> unless otherwise noted

Symbol	Parameter		Ratings	Units
$V_{DSS}$	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	14	А
	- Pulsed		50	
P <sub>D</sub>	Power Dissipation for Single Operation	(Note 1a)	2.5	W
		(Note 1b)	1.2	
		(Note 1c)	1.0	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C

### **Thermal Characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W

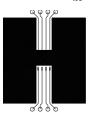
### **Package Marking and Ordering Information**

Device Marking	Device	Reel Size	Tape width	Quantity
FDS6682	FDS6682	13"	12mm	2500 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	racteristics		ı	I		I
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \qquad I_{D} = 250 \mu\text{A}$	30			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		23		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			10	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -20 \text{ V},  V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1	1.7	3	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C		-5.6		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$		5.7 6.6 8	7.5 9 11.5	mΩ
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 5 \text{ V}$	50			Α
<b>g</b> FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 14 \text{ A}$		70		S
Dvnami	c Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 15 \text{ V},  V_{GS} = 0 \text{ V},$		2310		pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		582		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1		237		pF
Switchir	ng Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 15 \text{ V}, \qquad I_{D} = 1 \text{ A},$		10	20	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		7	14	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	1		44	70	ns
t <sub>f</sub>	Turn-Off Fall Time	1		16	29	ns
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = 15 \text{ V}, \qquad I_{D} = 14 \text{ A},$		22	31	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = 5 \text{ V}$		6.4		nC
$Q_{gd}$	Gate-Drain Charge	<u> </u>		8		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				2.1	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = 2.1 \text{ A}  \text{(Note 2)}$		0.7	1.2	V

#### Notes:

<sup>1.</sup> R<sub>8JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>8JC</sub> is guaranteed by design while R<sub>8CA</sub> is determined by the user's board design.



a) 50°C/W when mounted on a 1in<sup>2</sup> pad of 2 oz copper



b) 105°C/W when mounted on a .04 in² pad of 2 oz copper



c) 125°C/W when mounted on a minimum pad.

Scale 1:1 on letter size paper

2. Pulse Test: Pulse Width < 300µs, Duty Cycle < 2.0%

### **Typical Characteristics**

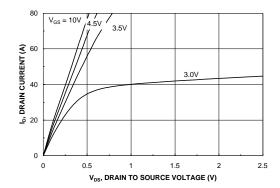


Figure 1. On-Region Characteristics.

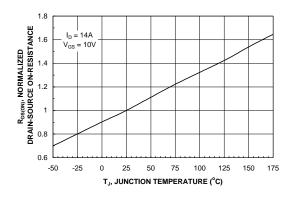


Figure 3. On-Resistance Variation with Temperature.

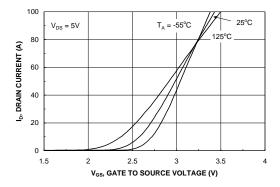


Figure 5. Transfer Characteristics.

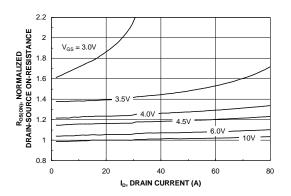


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

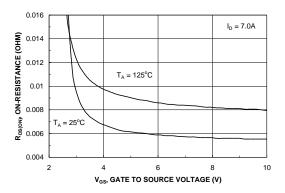


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

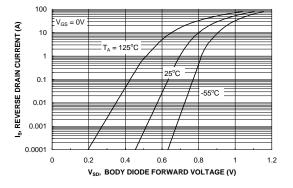
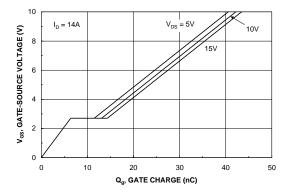


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

### **Typical Characteristics**



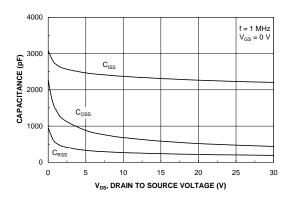
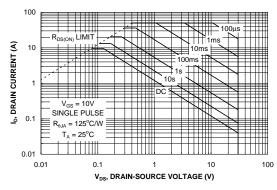


Figure 7. Gate Charge Characteristics.





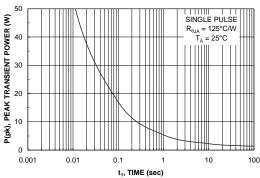


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

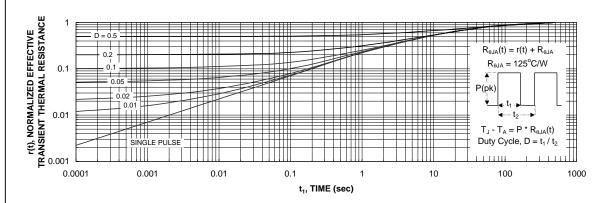


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.

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