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December 2011

FDS6911 Dual N-Channel Logic Level PowerTrench[®] MOSFET 20V, 7.5A, 13m Ω

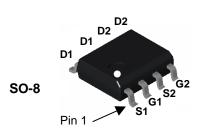
General Description

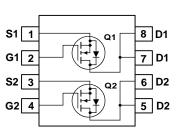
These N-Channel Logic Level MOSFETs are produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.

Features

- $r_{DS(on)} = 13 \text{ m}\Omega @ V_{GS} = 10 \text{ V}$ $r_{DS(on)} = 17 \text{ m}\Omega @ V_{GS} = 4.5 \text{ V}$
- Fast switching speed
- Low gate charge
- High performance trench technology for extremely low R_{DS(ON)}
- High power and current handling capability





Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DSS}	Drain-Sourc	e Voltage		20	V	
V _{GSS}	Gate-Source	e Voltage		± 20	V	
ID	Drain Currer	nt – Continuous	(Note 1a)	7.5	A	
		– Pulsed		20		
P _D	Power Dissi	pation for Single Operation	(Note 1a)	1.6	W	
			(Note 1b)	1.0		
			(Note 1c)	0.9		
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150		
Therma	I Charact	eristics				
R _{0JA}	Thermal Resistance, Junction-to-Ambient		nt (Note 1a)	78	°C/W	
R _{eJC}	Thermal Resistance, Junction-to-Case (Note 1)			40	°C/W	
Packag	e Marking	g and Ordering In	formation			
Device Marking		Device	Reel Size	Tape width	Quantity	
FDS6911		FDS6911	13"	12mm	2500 units	

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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics		•			
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 V$, $I_D = 250 \mu A$	20			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I_D = 250 µA, Referenced to 25°C		28		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current				1 10	μΑ
I _{GSS}	Gate-Source Leakage	$V_{GS}=\pm 20~V,~V_{DS}=0~V$			±100	nA
On Chara	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, \qquad I_D = 250 \ \mu A$	1	1.8	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to 25° C		-4.7		mV/°C
r _{DS(on)}	Static Drain–Source On–Resistance	$ \begin{array}{l} V_{\rm GS} = 10 \ V, I_{\rm D} = 7.5 \ A \\ V_{\rm GS} = 4.5 \ V, I_{\rm D} = 6.5 \ A \\ V_{\rm GS} = 10 \ V, \ I_{\rm D} = 7.5 \ A, T_{\rm J} = 125^{\circ} C \end{array} $		10.6 13 14.5	13 17 20	mΩ
I _{D(on)}	On-State Drain Current	$V_{GS} = 10 \text{ V}, V_{DS} = 5 \text{ V}$	20			А
g fs	Forward Transconductance	$V_{DS} = 5 V$, $I_D = 7.5 A$		36		S
Dvnamic	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 15 V$, $V_{GS} = 0 V$,	1	1130		pF
C _{oss}	Output Capacitance	f = 1.0 MHz		300		pF
C _{rss}	Reverse Transfer Capacitance	7		100		pF
R _G	Gate Resistance	$V_{GS} = 15 \text{ mV}, \text{ f} = 1.0 \text{ MHz}$		2.4		Ω
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn–On Delay Time	$V_{DD} = 15 V$, $I_D = 1 A$,	1	9	18	ns
t _r	Turn–On Rise Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$		5	10	ns
t _{d(off)}	Turn-Off Delay Time	7		26	42	ns
t _f	Turn–Off Fall Time]		7	14	ns
Q _{g(TOT)}	Total Gate Charge at Vgs=10V			17	24	nC
Q _g	Total Gate Charge at Vgs=5V	$V_{DD} = 15 V, I_D = 7.5 A,$		9	13	nC
Q _{gs}	Gate-Source Charge			3.1		nC
Q _{gd}	Gate-Drain Charge			2.7		nC

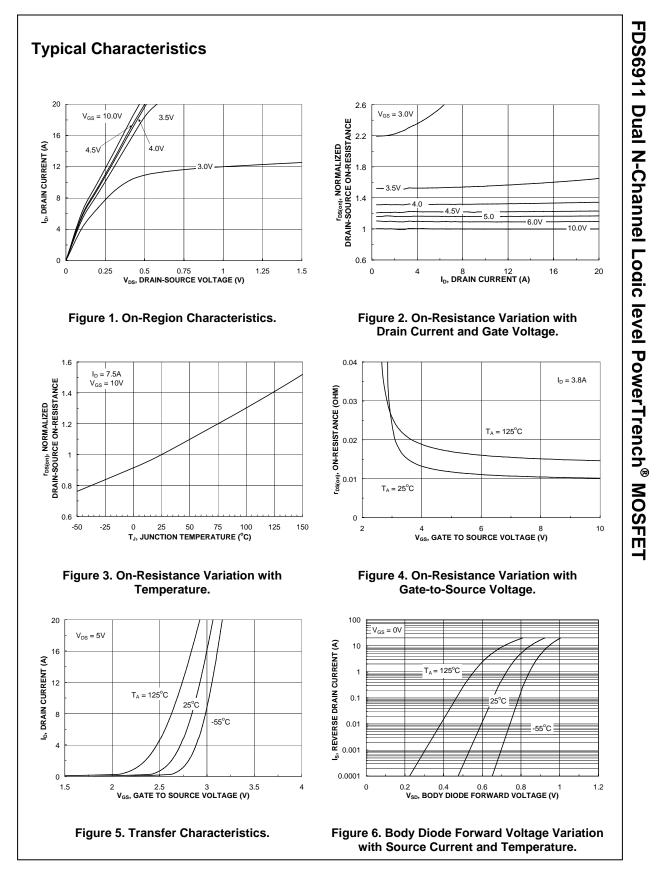
FDS6911 Dual N-Channel Logic level PowerTrench[®] MOSFET

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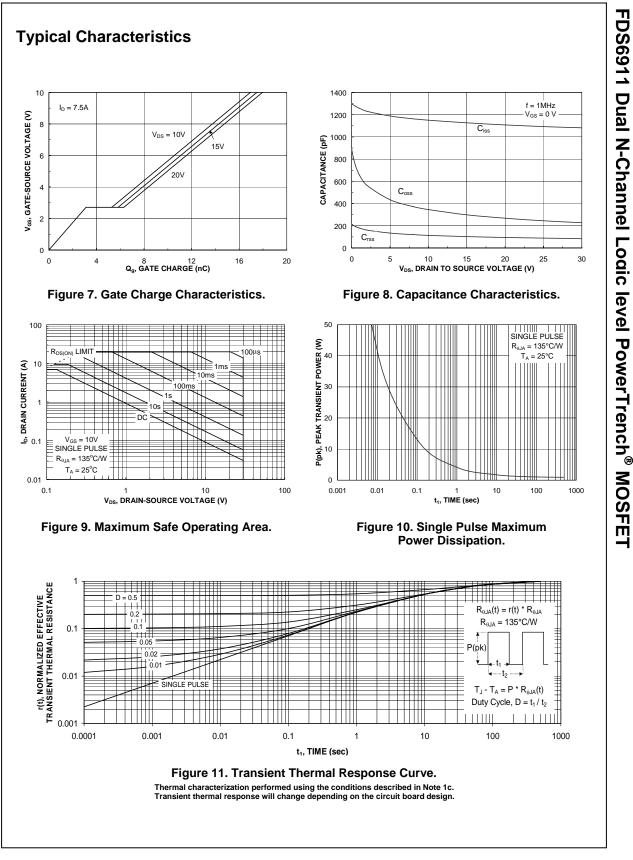
ymbol	Parameter		Test	Conditio	ns	Min	Тур	Max	Units
orain–Sou	Irce Diode Characterist	tics and	I Maximum	Ratings					
	Maximum Continuous Drain-							1.3	А
SD	Drain-Source Diode Forward	d	$V_{GS} = 0 V, I$	_s = 1.3 A	(Note 2)			1.2	V
	Voltage Diode Reverse Recovery Time Diode Reverse Recovery Charge		$I_F = 7.5 \text{ A}, d_{iF}/d_t = 100 \text{ A}/\mu\text{s}$		A/us		24		nS
rr							13		nC
cale 1 : 1 on lette	 R_{0JC} is guaranteed by design while R_{0CA} a) 78°C/W when mounted on a 0.5 in² pad of 2 oz copper er size paper se Width < 300μs, Duty Cycle < 2.0% 		b) 125°C/W whe mounted on a pad of 2 oz co	n .02 in ²	3117 288	C) I	35°C/W w	/hen mour ad.	ted on a

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