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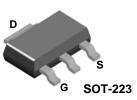
FDT3N40 N-Channel UniFET[™] MOSFET 400 V, 2.0 A, 3.4

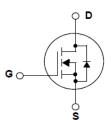
Features

- $R_{DS(on)} = 3.4 \Omega$ (Max.) @ $V_{GS} = 10$ V, $I_D = 1.0$ A
- Low Gate Charge (Typ. 4.5 nC)
- Low Crss (Typ. 3.7 pF)
- 100% Avalanche Tested

Applications

- LCD/LED TV
- Lighting
- Uninterruptible Power Supply





UniFET[™] MOSFET is Fairchild Semiconductor[®]'s high voltage

MOSFET family based on planar stripe and DMOS technology.

This MOSFET is tailored to reduce on-state resistance, and to

provide better switching performance and higher avalanche

energy strength. This device family is suitable for switching power converter applications such as power factor correction

(PFC), flat panel display (FPD) TV power, ATX and electronic

Description

lamp ballasts.

Absolute Maximum Ratings

| Symbol | Parameter | | | FDT3N40 | Unit |
|----------------------------------|---|--|----------|----------------|-----------|
| V _{DSS} | Drain-Source Voltage | | | 400 | V |
| I _D | Drain Current | - Continuous (T _C = 25 - Continuous (T _C = 10 | | 2.0 * 1.2 * | A A |
| I _{DM} | Drain Current | - Pulsed | (Note 1) | 8.0 * | А |
| V _{GSS} | Gate-Source voltage | | | ±30 | V |
| E _{AS} | Single Pulsed Avala | anche Energy | (Note 2) | 46 | mJ |
| I _{AR} | Avalanche Current | | (Note 1) | 2 | А |
| E _{AR} | Repetitive Avalanch | ne Energy | (Note 1) | 0.2 | mJ |
| dv/dt | Peak Diode Recovery dv/dt | | (Note 3) | 4.5 | V/ns |
| P _D | Power Dissipation $(T_C = 25^{\circ}C)$ - Derate above $25^{\circ}C$ | | | 2 0.02 | W W/°C |
| T _{J,} T _{STG} | Operating and Storage Temperature Range | | | -55 to +150 | °C |
| TL | Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds | | urpose, | 300 | °C |

Thermal Characteristics

| Symbol | Parameter | FDT3N40 | Unit | |
|--|---------------------------------------|---------|------|--|
| R_{\thetaJA}^{*} | Thermal Resistance, Case-to-Sink Typ. | 60 | °C/W | |
| * Surface Mounted on JESD51-3 Board, T<0.1sec. | | | | |

April 2013

Package Marking and Ordering Information

| Device Marking | Device | Package | Reel Size | Tape Width | Quantity |
|----------------|-----------|---------|-----------|------------|----------|
| FDT3N40 | FDT3N40TF | SOT-223 | 330mm | 12mm | 4000 |

Electrical Characteristics T_C = 25°C unless otherwise noted

| Symbol | Parameter | Conditions | Min. | Тур. | Max | Unit |
|---|---|--|------|------|---------|----------|
| Off Charac | teristics | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage $V_{GS} = 0V$, $I_D = 250\mu A$ | | 400 | | | V |
| ΔΒV _{DSS} / ΔT _J | Breakdown Voltage Temperature Coefficient | $I_D = 250 \mu A$, Referenced to 25°C | | 0.4 | | V/°C |
| I _{DSS} | Zero Gate Voltage Drain Current | $V_{DS} = 400V, V_{GS} = 0V$ $V_{DS} = 320V, T_{C} = 125^{\circ}C$ | | | 1 10 | μΑ μΑ |
| I _{GSSF} | Gate-Body Leakage Current, Forward | Gate-Body Leakage Current, Forward V _{GS} = 30V, V _{DS} = 0V | | | 100 | nA |
| I _{GSSR} | Gate-Body Leakage Current, Reverse | $V_{GS} = -30V, V_{DS} = 0V$ | | | -100 | nA |
| On Charac | teristics | - | | | | |
| V _{GS(th)} | Gate Threshold Voltage | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 3.0 | | 5.0 | V |
| R _{DS(on)} | Static Drain-Source On-Resistance | V _{GS} = 10V, I _D = 1A | | 2.8 | 3.4 | Ω |
| 9 _{FS} | Forward Transconductance $V_{DS} = 40V, I_D = 1A$ (No | | | 2 | | S |
| Dynamic C | haracteristics | | | | | |
| C _{iss} | Input Capacitance | $V_{DS} = 25V, V_{GS} = 0V,$ | | 173 | 225 | pF |
| C _{oss} | Output Capacitance | f = 1.0MHz | | 30 | 40 | pF |
| C _{rss} | Reverse Transfer Capacitance | | | 3.7 | 6 | pF |
| Switching | Characteristics | | | | | |
| t _{d(on)} | Turn-On Delay Time | V _{DD} = 200V, I _D = 2A | | 10 | 30 | ns |
| t _r | Turn-On Rise Time | $R_{G} = 25\Omega$ | | 30 | 70 | ns |
| t _{d(off)} | Turn-Off Delay Time | | | 10 | 30 | ns |
| t _f | Turn-Off Fall Time | (Note 4, 5) | | 25 | 60 | ns |
| Qg | Total Gate Charge | V _{DS} = 320V, I _D = 2A | | 4.5 | 6 | nC |
| Q _{gs} | Gate-Source Charge | V _{GS} = 10V | | 1.2 | | nC |
| Q _{gd} | Gate-Drain Charge | (Note 4, 5) | | 2 | | nC |
| | ce Diode Characteristics and Maximur | n Ratings | I | I | 1 | |
| I _S | Maximum Continuous Drain-Source Diode Forward Current | | | | 2 | Α |
| I _{SM} | Maximum Pulsed Drain-Source Diode Forward Current | | | | 8 | Α |
| V _{SD} | Drain-Source Diode Forward Voltage | $V_{GS} = 0V, I_S = 2A$ | | | 1.4 | V |
| t _{rr} | Reverse Recovery Time | $V_{GS} = 0V, I_S = 2A$ | | 210 | | ns |
| Q _{rr} | Reverse Recovery Charge | $dI_{F}/dt = 100 \text{Å}/\mu \text{s} $ (Note 4) | | 0.75 | | μC |
| | | | | | 1 | 1 |

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. L = 10mH, I_{AS} = 2A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$

3. I_{SD} \leq 2A, di/dt \leq 200A/µs, V_{DD} \leq BV_{DSS}, Starting T_J = 25°C

4. Pulse Test: Pulse width $\leq 300 \mu s,$ Duty Cycle $\leq 2\%$

5. Essentially Independent of Operating Temperature Typical Characteristics

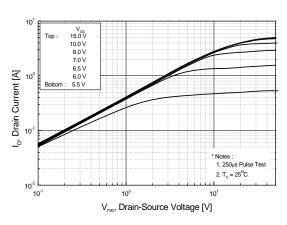
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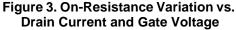
FDT3N40 N-Channel UniFET[™] MOSFET

Typical Performance Characteristics



Figure 2. Transfer Characteristics





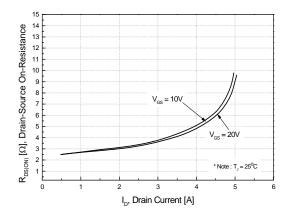
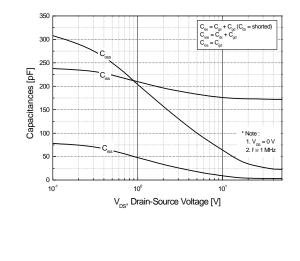
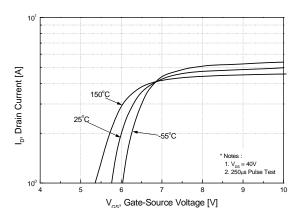


Figure 5. Capacitance Characteristics







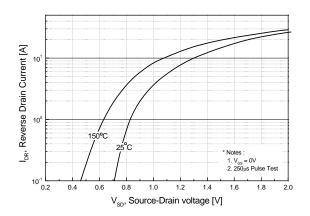
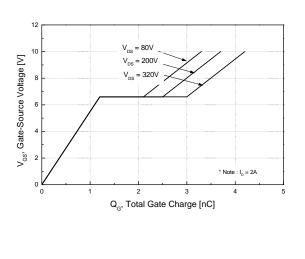
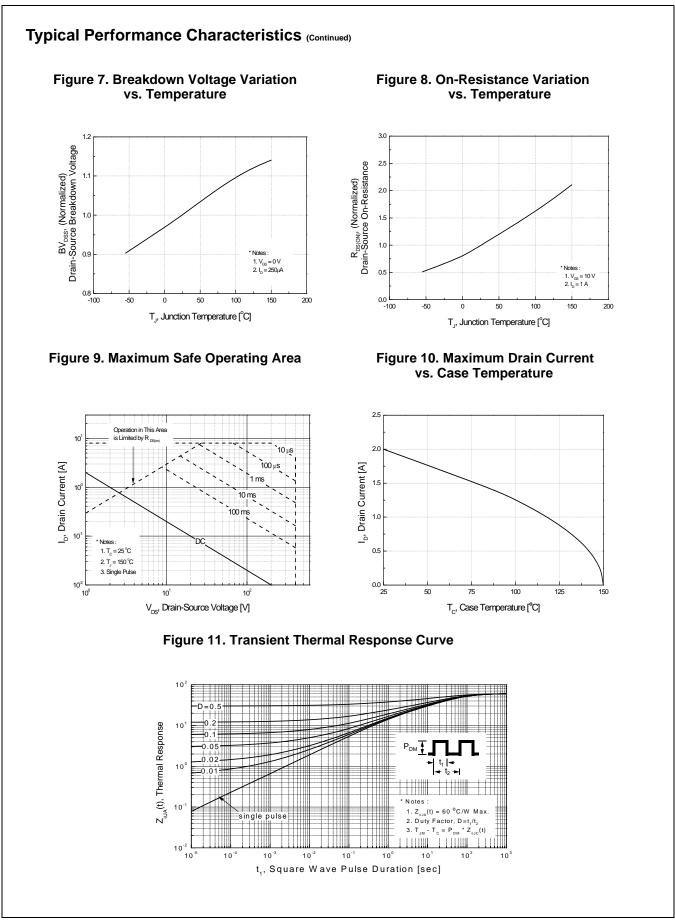


Figure 6. Gate Charge Characteristics

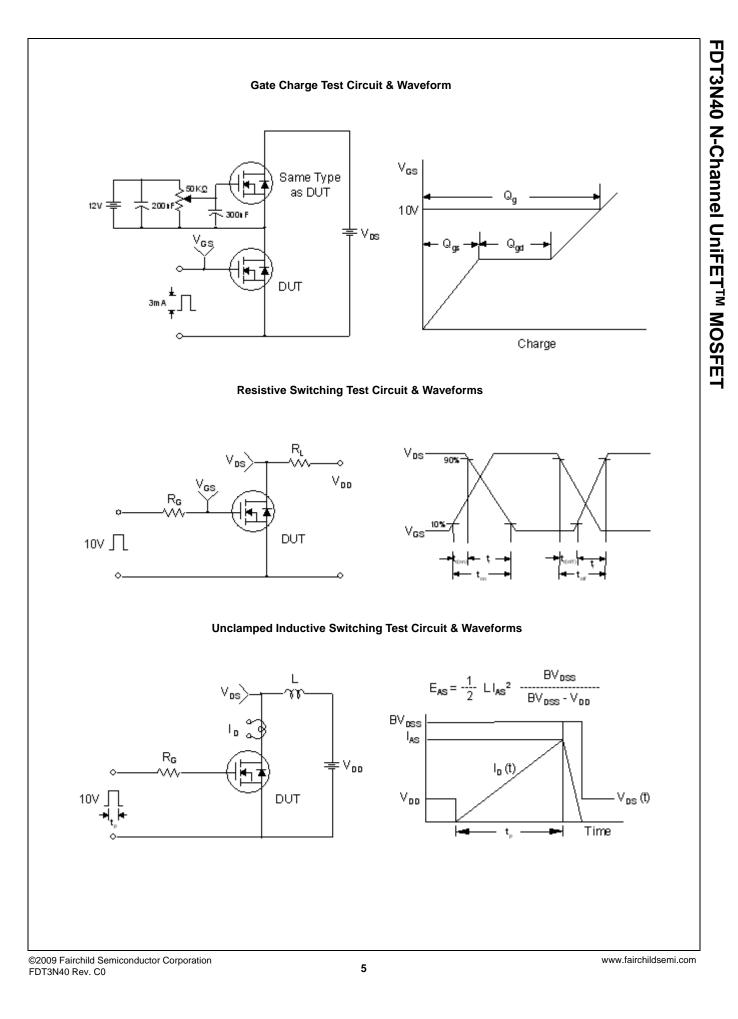


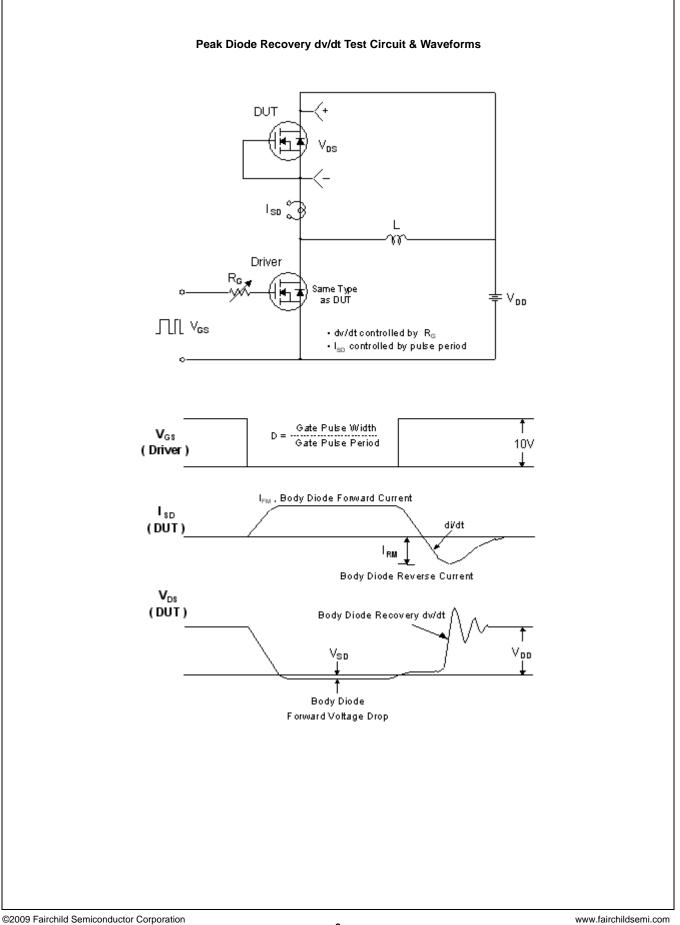
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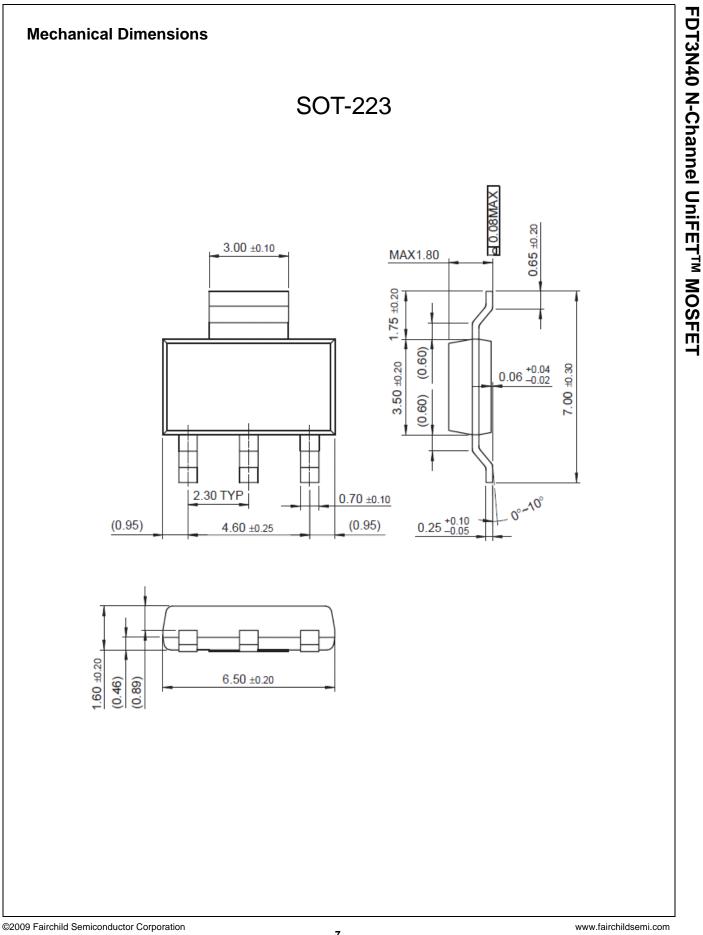
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